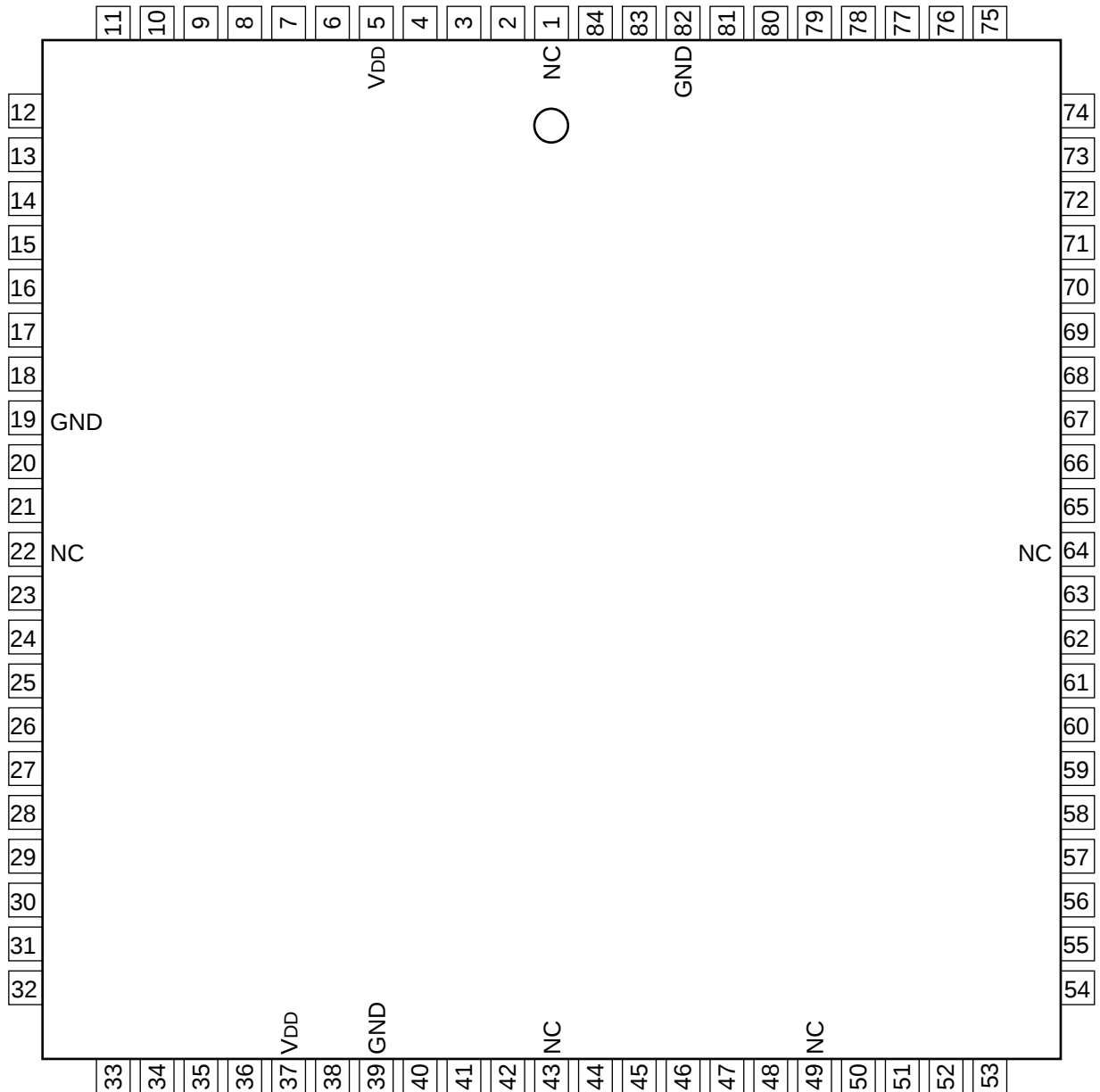


—TOP VIEW—



PIN No.	MODE 0		MODE 1		MODE 2		PROM MODE	
	I/O	SIGNAL	I/O	SIGNAL	I/O	SIGNAL	I/O	SIGNAL
1	—	NC	—	NC	—	NC	—	NC
2	I	MP0	I	MP0	I	MP0	I	MP0
3	I	XTAL	I	XTAL	I	XTAL	I	XTAL
4	I	EXTAL	I	EXTAL	I	EXTAL	I	EXTAL
5	—	V _{DD}	—	V _{DD}	—	V _{DD}	—	V _{DD}
6	I/O	PE7	I	WAIT	I	WAIT	—	NC
7	I/O	PE6	O	BUSACK	O	BUSACK	—	NC
8	I/O	PE5	I	BUSREQ	I	BUSREQ	—	NC
9	I	RESET	I	RESET	I	RESET	—	V _{PP}
10	I	NMI	I	NMI	I	NMI	O	A9
11	I	INT0	I	INT0	I	INT0	—	NC
12	I	INT0	I	INT0	I	INT0	—	NC
13	I	INT2	I	INT2	I	INT2	—	NC
14	I/O	PE4	O	ST	O	ST	—	NC
15	I/O	PC0	O	A0	O	A0	O	A0
16	I/O	PC1	O	A1	O	A1	O	A1
17	I/O	PC2	O	A2	O	A2	O	A2
18	I/O	PC3	O	A3	O	A3	O	A3
19	—	GND	—	GND	—	GND	—	GND
20	I/O	PC4	O	A4	O	A4	O	A4
21	I/O	PC5	O	A5	O	A5	O	A5
22	—	NC	—	NC	—	NC	—	NC
23	I/O	PC6	O	A6	O	A6	O	A6
24	I/O	PC7	O	A7	O	A7	O	A7
25	I/O	PD0	O	A8	I/O	A8/PD0	O	A8
26	I/O	PD1	O	A9	I/O	A9/PD1	—	NC
27	I/O	PD2	O	A10	I/O	A10/PD2	O	A10
28	I/O	PD3	O	A11	I/O	A11/PD3	O	A11
29	I/O	PD4	O	A12	I/O	A12/PD4	O	A12
30	I/O	PD5	O	A13	I/O	A13/PD5	O	A13
31	I/O	PD6	O	A14	I/O	A14/PD6	O	A14
32	I/O	PD7	O	A15	I/O	A15/PD7	I	OE
33	I/O	PE0	O	A16	I/O	A16/PE0	I	CE
34	I/O	PE1	O	A17	I/O	A17/PE1	—	NC
35	I/O	PE2	O	A18	I/O	A18/PE2	—	NC
36	O	TOUT1	O	TOUT1	O	TOUT1	—	NC
37	—	V _{DD}	—	V _{DD}	—	V _{DD}	—	V _{DD}
38	I/O	PE3	O	A19	I/O	A19/PE3	—	NC
39	—	GND	—	GND	—	GND	—	GND
40	I/O	PF0	I/O	D0	I/O	D0	O	O0
41	I/O	PF1	I/O	D1	I/O	D1	O	O1
42	I/O	PF2	I/O	D2	I/O	D2	O	O2

PIN No.	MODE 0		MODE 1		MODE 2		PROM MODE	
	I/O	SIGNAL	I/O	SIGNAL	I/O	SIGNAL	I/O	SIGNAL
43	—	NC	—	NC	—	NC	—	NC
44	I/O	PF3	I/O	D3	I/O	D3	O	O3
45	I/O	PF4	I/O	D4	I/O	D4	O	O4
46	I/O	PF5	I/O	D5	I/O	D5	O	O5
47	I/O	PF6	I/O	D6	I/O	D6	O	O6
48	I/O	PF7	I/O	D7	I/O	D7	O	O7
49	—	GND	—	GND	—	GND	—	GND
50	I	PG0/AN0	I	PG0/AN0	I	PG0/AN0	—	NC
51	I	PG1/AN1	I	PG1/AN1	I	PG1/AN1	—	NC
52	I	PG2/AN2	I	PG2/AN2	I	PG2/AN2	—	NC
53	I	PG3/AN3	I	PG3/AN3	I	PG3/AN3	—	NC
54	I	PG4/AN4	I	PG4/AN4	I	PG4/AN4	—	NC
55	I	PG5/AN5	I	PG5/AN5	I	PG5/AN5	—	NC
56	O	RTS0	O	RTS0	O	RTS0	—	NC
57	I	CTS0	I	CTS0	I	CTS0	—	NC
58	I	DCD0	I	DCD0	I	DCD0	—	NC
59	O	TXA0	O	TXA0	O	TXA0	—	NC
60	I	RXA0	I	RXA0	I	RXA0	—	NC
61	I/O	CKA0/DREQ0	I/O	CKA0/DREQ0	I/O	CKA0/DREQ0	—	NC
62	O	TOUT2	O	TOUT2	O	TOUT2	—	NC
63	O	TOUT3	O	TOUT3	O	TOUT3	—	NC
64	—	NC	—	NC	—	NC	—	NC
65	I	IC	I	IC	I	IC	—	NC
66	I/O	TXA1/PA0	I/O	TXA1/PA0	I/O	TXA1/PA0	—	NC
67	I/O	RXA1/PA1	I/O	RXA1/PA1	I/O	RXA1/PA1	—	NC
68	I/O	CKA1/TEND0/PA2	I/O	CKA1/TEND0/PA2	I/O	CKA1/TEND0/PA2	—	NC
69	I/O	TXS/PA3	I/O	TXS/PA3	I/O	TXS/PA3	—	NC
70	I/O	RXS/CTS1/PA4	I/O	RXS/CTS1/PA4	I/O	RXS/CTS1/PA4	—	NC
71	I/O	CKS/PA5	I/O	CKS/PA5	I/O	CKS/PA5	—	NC
72	I/O	DREQ1/PA6	I/O	DREQ1/PA6	I/O	DREQ1/PA6	—	NC
73	I/O	TEND1/PA7	I/O	TEND1/PA7	I/O	TEND1/PA7	—	NC
74	I/O	PB7	O	HALT	O	HALT	—	NC
75	I/O	PB6	O	REF	O	REF	—	NC
76	I/O	PB5	O	IOE	O	IOE	—	NC
77	I/O	PB4	O	ME	O	ME	—	NC
78	I/O	PB3	O	E	O	E	—	NC
79	I/O	PB2	O	LIR	O	LIR	—	NC
80	I/O	PB1	O	WR	O	WR	—	NC
81	I/O	PB0	O	RD	O	RD	—	NC
82	—	GND	—	GND	—	GND	—	GND
83	O	∅	O	∅	O	∅	—	NC
84	I	MP1	I	MP1	I	MP1	I	MP1

INPUT

AN0 - AN5 : ANALOG INPUT
BUSREQ : BUS REQUEST
CTS0, 1 : CLEAR TO SEND FOR ASYNCHRONOUS SCI CHANNEL n (n = 0 OR 1)
DCD0 : DATA CARRIER DETECT FOR ASYNCHRONOUS SCI CHANNEL n (n = 0)
DREQ0, 1 : DMA REQUEST FOR CHANNEL n (n = 0 OR 1)
 EXTAL : EXTERNAL CLOCK
 IC : INPUT CAPTURE
 INT0 - 2 : INTERRUPT
 MP0, 1 : MOD PROGRAM
 NMI : NON-MASKABLE INTERRUPT
 PG0 - PG5 : 6-BIT INPUT OF PORT G
 RXA0, 1 : RECEIVE DATA FOR ASYNCHRONOUS SCI CHANNEL n (n = 0 OR 1)
 RXS : RECEIVE DATA FOR SERIAL I/O PORT
 XTAL : CLOCK

OUTPUT

A0 - A19 : ADDRESS BUS
BUSACK : BUS ACKNOWLEDGE
E : ENABLE
IOE : I/O ENABLE
LIR : LOAD INSTRUCTION REGISTER
ME : MEMORY ENABLE
RD : READ
REF : REFRESH
 RTS0 : REQUEST TO SEND FOR ASYNCHRONOUS SCI CHANNEL n (n = 0)
 ST : STATUS
TEND0, 1 : TRANSFER END FOR CHANNEL n (n = 0 OR 1)
 TOUT1 - 3 : TIMER OUT
 TXA0, 1 : TRANSFER DATA FOR ASYNCHRONOUS SCI CHANNEL n (n = 0 OR 1)
 TXS : TRANSFER DATA FOR SERIAL I/O PORT
WR : WRITE
 $\emptyset$: SYSTEM CLOCK

INPUT/OUTPUT

CKA0, 1 : CLOCK FOR ASYNCHRONOUS SCI CHANNEL n (n = 0 OR 1)
 CKS : CLOCK FOR SERIAL I/O PORT
 D0 - D7 : DATA BUS
 PA0 - PA7 : 8-BIT INPUT/OUTPUT OF PORT A
 PB0 - PB7 : 8-BIT INPUT/OUTPUT OF PORT B
 PC0 - PC7 : 8-BIT INPUT/OUTPUT OF PORT C
 PD0 - PD7 : 8-BIT INPUT/OUTPUT OF PORT D
 PE0 - PE7 : 8-BIT INPUT/OUTPUT OF PORT E
 PF0 - PF7 : 8-BIT INPUT/OUTPUT OF PORT F

