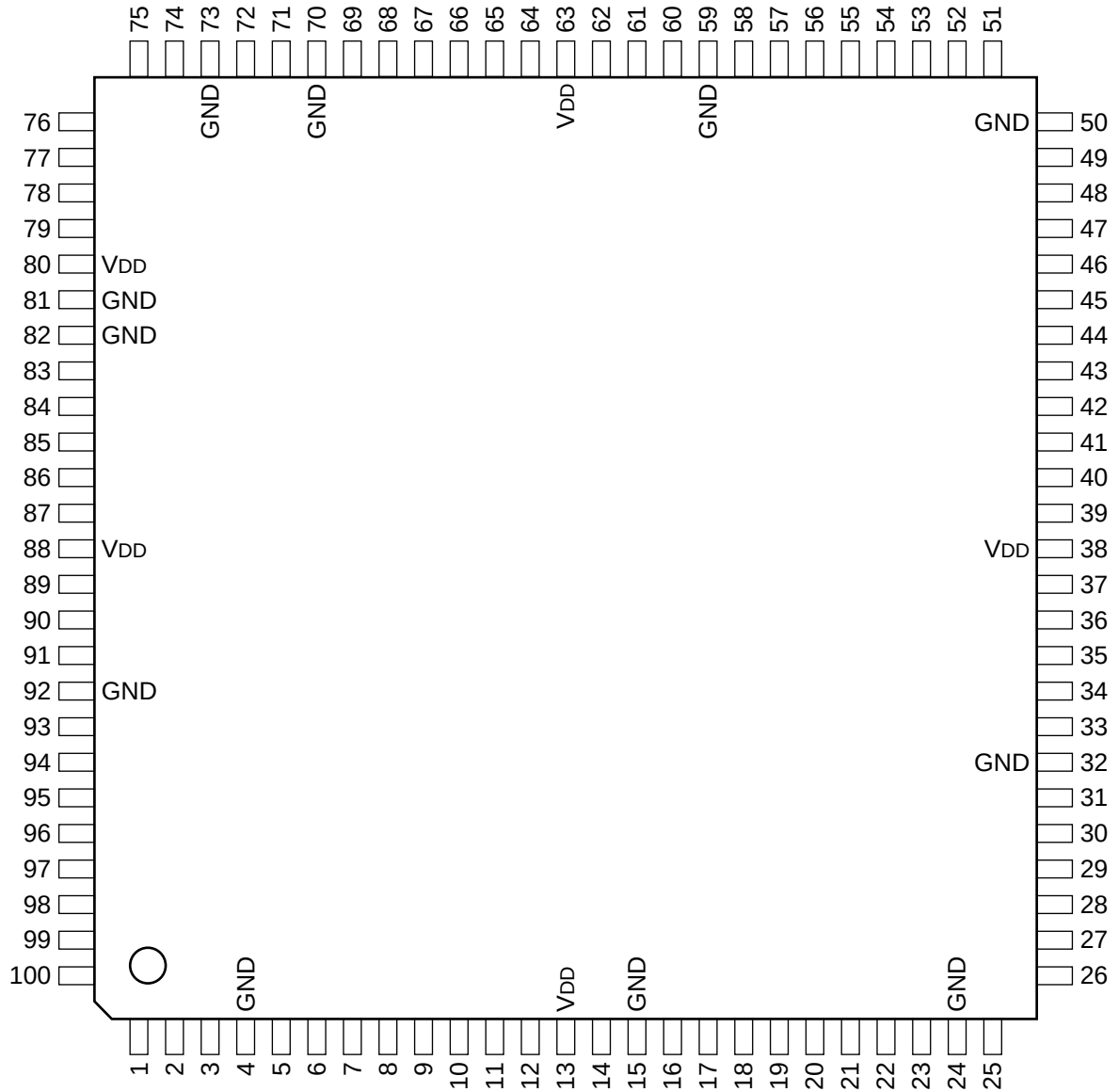


C-MOS 32-BIT MICROPROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	AD0	35	O	A13	69	O	CK
2	I/O	AD1	36	O	A14	70	—	GND
3	I/O	AD2	37	O	A15	71	I	EXTAL
4	—	GND	38	—	V _{DD}	72	I	XTAL
5	I/O	AD3	39	O	A16	73	—	GND
6	I/O	AD4	40	O	A17	74	I	NMI
7	I/O	AD5	41	—	GND	75	O	W _{DT} OV _F
8	I/O	AD6	42	O	A18	76	I	RES
9	I/O	AD7	43	O	A19	77	I	MD0
10	I/O	AD8	44	O	A20	78	I	MD1
11	I/O	AD9	45	O	A21	79	I	MD2
12	I/O	AD10	46	O	CS0	80	—	V _{DD}
13	—	V _{DD}	47	O	CS1/CASH	81	—	GND
14	I/O	AD11	48	O	CS2	82	—	GND
15	—	GND	49	O	CS3/CASL	83	I/O	PB0/TP0/TIOCA2
16	I/O	AD12	50	—	GND	84	I/O	PB1/TP1/TIOCB2
17	I/O	AD13	51	I/O	PA0/CS4/TIOCA0	85	I/O	PB2/TP2/TIOCA3
18	I/O	AD14	52	I/O	PA1/CS5/RAS	86	I/O	PB3/TP3/TIOCB3
19	I/O	AD15	53	I/O	PA2/CS6/TIOCB0	87	I/O	PB4/TP4/TIOCA4
20	O	A0 (HBS)	54	I/O	PA3/CS7/WAIT	88	—	V _{DD}
21	O	A1	55	I/O	PA4/WRL/(WR)	89	I/O	PB5/TP5/TIOCB4
22	O	A2	56	I/O	PA5/WRH/(LBS)	90	I/O	PB6/TP6/TOCXA4/TCLKC
23	O	A3	57	I/O	PA6/RD	91	I/O	PB7/TP7/TOCXB4/TCLKD
24	—	GND	58	I/O	PA7/BACK	92	—	GND
25	O	A4	59	—	GND	93	I/O	PB8/TP8/RXD0
26	O	A5	60	I/O	PA8/BREQ	94	I/O	PB9/TP9/TXD0
27	O	A6	61	I/O	PA9/AH/IRQOUT	95	I/O	PB10/TP10/RXD1
28	O	A7	62	I/O	PA10/DPL/TIOCA1	96	I/O	PB11/TP11/TXD1
29	O	A8	63	—	V _{DD}	97	I/O	PB12/TP12/IRQ4/SCK0
30	O	A9	64	I/O	PA11/DPH/TIOCB1	98	I/O	PB13/TP13/IRQ5/SCK1
31	O	A10	65	I/O	PA12/IRQ0/DACK0/TCLKA	99	I/O	PB14/TP14/IRQ6
32	—	GND	66	I/O	PA13/IRQ1/DREQ0/TCLKB	100	I/O	PB15/TP15/IRQ7
33	O	A11	67	I/O	PA14/IRQ2/DACK1			
34	O	A12	68	I/O	PA15/IRQ3/DREQ1			

1	AD0	A0(HSB)	20
2	AD1	A1	21
3	AD2	A2	22
5	AD3	A3	23
6	AD4	A4	25
7	AD5	A5	26
8	AD6	A6	27
9	AD7	A7	28
10	AD8	A8	29
11	AD9	A9	30
12	AD10	A10	31
14	AD11	A11	33
16	AD12	A12	34
17	AD13	A13	35
18	AD14	A14	36
19	AD15	A15	37
		A16	39
		A17	40
		A18	42
		A19	43
		A20	44
		A21	45
83	PB0/TP0/TIOCA2	PA0/CS4/TIOCA0	51
84	PB1/TP1/TIOCB2	PA1/CS5/RAS	52
85	PB2/TP2/TIOCA3	PA2/CS6/TIOCB0	53
86	PB3/TP3/TIOCB3	PA3/CS7/WAIT	54
87	PB4/TP4/TIOCA4	PA4/WRL(WR)	55
89	PB5/TP5/TIOCB4	PA5/WRH(LBS)	56
90	PB6/TP6/TOCXA4/TCLKC	PA6/RD	57
91	PB7/TP7/TOCXB4/TCLKD	PA7/BACK	58
93	PB8/TP8/RXD0	PA8/BREQ	60
94	PB9/TP9/TXD0	PA9/AH/IRQOUT	61
95	PB10/TP10/RXD1	PA10/DPL/TIOCA1	62
96	PB11/TP11/TXD1	PA11/DPH/TIOCB1	64
97	PB12/TP12/IRQ4/SCK0	PA12/IRQ0/DACK0/TCLKA	65
98	PB13/TP13/IRQ5/SCK1	PA13/IRQ1/DREQ0/TCLKB	66
99	PB14/TP14/IRQ6	PA14/IRQ2/DACK1	67
100	PB15/TP15/IRQ7	PA15/IRQ3/DREQ1	68
72	XTAL	CS0	46
71	EXTAL	CS1/CASH	47
74	NMI	CS2	48
77	MD0	CS3/CASL	49
78	MD1	CK	69
79	MD2	WDTOVF	75
76	RES		

INPUT

BREQ	; BUS REQUEST
DREQ0, DREQ1	; DMA REQUEST
EXTAL	; CRYSTAL OSCILLATOR AND EXTERNAL CLOCK
IRQ0 - IRQ7	; INTERRUPT REQUEST
MD0 - MD2	; MODE SELECT
NMI	; NON-MASKABLE INTERRUPT
RES	; RESET
RXD0, RXD1	; RECEIVE DATA
TCLKA - TCLKD	; TIMER CLOCK
WAIT	; WAIT
XTAL	; EXTERNAL CRYSTAL OSCILLATOR

OUTPUT

AH	; ADDRESS HOLD
A0 - A21	; ADDRESS BUS
BACK	; BUS REQUEST ACKNOWLEDGE
CASH	; HIGH COLUMN ADDRESS STROBE
CASL	; LOW COLUMN ADDRESS STROBE
CK	; SYSTEM CLOCK
CS0 - CS7	; CHIP SELECT
DACK0, DACK1	; DMA REQUEST ACKNOWLEDGE
HBS	; HIGH BYTE STROBE
LBS	; LOW BYTE STROBE
RAS	; ROW ADDRESS STROBE
RD	; READ
RQOUT	; REQUEST OUTPUT
TOCXA4	; OUTPUT COMPARE XA4
TOCXB4	; OUTPUT COMPARE XB4
TP0 - TP15	; TIMING PATTERN CONTROLLER
TXD0, TXD1	; TRANSMIT DATA
WDTOVF	; WATCH-DOG TIMER OVERFLOW
WR	; WRITE
WRH	; HIGH WRITE
WRL	; LOW WRITE

INPUT/OUTPUT

AD0 - AD15	; DATA/ADDRESS BUS
DPH	; HIGH DATA PARITY
DPL	; LOW DATA PARITY
PA0 - PA15	; I/O PORT A
PB0 - PB15	; I/O PORT B
SCK0, SCK1	; SERIAL CLOCK
TIOCA0 - TIOCA4	; ITU INPUT CAPTURE/OUTPUT COMPARE
TIOB0 - TIOCB4	; ITU INPUT CAPTURE/OUTPUT COMPARE

