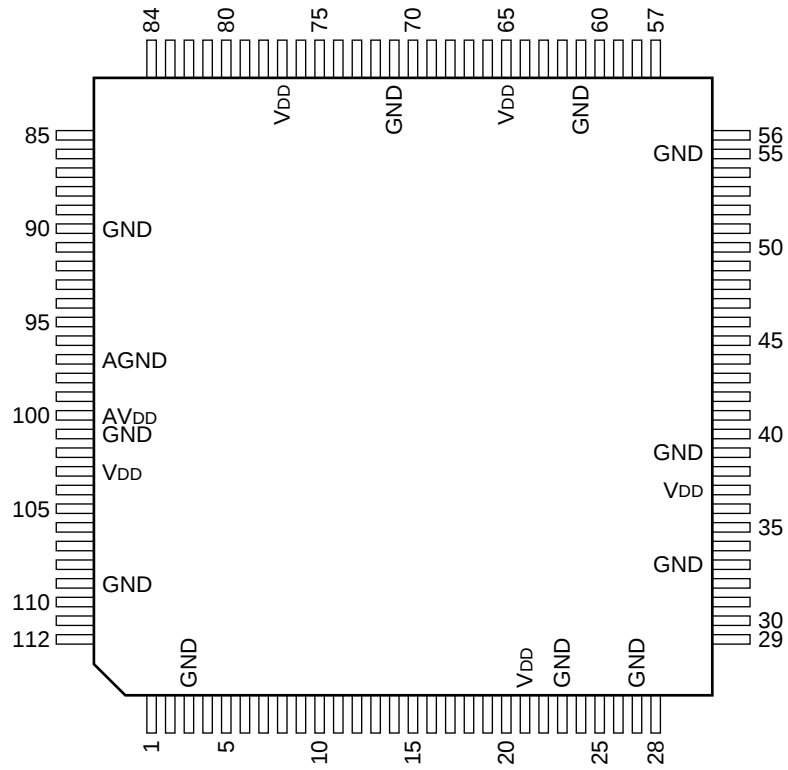


C-MOS SINGLE CHIP MICROCOMPUTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	PE14/DACK0/AH	39	—	GND	77	—	VDD
2	I/O	PE15/DACK1	40	O	CS1	78	I	MD1
3	—	GND	41	O	CS0	79	I	MD0
4	O	A0	42	I/O	PA9/TCLKD/IRQ3	80	—	PLLVD
5	O	A1	43	I/O	PA8/TCLKC/IRQ2	81	—	PLLCAP
6	O	A2	44	I/O	PA7/TCLKB/CS3	82	—	PLLGND
7	O	A3	45	I/O	PA6/TCLKA/CS2	83	I/O	PA15/CK
8	O	A4	46	I/O	PA5/SCK1/DREQ1/IRQ1	84	I	RES
9	O	A5	47	I/O	PA4/TXD1	85	I/O	PE0/TIOC0A/DREQ0
10	O	A6	48	I/O	PA3/RXD1	86	I/O	PE1/TIOC0B/DRAK0
11	O	A7	49	I/O	PA2/SCK0/DREQ0/IRQ0	87	I/O	PE2/TIOC0C/DREQ1
12	O	A8	50	I/O	PA1/TXD0	88	I/O	PE3/TIOC0D/DRAK1
13	O	A9	51	I/O	PA0/RXD0	89	I/O	PE4/TIOC1A
14	O	A10	52	I/O	D15	90	—	GND
15	O	A11	53	I/O	D14	91	I	PF0/AN0
16	O	A12	54	I/O	D13	92	I	PF1/AN1
17	O	A13	55	—	GND	93	I	PF2/AN2
18	O	A14	56	I/O	D12	94	I	PF3/AN3
19	O	A15	57	I/O	D11	95	I	PF4/AN4
20	O	A16	58	I/O	D10	96	I	PF5/AN5
21	—	VDD	59	I/O	D9	97	—	AGND
22	O	A17	60	I/O	D8	98	I	PF6/AN6
23	—	GND	61	—	GND	99	I	PF7/AN7
24	I/O	PB2/IRQ0/RAS	62	I/O	D7	100	—	AVDD
25	I/O	PB3/IRQ1/CASL	63	I/O	D6	101	—	GND
26	I/O	PB4/IRQ2/CASH	64	I/O	D5	102	I/O	PE5/TIOC1B
27	—	GND	65	—	VDD	103	—	VDD
28	I/O	PB5/IRQ3/RDWR	66	I/O	D4	104	I/O	PE6/TIOC2A
29	I/O	PB6/A18	67	I/O	D3	105	I/O	PE7/TIOC2B
30	I/O	PB7/A19	68	I/O	D2	106	I/O	PE8
31	I/O	PB8/IRQ6/A20/WAIT	69	I/O	D1	107	I/O	PE9
32	I/O	PB9/IRQ7/A21	70	I/O	D0	108	I/O	PE10
33	—	GND	71	—	GND	109	—	GND
34	O	RD	72	I	XTAL	110	I/O	PE11
35	O	WDTOVF	73	I	MD3	111	I/O	PE12
36	O	WRH	74	I	EXTAL	112	I/O	PE13
37	—	VDD	75	I	MD2			
38	O	WRL	76	I	NMI			

INPUT

AN0 - AN7	: ANALOG
DREQ0, DREQ1	: DMA TRANSFER REQUEST (CH0, 1)
EXTAL	: CRYSTAL OSCILLATION OR EXTERNAL CLOCK
IRQ0 - IRQ3, IRQ6, IRQ7	: MASKABLE INTERRUPT REQUEST
MD0 - MD3	: MODE CONTROL
NMI	: NON-MASKABLE INTERRUPT REQUEST
PF0 - PF7	: PORT F
RES	: RESET
RXD0, RXD1	: RECEIVE DATA (CH0, 1)
TCLKA, TCLKB, TCLKC, TCLKD	: MTU TIMER CLOCK
WAIT	: WAIT
XTAL	: CRYSTAL OSCILLATION

OUTPUT

A0 - A21	: ADDRESS BUS
AH	: ADDRESS HOLD
CK	: SYSTEM CLOCK
CASH	: HIGH ORDER COLUMN ADDRESS STROBE
CASL	: LOW ORDER COLUMN ADDRESS STROBE
CS0 - CS3	: CHIP SELECT
DACK0, DACK1	: DMA TRANSFER STROBE (CH0, 1)
DRAK0, DRAK1	: DREQ ACKNOWLEDGE (CH0, 1)
RAS	: ROW ADDRESS STROBE
RD	: READ
RDWR	: DRAM READ/WRITE
TXD0, TXD1	: TRANSMISSION DATA (CH0, 1)
WDTOVF	: WATCHDOG TIMER OVERFLOW
WRH	: HIGH ORDER WRITE
WRL	: LOW ORDER WRITE

INPUT/OUTPUT

D0 - D15	: DATA BUS
PA0 - PA9, PA15	: PORT A
PB2 - PB9	: PORT B
PE0 - PE15	: PORT E
SCK0, SCK1	: SERIAL CLOCK (CH0, 1)
TIOC0A, TIOC0B, TIOC0C, TIOC0D	: MTU INPUT CAPTURE/OUTPUT COMPARE (CH0)
TIOC1A, TIOC1B	: MTU INPUT CAPTURE/OUTPUT COMPARE (CH1)
TIOC2A, TIOC2B	: MTU INPUT CAPTURE/OUTPUT COMPARE (CH2)