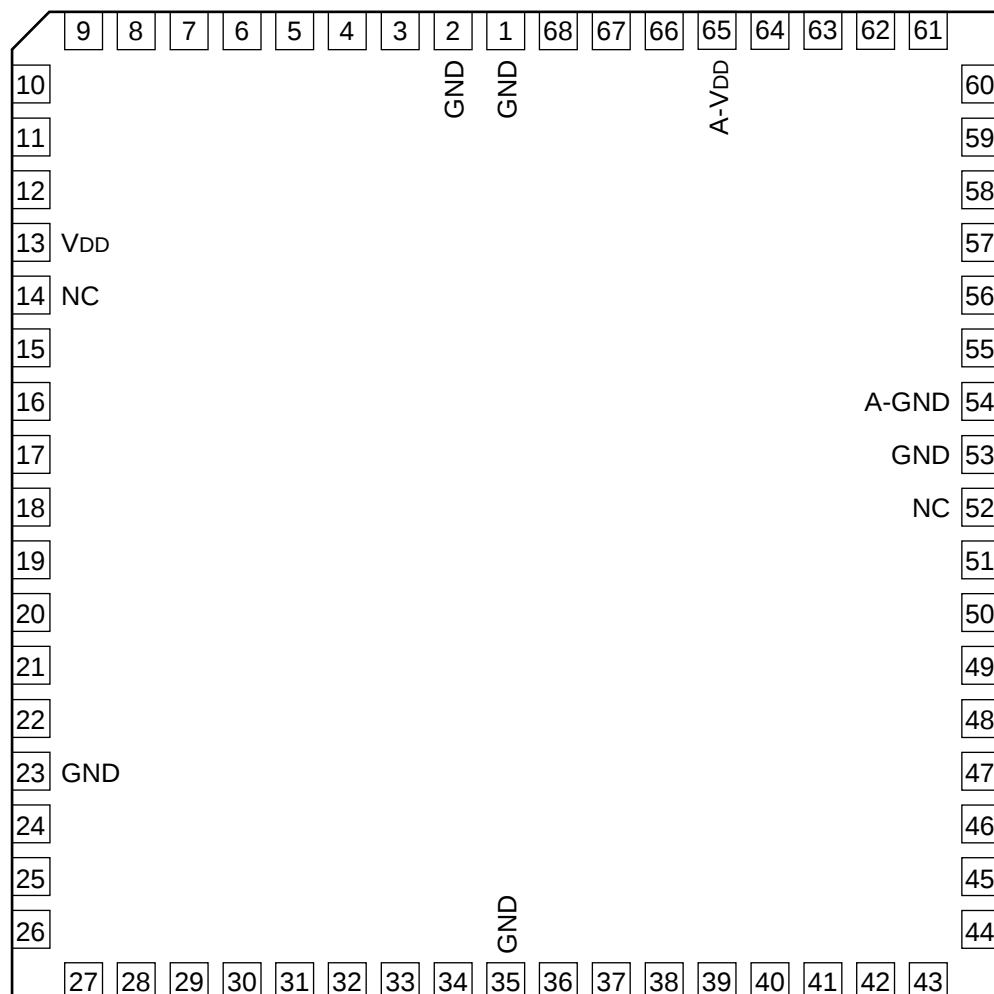
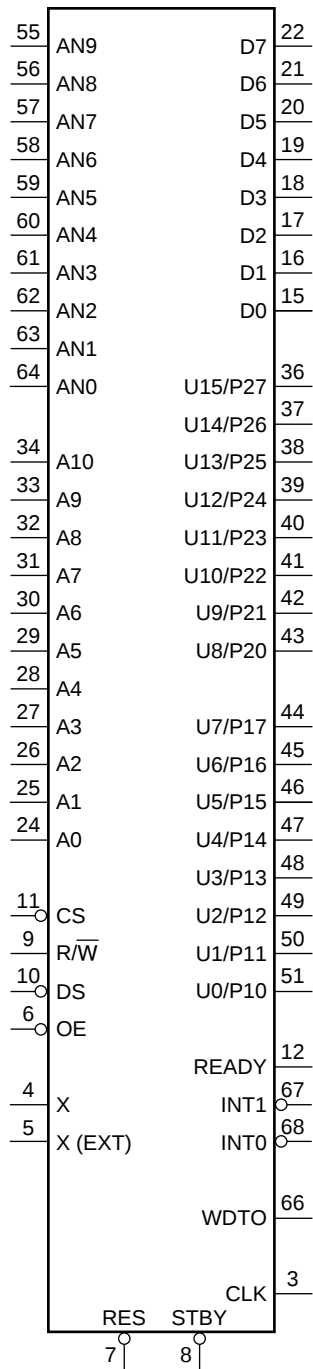


C-MOS UNIVERSAL PULSE PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	18	I/O	D3	35	—	GND	52	—	NC
2	—	GND	19	I/O	D4	36	I/O	U15/P27	53	—	GND
3	O	CLK	20	I/O	D5	37	I/O	U14/P26	54	—	A-GND
4	I	X	21	I/O	D6	38	I/O	U13/P25	55	I	AN9
5	I	X (EXT)	22	I/O	D7	39	I/O	U12/P24	56	I	AN8
6	I	OE	23	—	GND	40	I/O	U11/P23	57	I	AN7
7	I	RES	24	I	A0	41	I/O	U10/P22	58	I	AN6
8	I	STBY	25	I	A1	42	I/O	U9/P21	59	I	AN5
9	I	R/W	26	I	A2	43	I/O	U8/P20	60	I	AN4
10	I	DS	27	I	A3	44	I/O	U7/P17	61	I	AN3
11	I	CS	28	I	A4	45	I/O	U6/P16	62	I	AN2
12	O	READY	29	I	A5	46	I/O	U5/P15	63	I	AN1
13	—	VDD	30	I	A6	47	I/O	U4/P14	64	I	AN0
14	—	NC	31	I	A7	48	I/O	U3/P13	65	—	A-VDD
15	I/O	D0	32	I	A8	49	I/O	U2/P12	66	O	WDTO
16	I/O	D1	33	I	A9	50	I/O	U1/P11	67	O	INT1
17	I/O	D2	34	I	A10	51	I/O	U0/P10	68	O	INT0



INPUT

A0 - A10 : ADDRESS
 AN0 - AN9 : ANALOG SIGNAL
 $\overline{CS}$: CHIP SELECT
 $\overline{DS}$: DATA STROBE
 $\overline{OE}$: OUTPUT ENABLE
 $\overline{RES}$: RESET
 $\overline{R/W}$: READ/WRITE
 $\overline{STBY}$: STANDBY
 X : X'TAL
 X (EXT) : X'TAL OR EXT CLOCK

OUTPUT

CLK : CLOCK
 INT0, INT1 : INTERRUPT
 READY : READY

INPUT/OUTPUT

D0 - D7 : DATA
 U0/P10 - U15/P27 : UPP PULSE OR MPU PORT

