SGS-THOMSON MICROELECTRONICS

HCC/HCF4095B
HCC/HCF4096B

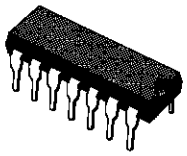
GATE J-K MASTER-SLAVE FLIP-FLOPS

- 16 MHz TOGGLE RATE (typ.) AT V_{DD} - V_{SS} = 10V
- GATED INPUTS
- QUIESCENT CURRENT SPECIFIED TO 20v FOR HCC DEVICE
- 5V, 10V AND 15V PARAMETRIC RATINGS
- INPUT CURRENT OF 100 nA AT 18V AND 25oC FOR HCC DEVICE
- 100% TESTED FOR QUIESCENT CURRENT
- MEETS ALL REQUIREMENTS OF JEDEC TENTATIVE STANDARD No 13 A, "STANDARD SPECIFICATIONS FOR DESCRIPTION OF B SERIES CMOS DEVICES"

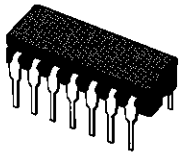
DESCRIPTION

The **HCC4095B/4096B** (extended temperature range) and **HCF4095B/4096B** (intermediate temperature range) are monolithic integrated circuits, available in 14 lead dual in-line plastic or ceramic package and plastic micropackage.

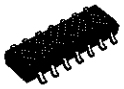
The **HCC/HCF4095B** and **HCC/HCF4096B** are J-K Master-Slave Flip-Flops featuring separate AND gating of multiple J and K inputs. The gated J-K input control transfer of information into the master section during clocked operation. Information on the J-K inputs is transferred to the Q and $\overline{Q}$ outputs on the positive edge of the clock pulse. SET and RESET inputs (active high) are provided for asynchronous operation.



EY
(Plastic Package)



F
(Ceramic Package)



M1
(Micro Package)

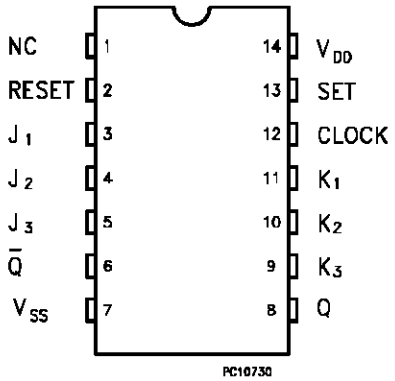
C1
(Chip Carrier)

ORDER CODES :

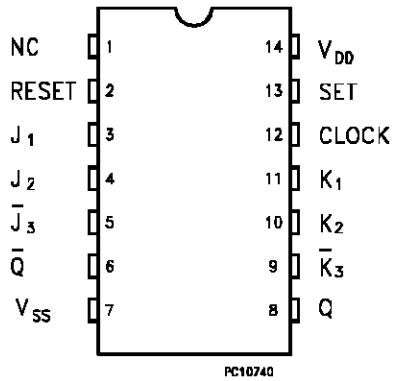
HCC40XXBF	HCF40XXBM1
HCF40XXBEY	HCF40XXBC1

PIN CONNECTIONS

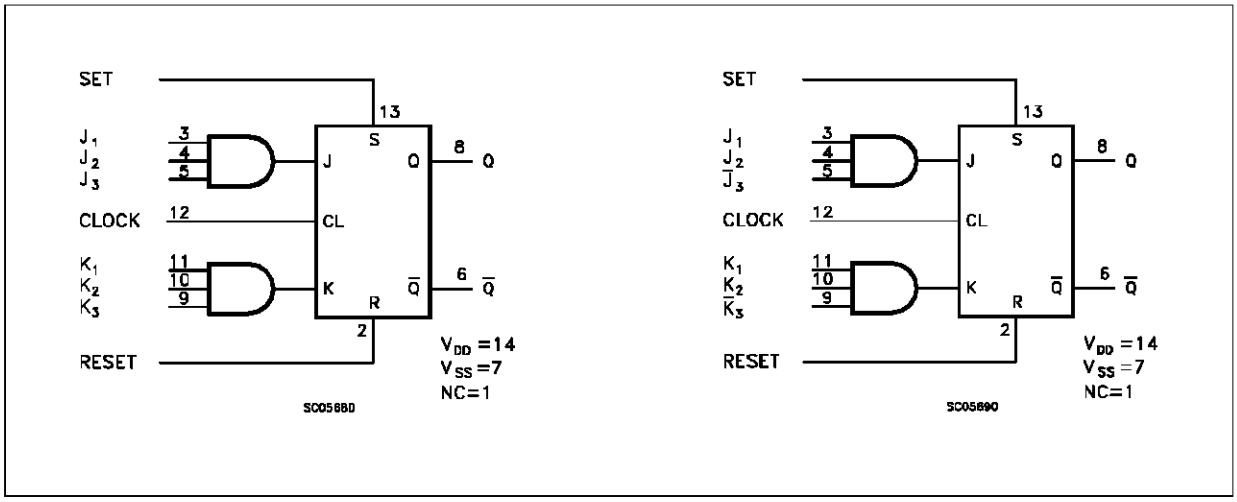
4095B



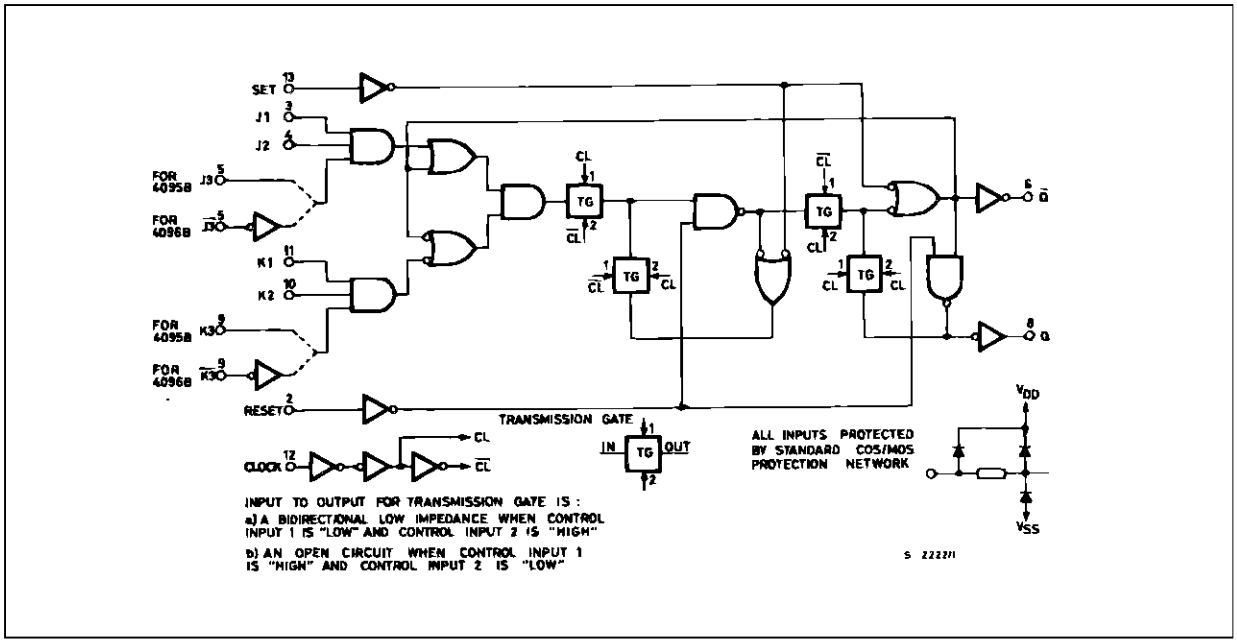
4096B



FUNCTIONAL DIAGRAMS



LOGIC DIAGRAM



TRUTH TABLES

SYNCHRONOUS OPERATION (S=0 R=0)

Inputs Before Positive Clock Transition		Outputs After Positive Clock Transition	
J *	K *	Q	$\overline{Q}$
0	0	No Change	
0	1	0	1
1	0	1	0
1	1	Toggles	

* For 4095B J = J1 wJ2 w$\overline{J3}$, K = K1 wK2 w$\overline{K3}$
* For 4095B J = J1 wJ2 wJ3, K = K1 wK2 wK3

ASYNCHRONOUS OPERATION (J and K DON'T CARE)

S	R	Q	$\overline{Q}$
0	0	No Change	
0	1	0	1
1	0	1	0
1	1	0	0

0 = V_{SS}, 1 = V_{DD}

ABSOLUTE MAXIMUM RATING

Symbol	Parameter	Value	Unit
V_{DD} *	Supply Voltage: **HCC** Types **HCF** Types	-0.5 to +20 -0.5 to +18	V V
V_i	Input Voltage	-0.5 to V_{DD} + 0.5	V
I_I	DC Input Current (any one input)	+ 10	mA
P_{tot}	Total Power Dissipation (per package) Dissipation per Output Transistor for Top = Full Package Temperature Range	200 100	mW mW
T_{op}	Operating Temperature: **HCC** Types **HCF** Types	-55 to +125 -40 to +85	^oC ^oC
T_{stg}	Storage Temperature	-65 to +150	^oC

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for external periods may affect device reliability.

* All voltage values are referred to V_{SS} pin voltage.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V_{DD}	Supply Voltage: **HCC** Types **HCF** Types	3 to 18 3 to 15	V V
V_I	Input Voltage	0 to V_{DD}	V
T_{op}	Operating Temperature: **HCC** Types **HCF** Types	-55 to +125 -40 to +85	^oC ^oC

STATIC ELECTRICAL CHARACTERISTICS (over recommended operating conditions)

Symbol	Parameter		Test Conditios				Value							Unit
			V_I (V)	V_O (V)	$\|I_O\|$ (mA)	V_{DD} (V)	T_{LOW} * Min.	T_{LOW} * Max.	25 °C Min.	25 °C Typ.	25 °C Max.	T_{HIGH} * Min.	T_{HIGH} * Max.	
I_L	Quiescent Current	HCC Types	0/5			5		1		0.02	1		30	mA
			0/10			10		2		0.02	2		60	
			0/15			15		4		0.02	4		120	
			0/20			20		20		0.04	20		600	
		HCF Types	0/5			5		4		0.02	4		30	
			0/10			10		8		0.02	8		60	
			0/15			15		16		0.02	16		120	
V_{OH}	Output High Voltage		0/5		< 1	5	4.95		4.95			4.95		V
			0/10		< 1	10	9.95		9.95			9.95		
			0/15		< 1	15	14.95		14.95			14.95		
V_{OL}	Output Low Voltage		5/0		< 1	5		0.05			0.05		0.05	V
			10/0		< 1	10		0.05			0.05		0.05	
			15/0		< 1	15		0.05			0.05		0.05	
V_{IH}	Input High Voltage			4.5	< 1	5	3.5		3.5			3.5		V
				9	< 1	10	7		7			7		
				13.5	< 1	15	11		11			11		
V_{IL}	Input Low Voltage			0.5	< 1	5		1.5			1.5		1.5	V
				1	< 1	10		3			3		3	
				1.5	< 1	15		4			4		4	
I_{OH}	Output Drive Current	HCC Types	0/5	2.5		5	-2		-1.6	-3.2		-1.15		mA
			0/5	4.6		5	-0.64		-0.51	-1		-0.36		
			0/10	9.5		10	-1.6		-1.3	-2.6		-0.9		
			0/15	13.5		15	-4.2		-3.4	-6.8		-2.4		
		HCF Types	0/5	2.5		5	-1.53		-1.36	-3.2		-1.1		
			0/5	4.6		5	-0.52		-0.44	-1		-0.36		
			0/10	9.5		10	-1.3		-1.1	-2.6		-0.9		
			0/15	13.5		15	-3.6		-3.0	-6.8		-2.4		
I_{OL}	Output Sink Current	HCC Types	0/5	0.4		5	0.64		0.51	1		0.36		mA
			0/10	0.5		10	1.6		1.3	2.6		0.9		
			0/15	1.5		15	4.2		3.4	6.8		2.4		
		HCF Types	0/5	0.4		5	0.52		0.44	1		0.36		
			0/10	0.5		10	1.3		1.1	2.6		0.9		
			0/15	1.5		15	3.6		3.0	6.8		2.4		
I_{IH}, I_{IL}	Input Leakage Current		0/18	Any Input		18		+0.1		$+10^{-5}$	+0.1		+1	mA
			0/15			15		+0.3		$+10^{-5}$	+0.3		+1	
C_I	Input Capacitance			Any Input						5	7.5			pF

* T_{LOW} = -55 °C for **HCC** device: -40 °C for **HCF** device.

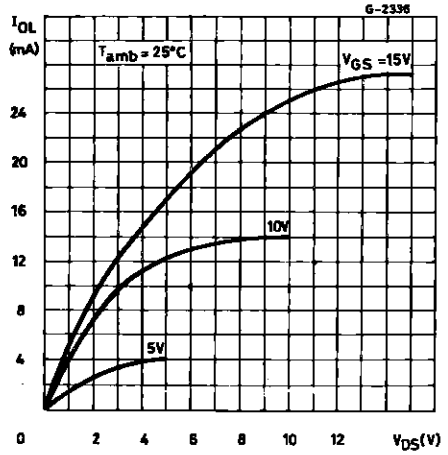
* T_{HIGH} = +125 °C for **HCC** device: +85 °C for **HCF** device.

The Noise Margin for both "1" and "0" level is: 1V min. with V_{DD} = 5 V, 2 V min. with V_{DD} = 10 V, 2.5 V min. with V_{DD} = 15 V

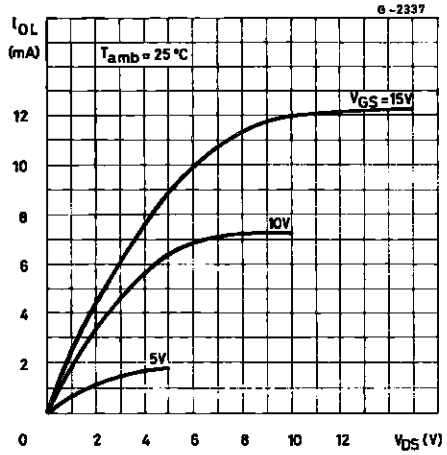
DYNAMIC ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, C_L = 50 pF, R_L = 200 KW, typical temperature coefficent for all V_{DD} values is 03 %/°C, all input rise and fall times= 20 ns)

Symbol	Parameter	Test Conditions		Value			Unit
			V_{DD} (V)	Min.	Typ.	Max.	
t_{PLH} t_{PHL}	Propagation Delay Time		5		250	500	ns
			10		100	200	
			15		75	150	
t_{PLH} t_{PHL}	Propagation Delay Time (Set or Reset)		5		150	300	ns
			10		75	150	
			15		50	100	
t_{THL} t_{TLH}	Transition Time		5		100	200	ns
			10		50	100	
			15		40	80	
f_{CL}	Maximum Clock Input Frequency		5	3.5	7		MHz
			10	8	16		
			15	12	24		
t_W	Clock Pulse Width		5	140	70		ns
			10	60	30		
			15	40	20		
t_r t_f	Clock Input Rise or Fall Time		5			15	ms
			10			5	
			15			5	
t_W	Set or Reset Pulse Width		5	200	100		ns
			10	100	50		
			15	50	25		
t_{setup}	Data Setup Time		5	400	200		ns
			10	160	80		
			15	100	50		

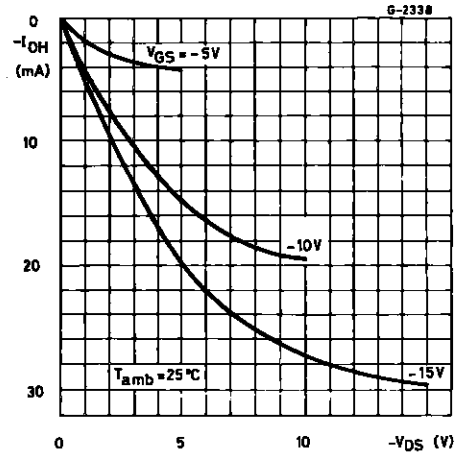
Typical Output Low (sink) Current Characteristics



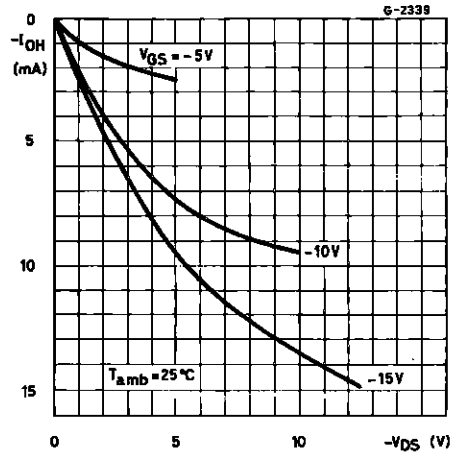
Minimum Output low (sink) Current Characteristics



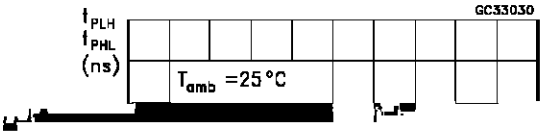
Typical Output High (source) Current Characteristics



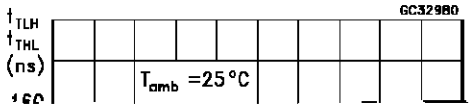
Minimum Output High (source) Current Characteristics



Typical Propagation Delay Time vs Load Capacitance



Typical Transition Time vs Load Capacitance



Typical Clock Frequency vs Supply Voltage (Toggle Mode)

Typical Power Dissipation Vs Input Clock Frequency