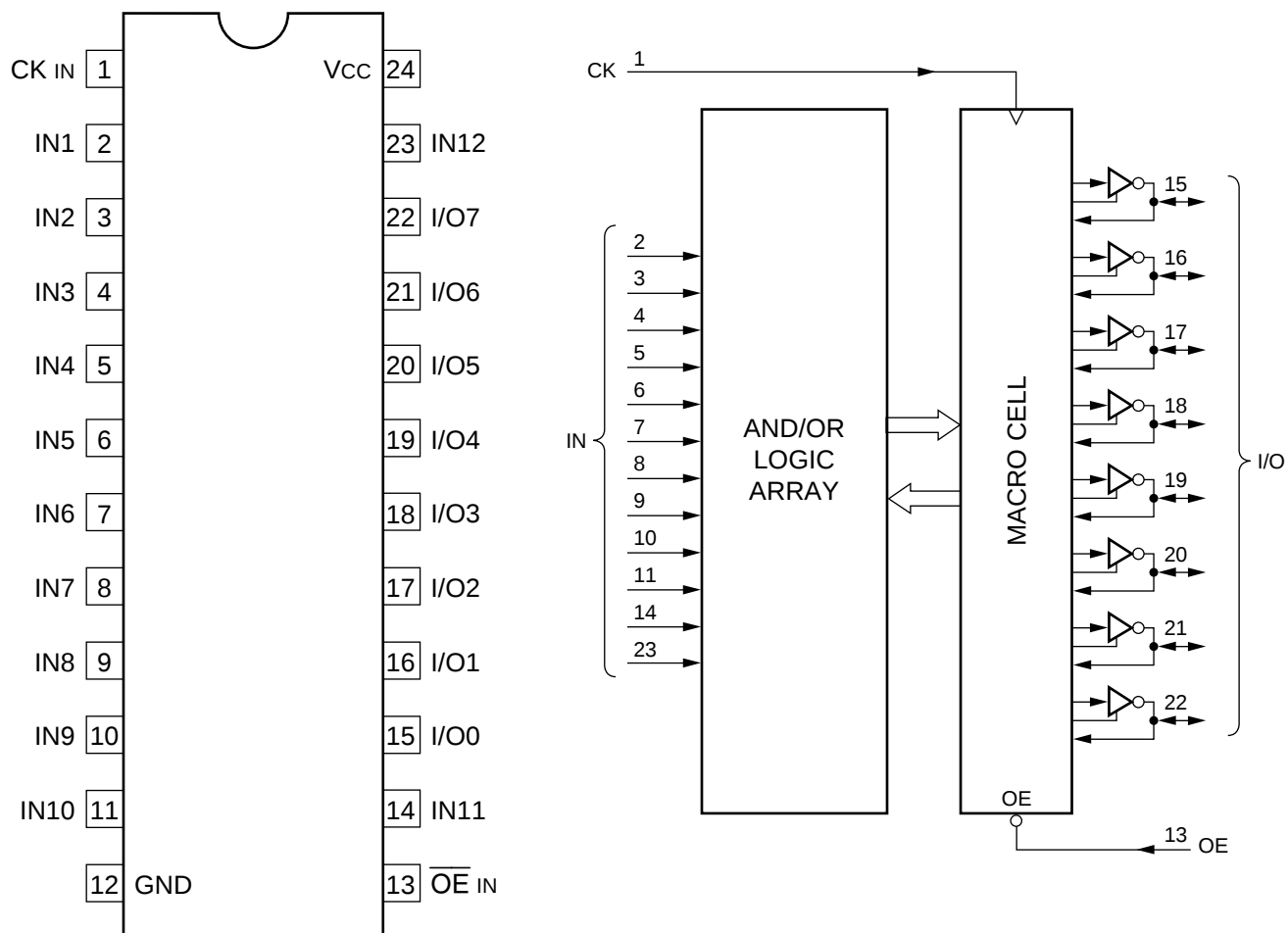


C-MOS ELECTRICALLY ERASABLE PROGRAMMABLE LOGIC DEVICE

—TOP VIEW—



* ABOVE DIAGRAM SHOWS CONDITIONS
BEFORE PROGRAMMING.