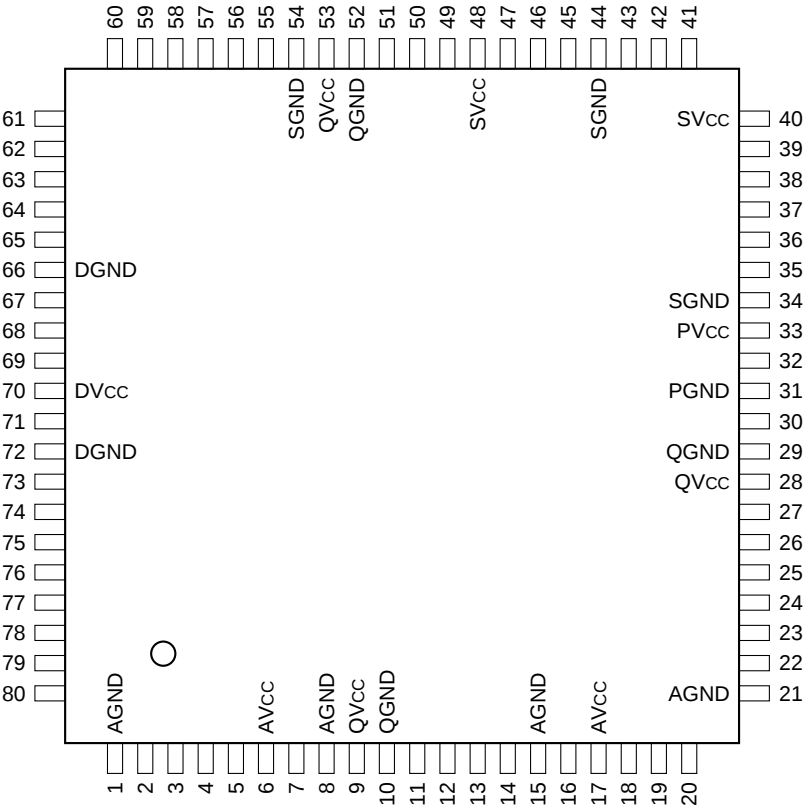


24-BIT DIGITAL SIGNAL PROCESSOR
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	AGND	21	—	AGND	41	I/O	MOSI/HA0	61	I	DR
2	O	MCS0	22	O	MA3	42	I	SS/HA2	62	I/O	MD7
3	O	MA15/MCS3	23	O	MA2	43	I/O	HREQ	63	I/O	MD6
4	O	MA14	24	O	MA1	44	—	SGND	64	I/O	MD5
5	O	MA13	25	O	MA0	45	O	SDO2	65	I/O	MD4
6	—	AVcc	26	I/O	SCK/SCL	46	O	SDO1	66	—	DGND
7	O	MA12	27	I	EXTAL	47	O	SDO0	67	I/O	MD3
8	—	AGND	28	—	QVcc	48	—	SVcc	68	I/O	MD2
9	—	QVcc	29	—	QGND	49	I/O	SCKT	69	I/O	MD1
10	—	QGND	30	I	PINIT	50	I/O	WST	70	—	DVcc
11	O	MA11	31	—	PGND	51	I/O	SCKR	71	I/O	MD0
12	O	MA10	32	I	PCAP	52	—	QGND	72	—	DGND
13	O	MA9	33	—	PVcc	53	—	QVcc	73	I/O	GPIO3
14	O	MA8	34	—	SGND	54	—	SGND	74	I/O	GPIO2
15	—	AGND	35	I/O	MISO/SDA	55	I/O	WSR	75	I/O	GPIO1
16	O	MA7	36	I	RESET	56	I	SDI1	76	I/O	GPIO0
17	—	AVcc	37	I	MODA/IRQA	57	I	SDI0	77	O	MRD
18	O	MA6	38	I	MODB/IRQB	58	O	DSO	78	O	MWR
19	O	MA5	39	I	MODC/NMI	59	I/O	DSI/OS0	79	O	MA17/MCS1/MRAS
20	O	MA4	40	—	SVcc	60	I/O	DSCK/OS1	80	O	MA16/MCS2/MCAS

INPUT

$\overline{\text{DR}}$	: DEBUG REQUEST
$\overline{\text{EXTAL}}$	: EXTERNAL CLOCK/CRYSTAL
$\overline{\text{MODA/IRQA}}$	: MODE SELECT A/EXTERNAL INTERRUPT REQUEST A
$\overline{\text{MODB/IRQB}}$	: MODE SELECT B/EXTERNAL INTERRUPT REQUEST B
$\overline{\text{MODC/NMI}}$	: MODE SELECT C/NON-MASKABLE INTERRUPT REQUEST
$\overline{\text{PCAP}}$	: PLL FILTER CAPACITOR
$\overline{\text{PINIT}}$	: PLL INITIALIZATION
$\overline{\text{RESET}}$	: RESET
$\overline{\text{SDI0, SDI1}}$	: SERIAL DATA
$\overline{\text{SS/HA2}}$	: SPI CONTROLLED SELECT/I ² C CONTROLLED ADDRESS 2

OUTPUT

$\overline{\text{DSO}}$	: DEBUG SERIAL
$\overline{\text{MA0 - MA17}}$	: MEMORY ADDRESS LINE
$\overline{\text{MCAS}}$	: MEMORY COLUMN ADDRESS STROBE
$\overline{\text{MCS0 - MCS3}}$	: MEMORY CHIP SELECT
$\overline{\text{MRAS}}$	: MEMORY ROW ADDRESS STROBE
$\overline{\text{MRD}}$	: MEMORY READ STROBE
$\overline{\text{MWR}}$	: MEMORY WRITE STROBE
$\overline{\text{SDO0 - SDO2}}$	: SERIAL DATA

INPUT/OUTPUT

$\overline{\text{DSCK/OS1}}$	: DEBUG SERIAL CLOCK/OPERATING STATUS 1
$\overline{\text{DSI/OS0}}$	: DEBUG SERIAL INPUT/OPERATING STATUS 0
$\overline{\text{GPIO0 - GPIO3}}$	: GENERAL PURPOSE
$\overline{\text{HREQ}}$	: HOST REQUEST
$\overline{\text{MD0 - MD7}}$	: DATA BUS
$\overline{\text{MISO/SDA}}$	: SPI MASTER IN CONTROLLED OUT/I ² C SERIAL DATA AND ACKNOWLEDGE
$\overline{\text{MOSI/HA0}}$	: SPE MASTER OUT CONTROLLED IN/I ² C CONTROLLED ADDRESS 0
$\overline{\text{SCK/SCL}}$	: SPI SERIAL CLOCK/I ² C SERIAL CLOCK
$\overline{\text{SCKT}}$	: SERIAL CLOCK TRANSMIT
$\overline{\text{SCKR}}$	: RECEIVE SERIAL CLOCK
$\overline{\text{WSR}}$	: WORD SELECT RECEIVE
$\overline{\text{WST}}$	: WORD SELECT TRANSMIT

