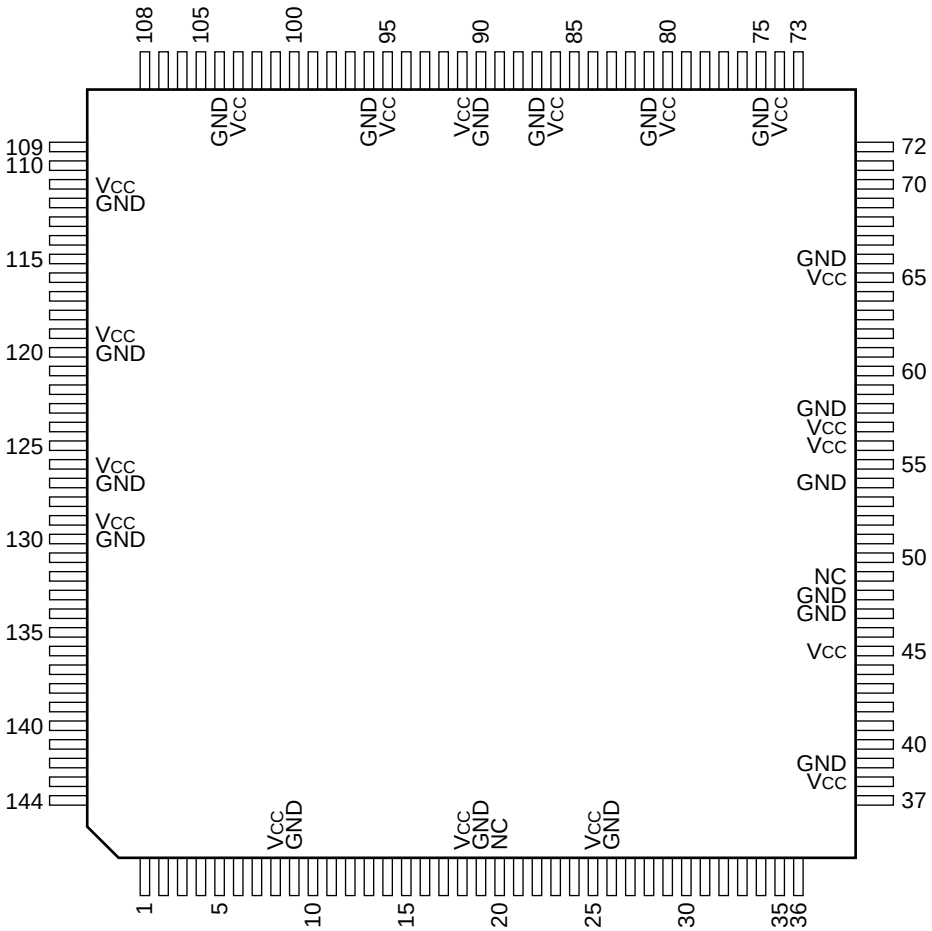
C-MOS 24-BIT DIGITAL SIGNAL PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	SRD1	37	I/O	HAD4	73	O	A1	109	I/O	D7
2	I/O	STD1	38	—	VCC	74	—	VCC	110	I/O	D8
3	I/O	SC02	39	—	GND	75	—	GND	111	—	VCC
4	I/O	SC01	40	I/O	HAD3	76	O	A2	112	—	GND
5	I/O	$\overline{DE}$	41	I/O	HAD2	77	O	A3	113	I/O	D9
6	I	PINIT/$\overline{NMI}$	42	I/O	HAD1	78	O	A4	114	I/O	D10
7	I/O	SRD0	43	I/O	HAD0	79	O	A5	115	O	A11
8	—	VCC	44	I	$\overline{RESET}$	80	—	VCC	116	O	A12
9	—	GND	45	—	VCC	81	—	GND	117	O	A13
10	I/O	STD0	46	I	PCAP	82	O	A6	118	O	A14
11	I/O	SC10	47	—	GND	83	O	A7	119	—	VCC
12	I/O	SC00	48	—	GND	84	O	A8	120	—	GND
13	I	RXD	49	—	NC	85	O	A9	121	I/O	D15
14	O	TXD	50	O	AA3/$\overline{RAS3}$	86	—	VCC	122	I/O	D16
15	I/O	SCLK	51	O	AA2/$\overline{RAS2}$	87	—	GND	123	I/O	D17
16	I/O	SCK1	52	O	$\overline{CAS}$	88	O	A10	124	I/O	D18
17	I/O	SCK0	53	O	XTAL	89	O	A11	125	I/O	D19
18	—	VCC	54	—	GND	90	—	GND	126	—	VCC
19	—	GND	55	I	EXTAL	91	—	VCC	127	—	GND
20	—	NC	56	—	VCC	92	O	A12	128	I/O	D20
21	I	HDS	57	—	VCC	93	O	A13	129	—	VCC
22	I	HRW	58	—	GND	94	O	A14	130	—	GND
23	I	HACK	59	O	CLKOUT	95	—	VCC	131	I/O	D21
24	O	HREQ	60	O	BCLK	96	—	GND	132	I/O	D22
25	—	VCC	61	O	$\overline{BCLK}$	97	O	A15	133	I/O	D23
26	—	GND	62	I	$\overline{TA}$	98	O	A16	134	I	MODD/$\overline{IRQD}$
27	I/O	TIO2	63	O	$\overline{BR}$	99	O	A17	135	I	MODC/$\overline{IRQC}$
28	I/O	TIO1	64	I/O	$\overline{BB}$	100	I/O	D0	136	I	MODB/$\overline{IRQB}$
29	I/O	TIO0	65	—	VCC	101	I/O	D1	137	I	MODA/$\overline{IRQA}$
30	I	HCS	66	—	GND	102	I/O	D2	138	I	$\overline{TRST}$
31	I	HA9	67	O	$\overline{WR}$	103	—	VCC	139	O	TDO
32	I	HA8	68	O	$\overline{RD}$	104	—	GND	140	I	TDI
33	I	HAS	69	O	AA1/$\overline{RAS1}$	105	I/O	D3	141	I	TCK
34	I/O	HAD7	70	O	AA0/$\overline{RAS0}$	106	I/O	D4	142	I	TMS
35	I/O	HAD6	71	I	$\overline{BG}$	107	I/O	D5	143	I/O	SC12
36	I/O	HAD5	72	O	A0	108	I/O	D6	144	I/O	SC11

INPUT

$\overline{BG}$	: BUS GRANT
EXTAL	: EXTERNAL CLOCK/CRYSTAL
HA8, HA9	: HOST ADDRESS
HACK	: HOST ACKNOWLEDGE
HAS	: HOST ADDRESS STRODE
HCS	: HOST CHIP SELECT
HDS	: HOST DATA STROBE
HRW	: HOST READ/WRITE
MODA/$\overline{IRQA}$	: MODE SELECT A/EXTERNAL INTERRUPT REQUEST A
MODB/$\overline{IRQB}$	: MODE SELECT B/EXTERNAL INTERRUPT REQUEST B
MODC/$\overline{IRQC}$	: MODE SELECT C/EXTERNAL INTERRUPT REQUEST C
MODD/$\overline{IRQD}$	: MODE SELECT D/EXTERNAL INTERRUPT REQUEST D
PCAP	: PLL CAPACITOR
PINIT/$\overline{NMI}$	: PLL INITIAL/NON-MASKABLE INTERRUPT
$\overline{RESET}$	: RESET
RXD	: SERIAL RECEIVE DATA
$\overline{TA}$	: TRANSFER ACKNOWLEDGE
TCK	: TEST CLOCK
TDI	: TEST DATA
TMS	: TEST MODE SELECT
$\overline{TRST}$	: TEST RESET

OUTPUT

A0 - A17	: EXTERNAL ADDRESS BUS
AA0/$\overline{RAS0}$ - AA3 /$\overline{RAS3}$	: ADDRESS ATTRIBUTE OR ROW ADDRESS STROBE
BCLK	: BUS CLOCK
$\overline{BCLK}$	: BUS CLOCK NOT
$\overline{BR}$	: BUS REQUEST
$\overline{CAS}$	: COLUMN ADDRESS STROBE
CLKOUT	: CLOCK
HREQ	: HOST REQUEST
$\overline{RD}$	: READ ENABLE
TDO	: TEST DATA
TXD	: SERIAL TRANSMIT DATA
$\overline{WR}$	: WRITE ENABLE
XTAL	: CRYSTAL

INPUT/OUTPUT

$\overline{BB}$	: BUS BUSY
D0 - D23	: EXTERNAL DATA BUS
$\overline{DE}$	: DEBUG EVENT
HAD0 - HAD7	: HOST ADDRESS
SC00 - SC02, SC10 - SC12	: SERIAL CONTROL
SCK0, SCK1, SCLK	: SERIAL CLOCK
SRD0, SRD1	: SERIAL RECEIVE DATA
STD0, STD1	: SERIAL TRANSMIT DATA
TIO0 - TIO2	: TIMER SCHMITT-TRIGGER