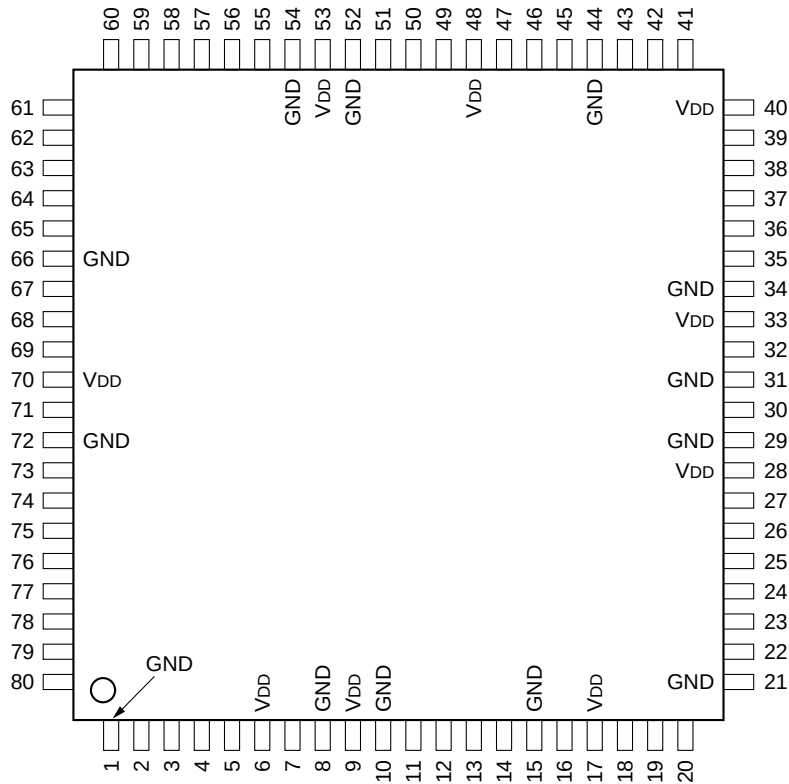


C-MOS 24-BIT DIGITAL SIGNAL PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	21	—	GND	41	I/O	MOSI/HA0	61	I	$\overline{\text{DR}}$
2	O	$\overline{\text{MCS0}}$	22	O	MA3	42	I	$\overline{\text{SS/HA2}}$	62	I/O	MD7
3	O	MA15/ $\overline{\text{MCS3}}$	23	O	MA2	43	I/O	$\overline{\text{HREQ}}$	63	I/O	MD6
4	O	MA14	24	O	MA1	44	—	GND	64	I/O	MD5
5	O	MA13	25	O	MA0	45	O	SDO2	65	I/O	MD4
6	—	VDD	26	I/O	SCK/SCL	46	O	SDO1	66	—	GND
7	O	MA12	27	I	EXTAL	47	O	SDO0	67	I/O	MD3
8	—	GND	28	—	VDD	48	—	VDD	68	I/O	MD2
9	—	VDD	29	—	GND	49	I/O	SCKT	69	I/O	MD1
10	—	GND	30	I	PINIT	50	I/O	WST	70	—	VDD
11	O	MA11	31	—	GND	51	I/O	SCKR	71	I/O	MD0
12	O	MA10	32	I	PCAP	52	—	GND	72	—	GND
13	O	MA9	33	—	VDD	53	—	VDD	73	I/O	GPIO3
14	O	MA8	34	—	GND	54	—	GND	74	I/O	GPIO2
15	—	GND	35	I/O	MISO/SDA	55	I/O	WSR	75	I/O	GPIO1
16	O	MA7	36	I	RESET	56	I	SDI1	76	I/O	GPIO0
17	—	VDD	37	I	MODA/ $\overline{\text{IROA}}$	57	I	SDI0	77	O	$\overline{\text{MRD}}$
18	O	MA6	38	I	MODB/ $\overline{\text{IROB}}$	58	O	DSO	78	O	$\overline{\text{MWR}}$
19	O	MA5	39	I	MODC/ $\overline{\text{NMI}}$	59	I/O	DSI/OS0	79	O	MA17/ $\overline{\text{MCS1}}$ / $\overline{\text{MRAS}}$
20	O	MA4	40	—	VDD	60	I/O	DSCK/OS1	80	O	MA16/ $\overline{\text{MCS2}}$ / $\overline{\text{MCAS}}$

INPUT

DR	: DEBUG REQUEST
DSCK	: DEBUG SERIAL CLOCK
DSI	: DEBUG SERIAL INPUT
EXTAL	: EXTERNAL CLOCK/CRYSTAL
HA0, HA2	: I ² C (CONTROLLED) ADDRESS
IRQA, IRQB	: EXTERNAL INTERRUPT REQUEST
MODA - MODC	: MODE SELECT
PCAP	: PLL FILTER CAPACITOR
PINIT	: PLL INITIALIZATION
RESET	: RESET
SDI0, SDI1	: SERIAL DATA INPUT
SS	: SPI (CONTROLLED) SELECT

OUTPUT

DSO	: DEBUG SERIAL OUTPUT
MA0 - MA17	: MEMORY ADDRESS BUS
MCAS	: MEMORY COLUMN ADDRESS STROBE
MCS0 - MCS3	: MEMORY CHIP SELECT
MRAS	: MEMORY ROW ADDRESS STROBE
MRD	: MEMORY READ STROBE
MWR	: MEMORY WRITE STROBE
OS0, OS1	: OPERATING STATUS
SDO0 - SDO2	: SERIAL DATA OUTPUT

INPUT/OUTPUT

GPIO0 - GPIO3	: GENERAL PURPOSE I/O SIGNAL
HREQ	: HOST REQUEST
MD0 - MD7	: DATA BUS
MISO	: SPI CONTROL-IN-CONTROLLED-OUT
MOSI	: SPI CONTROL-OUT-CONTROLLED-IN
SCK	: SPI SERIAL CLOCK
SCKR	: RECEIVE SERIAL CLOCK
SCKT	: SERIAL CLOCK TRANSMIT
SCL	: I ² C SERIAL CLOCK
SDA	: I ² C SERIAL DATA AND ACKNOWLEDGE
WSR	: WORD SELECT RECEIVE
WST	: WORD SELECT TRANSMIT