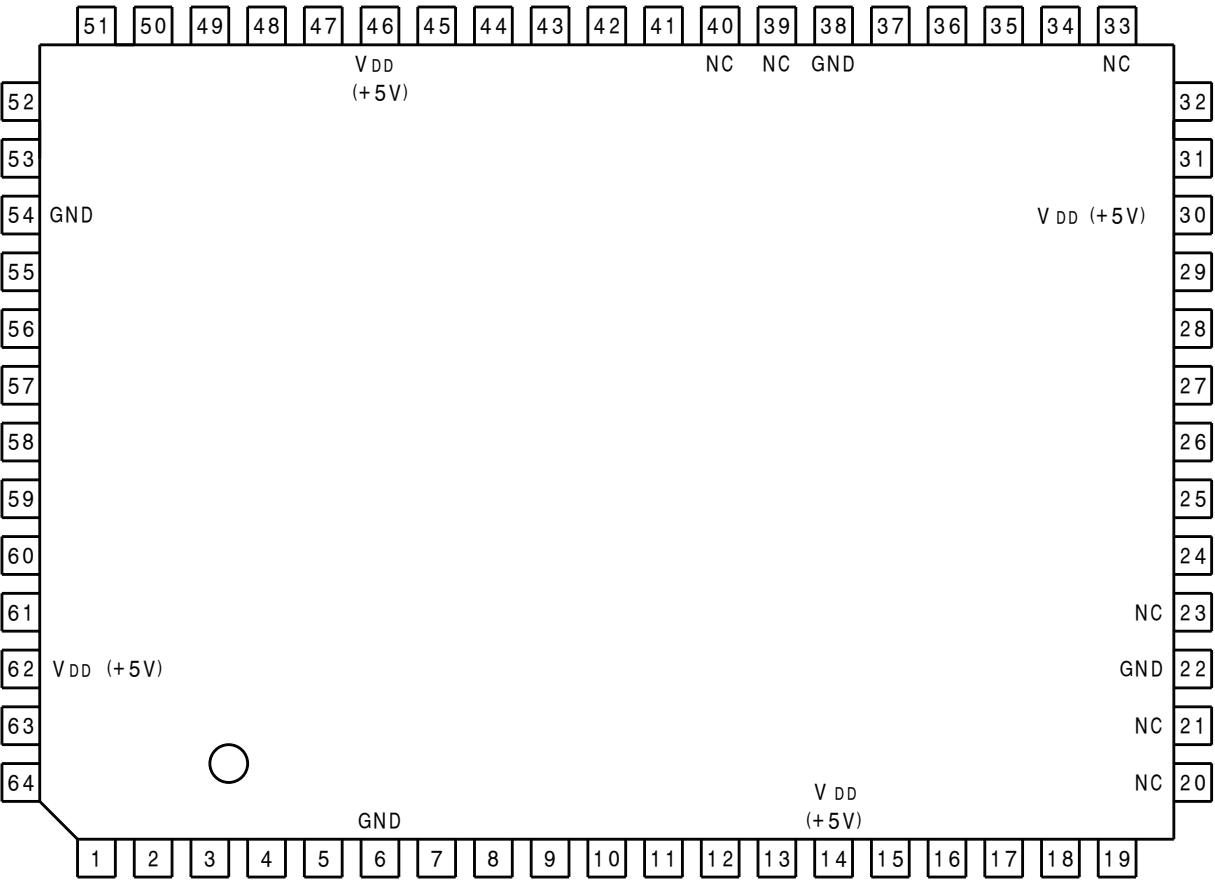

C-MOS DIGITAL OUTPUT
-TOP VIEW-



PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL
1	I	UB1	17	I	SEL1	33	—	NC	49	I	PI12
2	I	UB2	18	I	STB	34	I	EXTWS0	50	I	PI11
3	I	UB3	19	I	CS	35	I	EXTWS1	51	I	PI10
4	I	UB4	20	—	NC	36	I	EXTWS2	52	I	PI9
5	I	UB5	21	—	NC	37	I	EXTWS3	53	I	PI8
6	—	GND	22	—	GND	38	—	GND	54	—	GND
7	I	UB6	23	—	NC	39	—	NC	55	I	PI7
8	I	UB7	24	0	INT2563	40	—	NC	56	I	PI6
9	I	FI0	25	0	INT2562	41	I	PI19	57	I	PI5
10	I	FI1	26	0	INT2561	42	I	PI18	58	I	PI4
11	I	FI2	27	0	INT2560	43	I	PI17	59	I	PI3
12	I	W. SYNC	28	0	S00	44	I	PI16	60	I	PI2
13	I	CK	29	0	S01	45	I	PI15	61	I	PI1
14	—	VDD	30	—	VDD	46	—	VDD	62	—	VDD
15	I	RESET	31	0	S02	47	I	PI14	63	I	PI0
16	I	SELO	32	0	S03	48	I	PI13	64	I	UB0

INPUT

P10-P119 ;PCM DATA PARALELL INPUT
 UB0-UB7 ;USER'S BIT PARALELL INPUT
 F10-F12 ;FLAG PALALELL INPUT
 F10:EMPHASIS0
 F11:EMPHASIS1
 F12:DUBBING PROHIBIT
 EXTWS0-EXTWS3 ;EACH CHANNEL 256W.SYNC INPUT
 SEL0, 1 ;CHANNEL ADDRESS INPUT
 FOR PARALELL DATA LATCH
 STB ;STROBE INPUT FOR DATA LATCH
 CS ;CHIP SELECT INPUT FOR CASCADE
 CONNECTION

OUTPUT

S00-S03 ;SERIAL DATA OUTPUT
 INT2560-INT2563 ;256W.SYNC EACH CHANNEL OUTPUT

