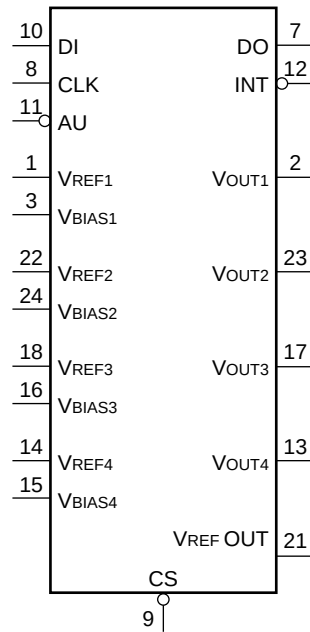
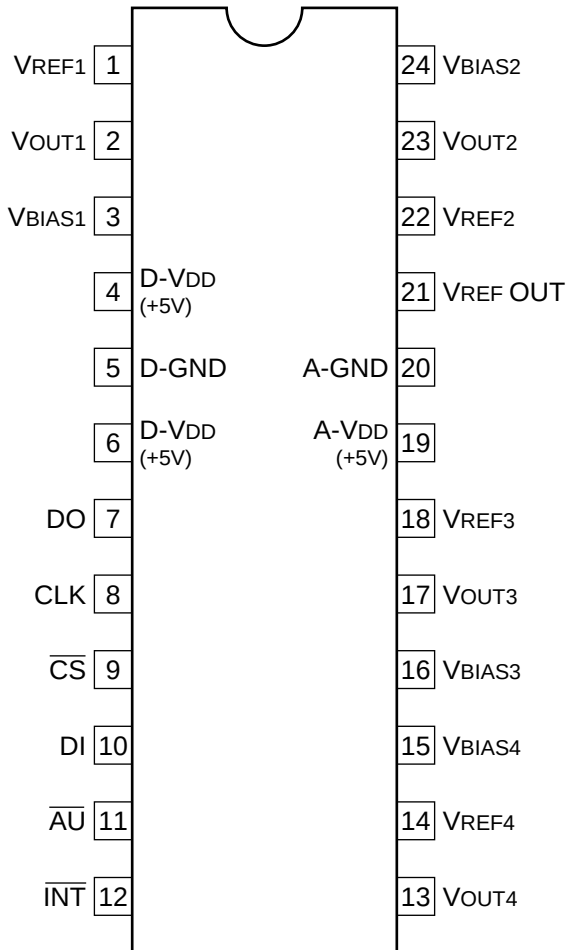

C-MOS QUAD 10-BIT SERIAL D/A CONVERTER WITH READBACK

-TOP VIEW-

**INPUT**

$\overline{AU}$; ASYNCHRONOUS UPDATE
 CLK ; EXTERNAL CLOCK
 $\overline{CS}$; CHIP SELECT
 DI ; SERIAL DATA
 VBIAS1 - 4 ; NON-INVERTING
 VREF1 - 4 ; VOLTAGE REFERENCE

OUTPUT

DO ; SERIAL DATA
 $\overline{INT}$; POWER INTERRUPT
 VOUT1 - 4 ; DACs OUTPUT VOLTAGE
 VREF OUT ; INTERNAL VOLTAGE REFERENCE

