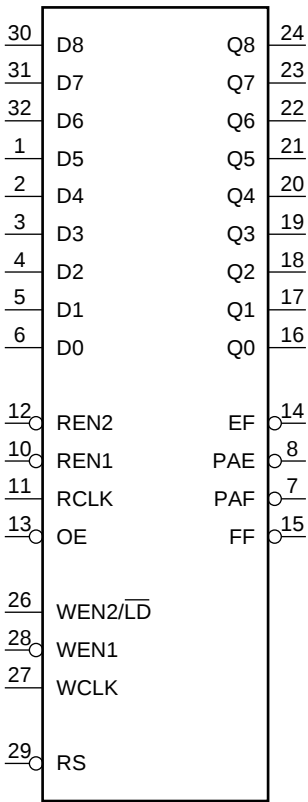
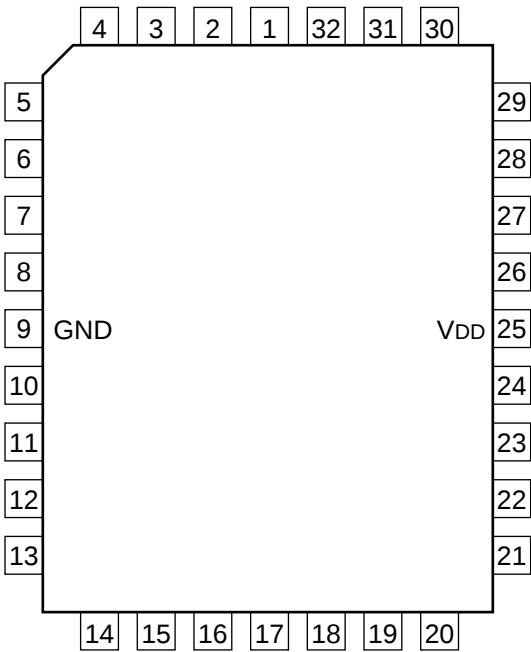


C-MOS 1 K × 9-BIT SYNCHRONOUS FIFO

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	D5	12	I	REN2	23	O	Q7
2	I	D4	13	I	OE	24	O	Q8
3	I	D3	14	O	EF	25	—	VDD
4	I	D2	15	O	FF	26	I	WEN2/LD
5	I	D1	16	O	Q0	27	I	WCLK
6	I	D0	17	O	Q1	28	I	WEN1
7	O	PAF	18	O	Q2	29	I	RS
8	O	PAE	19	O	Q3	30	I	D8
9	—	GND	20	O	Q4	31	I	D7
10	I	REN1	21	O	Q5	32	I	D6
11	I	RCLK	22	O	Q6			

INPUT

D0 - D8 : DATA INPUTS

OE : OUTPUT ENABLE

RCLK : READ CLOCK

REN1, REN2 : READ ENABLE 1, 2

RS : RESET

WCLK : WRITE CLOCK

WEN1 : WRITE ENABLE 1

WEN2/LD : WRITE ENABLE 2/LOAD

OUTPUT

EF : EMPTY FLAG

FF : FULL FLAG

PAE : PROGRAMMABLE ALMOST EMPTY

PAF : PROGRAMMABLE ALMOST FULL

Q0 - Q8 : DATA OUTPUTS

