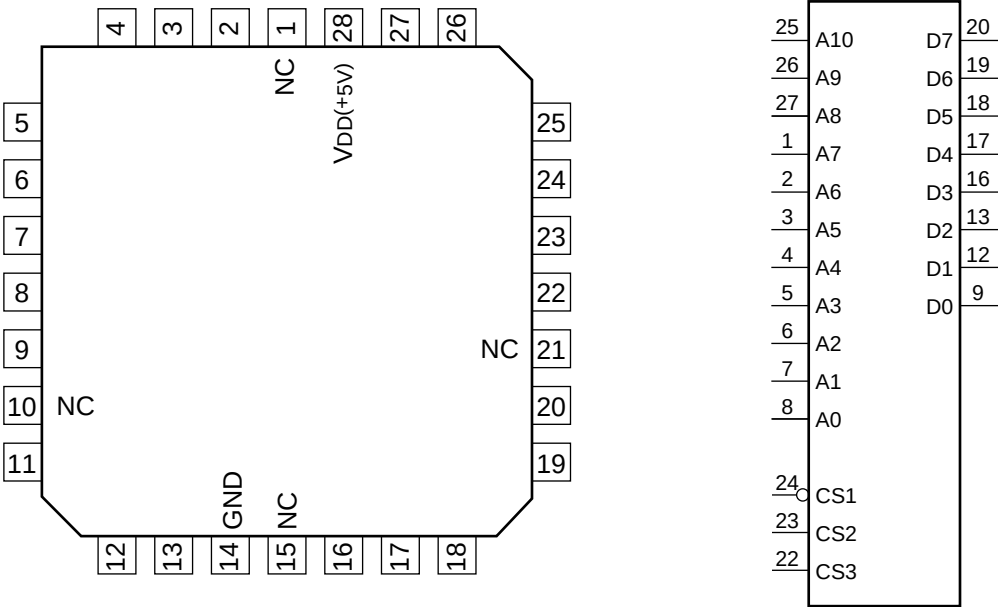

C-MOS 16K(2,048X8)-BIT PROGRAMMABLE ROM

-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	A7	15	—	—
2	I	A6	16	O	D3
3	I	A5	17	O	D4
4	I	A4	18	O	D5
5	I	A3	19	O	D6
6	I	A2	20	O	D7
7	I	A1	21	—	—
8	I	A0	22	I	CS3
9	O	D0	23	I	CS2
10	—	—	24	I	CS1
11	O	D0	25	I	A10
12	O	D1	26	I	A9
13	O	D2	27	I	A8
14	—	GND	28	—	VDD

