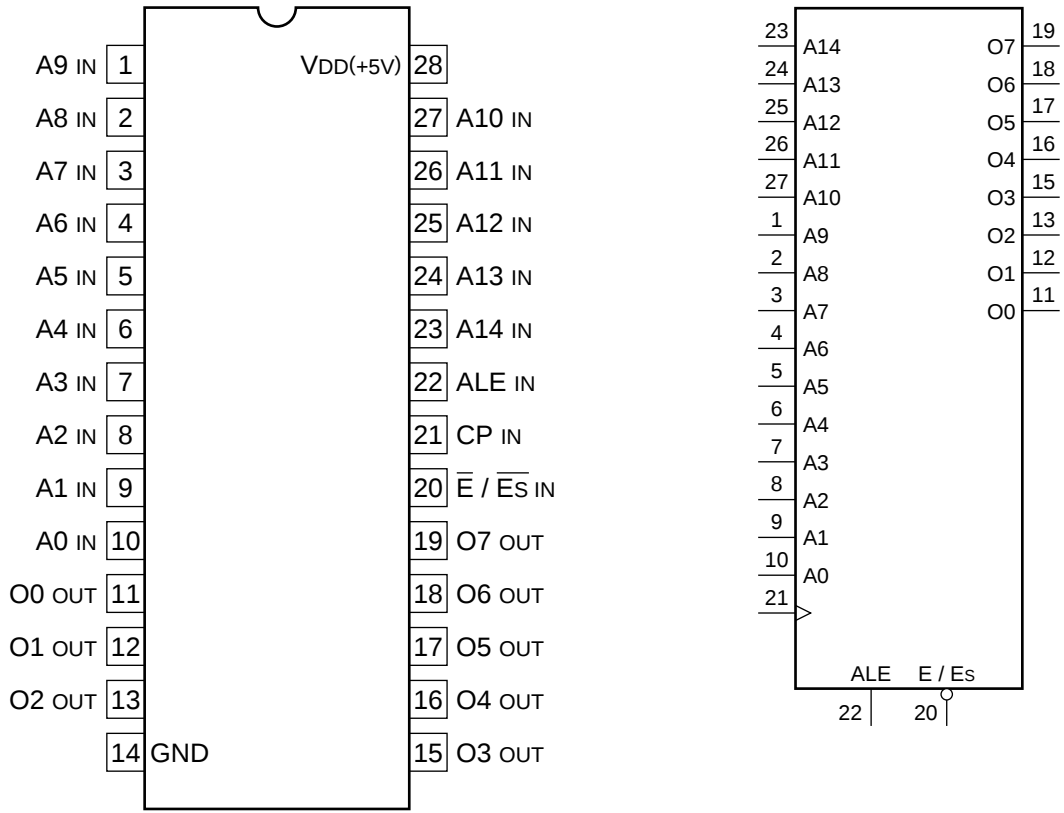
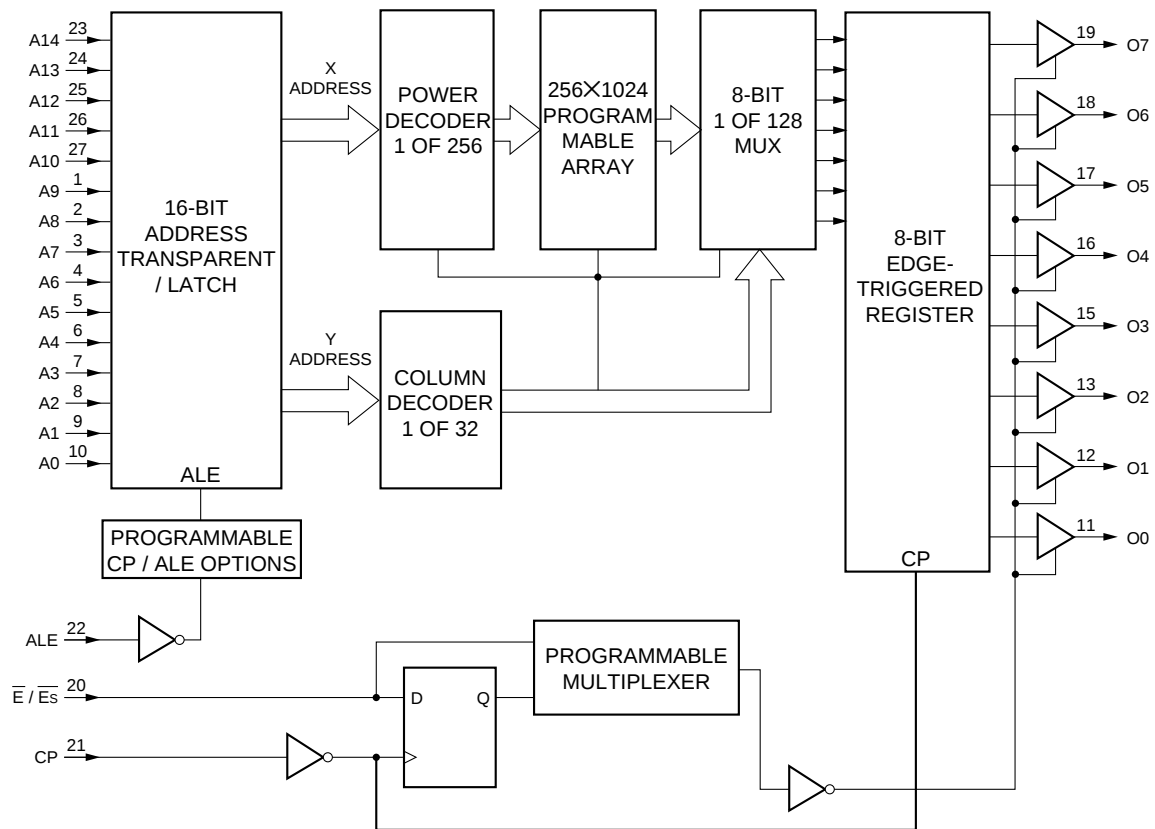


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C-MOS 256K (32K × 8)-BIT REGISTERED PROM  
-TOP VIEW-



- A0 - A14 ; ADDRESS INPUTS
- ALE ; ADDRESS LATCH ENABLE INPUT
- CP ; CLOCK PULSE INPUT
- O0 - O7 ; DATA OUTPUTS
- E / Es ; CHIP ENABLE INPUT



\*ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.