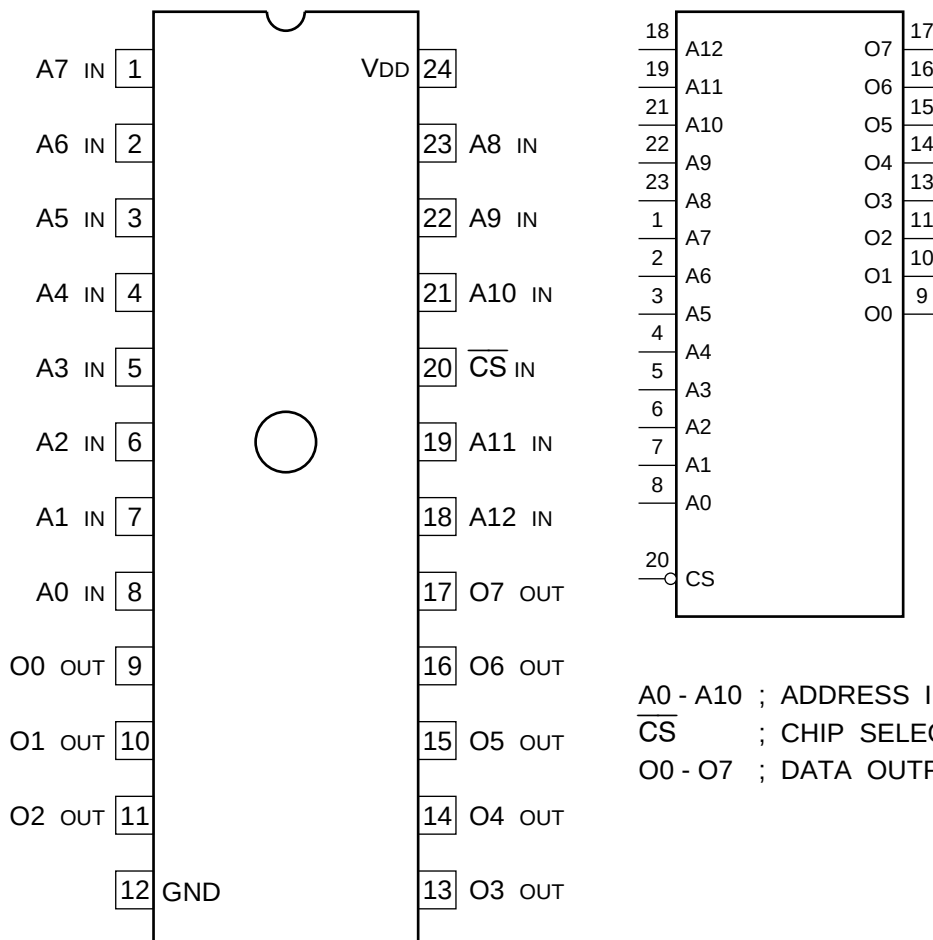


C-MOS 64 K (8,192 × 8)-BIT PROM

—TOP VIEW—



A0 - A10 ; ADDRESS INPUTS
 $\overline{\text{CS}}$; CHIP SELECT INPUT
 O0 - O7 ; DATA OUTPUTS

