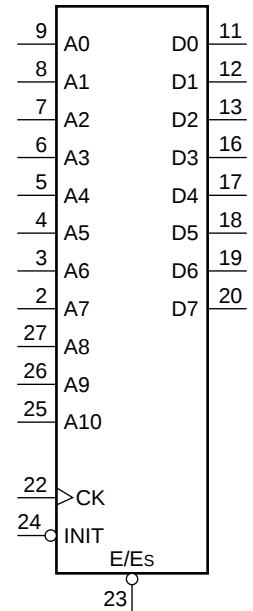
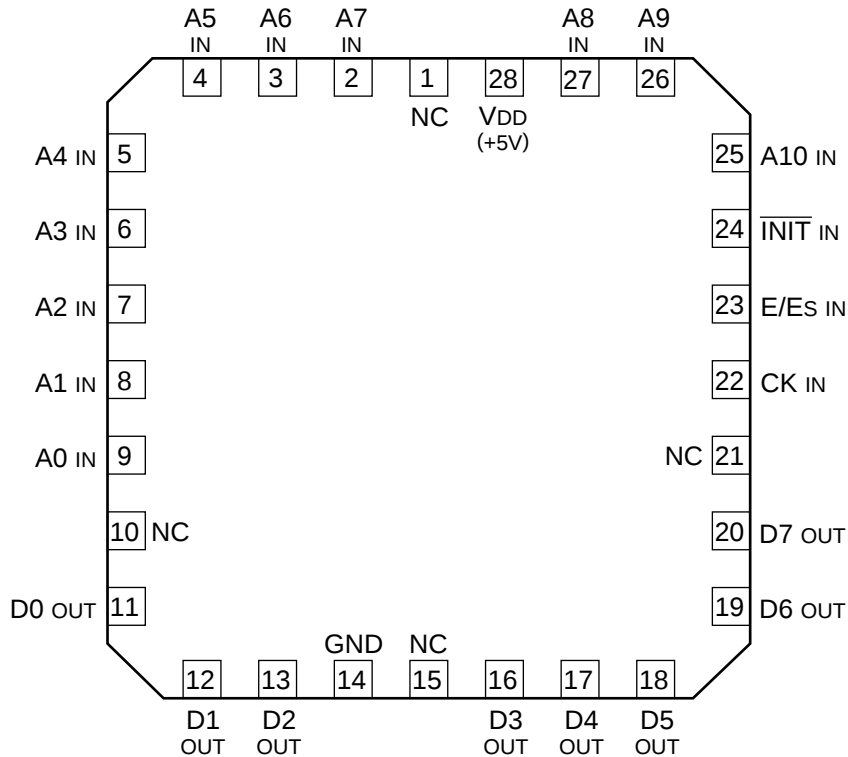

C-MOS 16K (2048 × 8)-BIT EPROM -TOP VIEW-



A0 - A10 ; ADDRESS INPUTS
CK ; CLOCK INPUT
E ; SYNCHRONOUS ENABLE INPUT
ES ; ASYNCHRONOUS ENABLE INPUT
INIT ; ASYNCHRONOUS INITIALIZATION
D0 - D7 ; DATA OUTPUT

A1	A2	CK	E/ES	INIT	OUTPUTS	FUNCTION
X	X	X	0	1	DATA OUT	READ
X	X	X	1	1	HI-Z	OUTPUT DISABLE
X	X	0	1	V _{PP}	DATA IN	PGM
X	X	1	0	V _{PP}	DATA OUT	PGM VERIFY
X	X	1	1	V _{PP}	HI-Z	PGM INH
X	X	0	1	V _{PP}	DATA IN	INTELLIGENT PGM
V _{PP}	1	0	1	V _{PP}	HI-Z	PGM SYNCH ENABLE
V _{PP}	0	0	1	V _{PP}	DATA IN	PGM INITIAL BYTE
X	X	V _{PP}	0	0	ONES	BLANK CHECK ONES
X	X	V _{PP}	1	0	ZEROS	BLANK CHECK ZEROS

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE
V_{PP} ; PROGRAM POWER SUPPLY
(+13V to +14V)

