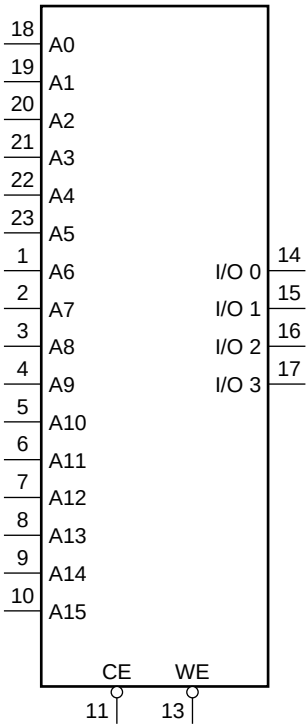
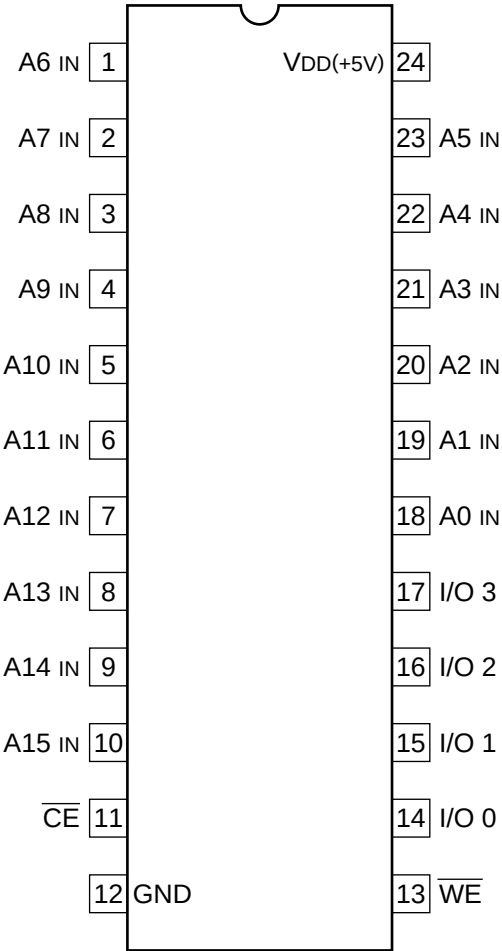

C-MOS 256K (65,536 × 4)-BIT STATIC READ / WRITE RAM

-TOP VIEW-



CE	WE	I/O	MODE
1	X	HI-Z	DESELECT / POWER-DOWN
0	1	DATA OUT	READ
0	0	DATA IN	WRITE

A0 - A15 ; ADDRESS INPUTS
CE ; CHIP ENABLE INPUT
I/O0 - I/O3 ; DATA INPUTS / OUTPUTS
WE ; WRITE ENABLE INPUT

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE

