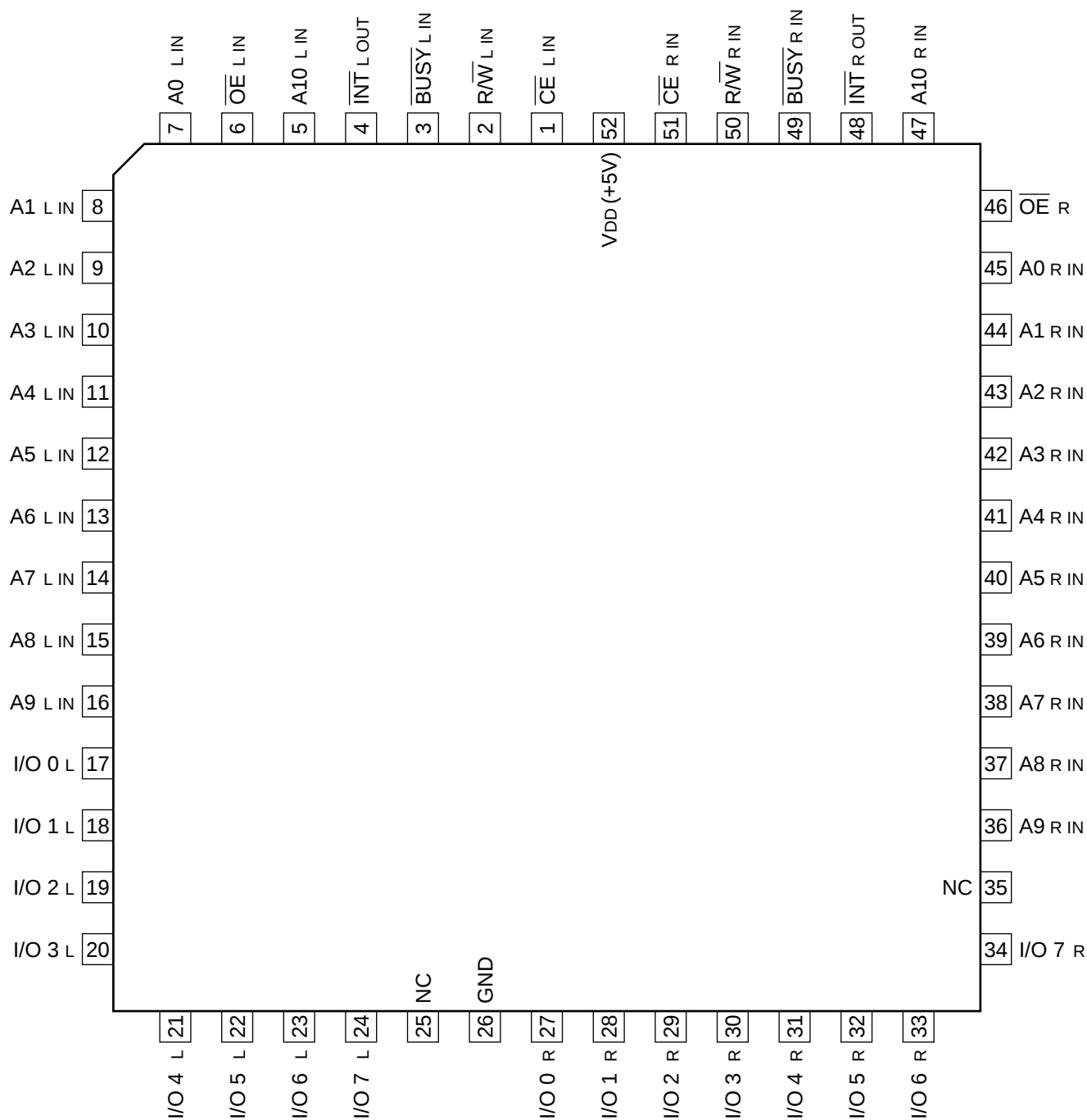
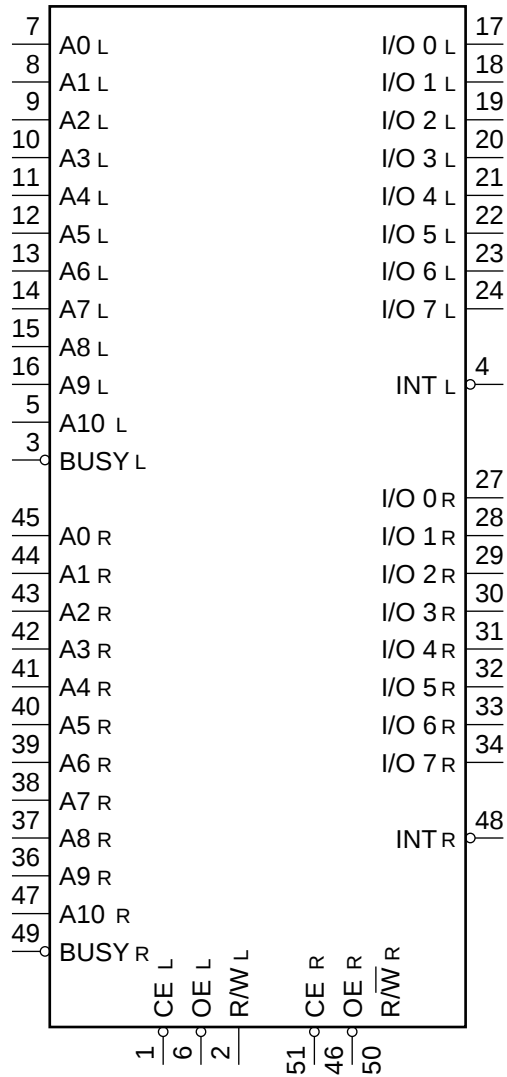

C-MOS 16K (2048 x 8)-BIT STATIC RAM

- TOP VIEW -





A0 L/R - A10 L/R ; ADDRESS INPUTS
 $\overline{\text{BUSY}}$ L/R ; BUSY INPUT
 $\overline{\text{CE}}$ L/R ; CHIP ENABLE INPUT
 $\overline{\text{INT}}$ L/R ; INTERRUPT OUTPUT
 I/O 0 L/R - I/O 7 L/R ; DATA INPUTS / OUTPUTS
 $\overline{\text{OE}}$ L/R ; OUTPUT ENABLE INPUT
 $\text{R}/\overline{\text{W}}$ L/R ; READ / WRITE INPUT

