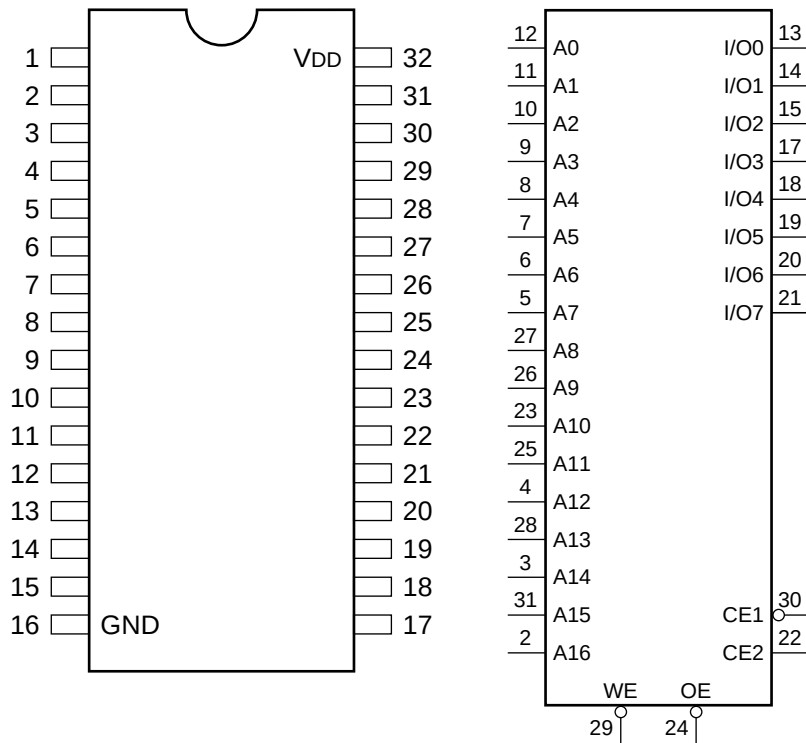


C-MOS (128 K × 8)-BIT SRAM

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	NC	17	I/O	I/O3
2	I	A16	18	I/O	I/O4
3	I	A14	19	I/O	I/O5
4	I	A12	20	I/O	I/O6
5	I	A7	21	I/O	I/O7
6	I	A6	22	I	CE2
7	I	A5	23	I	A10
8	I	A4	24	I	OE
9	I	A3	25	I	A11
10	I	A2	26	I	A9
11	I	A1	27	I	A8
12	I	A0	28	I	A13
13	I/O	I/O0	29	I	WE
14	I/O	I/O1	30	I	CE1
15	I/O	I/O2	31	I	A15
16	—	GND	32	—	VDD

INPUT

A0 - A16 : ADDRESS

CE1, CE2 : CHIP ENABLE

OE : OUTPUT ENABLE

WE : WRITE ENABLE

INPUT/OUTPUT

I/O0 - I/O7 : DATA