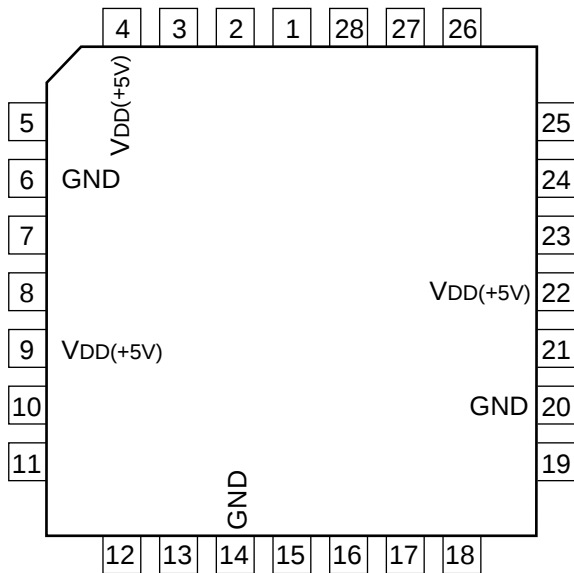
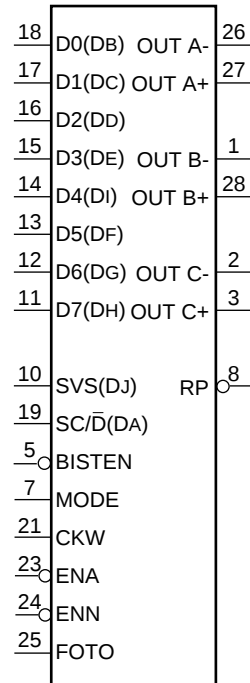

BiC-MOS TRANSMITTER

-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	OUT B	15	I	D3(DE)
2	O	OUT C	16	I	D2(DD)
3	O	OUT C	17	I	D1(DC)
4	—	VDD	18	I	D0(DB)
5	I	$\overline{\text{BISTEN}}$	19	I	SC/ $\overline{\text{D}}$ (DA)
6	—	GND	20	—	GND
7	I	MODE	21	I	CKW
8	O	RP	22	—	VDD
9	—	VDD	23	I	$\overline{\text{ENA}}$
10	I	SVS(DJ)	24	I	$\overline{\text{ENN}}$
11	I	D7(DH)	25	I	FOTO
12	I	D6(DG)	26	O	OUT A
13	I	D5(DF)	27	O	OUT A
14	I	D4(DI)	28	O	OUT B

**INPUT**

BISTEN	; BUILT-IN SELF TEST ENABLE
CKW	; CLOCK WRITE
D0 - D7	; PARALLEL DATA
ENA	; ENABLE PARALLEL DATA
ENN	; ENABLE NEXT PARALLEL DATA
FOTO	; FIBER OPTICAL TRANSMITTER OFF
MODE	; ENCODER MODE SELECT
SC/D(DA)	; SPECIAL CHARACTER/DATA SELECT
SVS(DJ)	; SEND VIOLATION SYMBOL

OUTPUT

OUT A+, OUT A	; DIFFERENTIAL SERIAL DATA
OUT B+, OUT B	; DIFFERENTIAL SERIAL DATA
OUT C+, OUT C	; DIFFERENTIAL SERIAL DATA
RP	; READ PULSE

