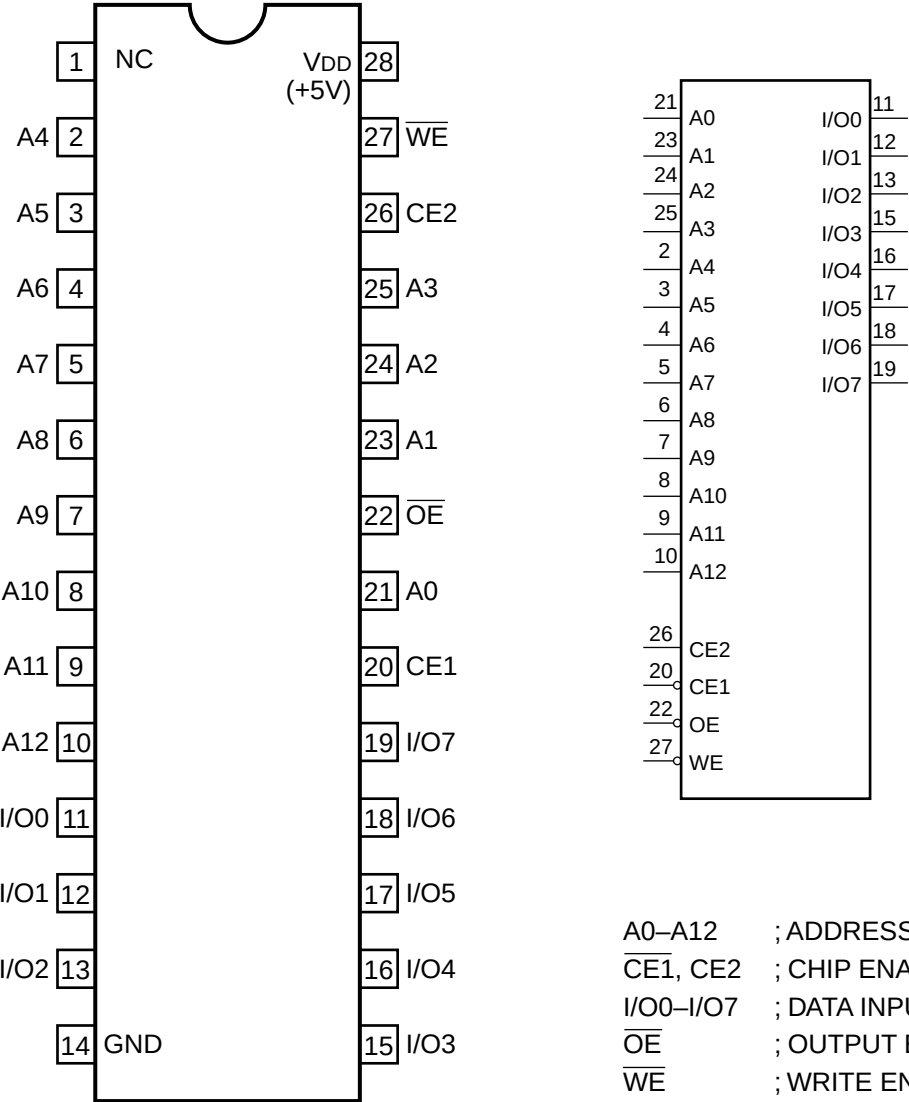


C-MOS 64k (8k × 8) BIT STATIC
-TOP VIEW-



A0-A12 ; ADDRESS INPUTS
CE1, CE2 ; CHIP ENABLE INPUTS
I/O0-I/O7 ; DATA INPUTS/OUTPUTS
OE ; OUTPUT ENABLE INPUT
WE ; WRITE ENABLE INPUT

(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	8	I	A10	15	I/O	I/O3	22	I	OE
2	I	A4	9	I	A11	16	I/O	I/O4	23	I	A1
3	I	A5	10	I	A12	17	I/O	I/O5	24	I	A2
4	I	A6	11	I/O	I/O0	18	I/O	I/O6	25	I	A3
5	I	A7	12	I/O	I/O1	19	I/O	I/O7	26	I	CE2
6	I	A8	13	I/O	I/O2	20	I	CE1	27	I	WE
7	I	A9	14	—	GND	21	I	A0	28	—	VDD

