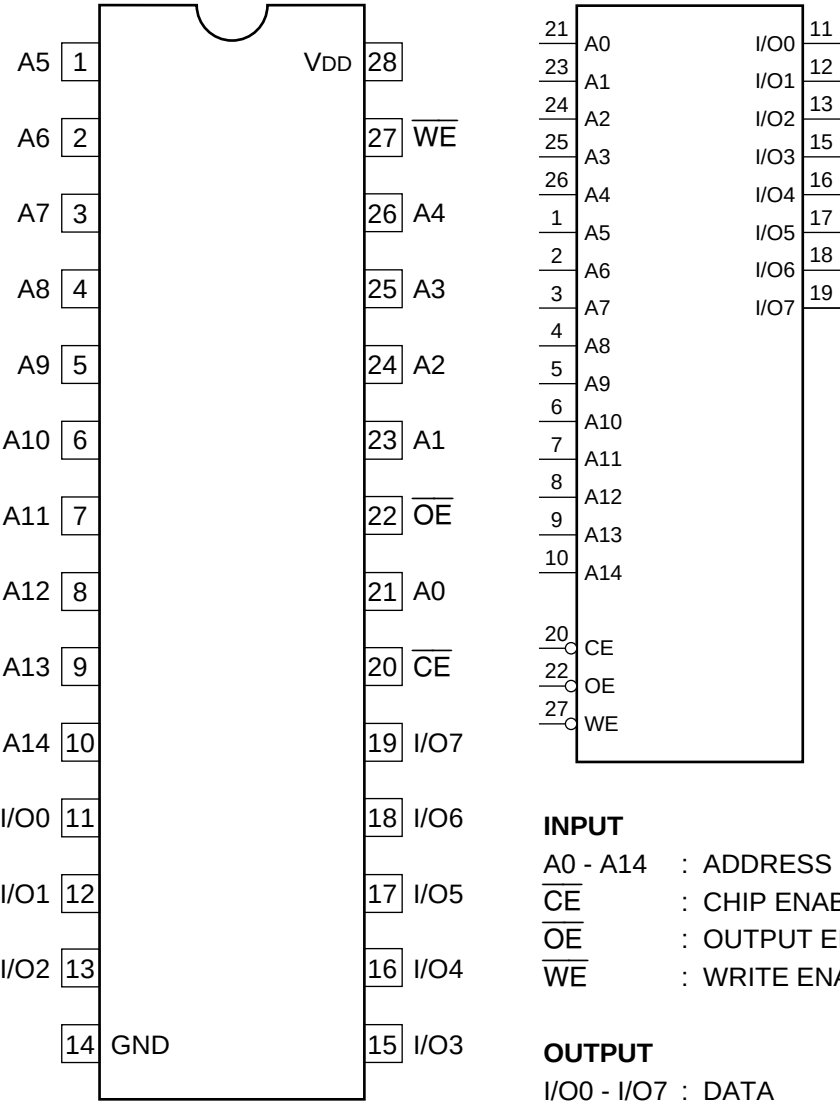


C-MOS 256 K (32 K × 8)-BIT SRAM
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	A5	8	I	A12	15	I/O	I/O3	22	I	$\overline{OE}$
2	I	A6	9	I	A13	16	I/O	I/O4	23	I	A1
3	I	A7	10	I	A14	17	I/O	I/O5	24	I	A2
4	I	A8	11	I/O	I/O0	18	I/O	I/O6	25	I	A3
5	I	A9	12	I/O	I/O1	19	I/O	I/O7	26	I	A4
6	I	A10	13	I/O	I/O2	20	I	$\overline{CE}$	27	I	$\overline{WE}$
7	I	A11	14	—	GND	21	I	A0	28	—	VDD

