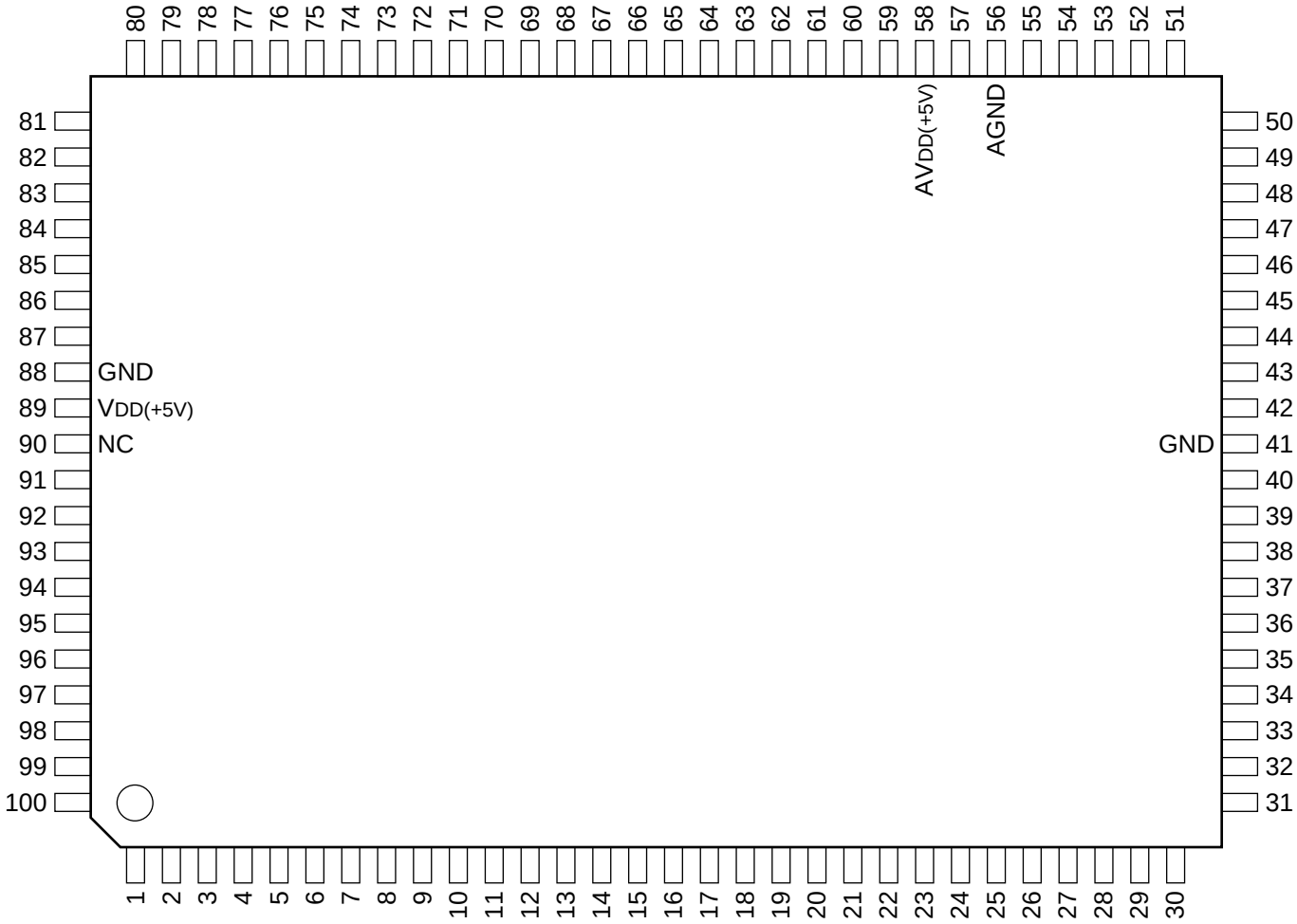
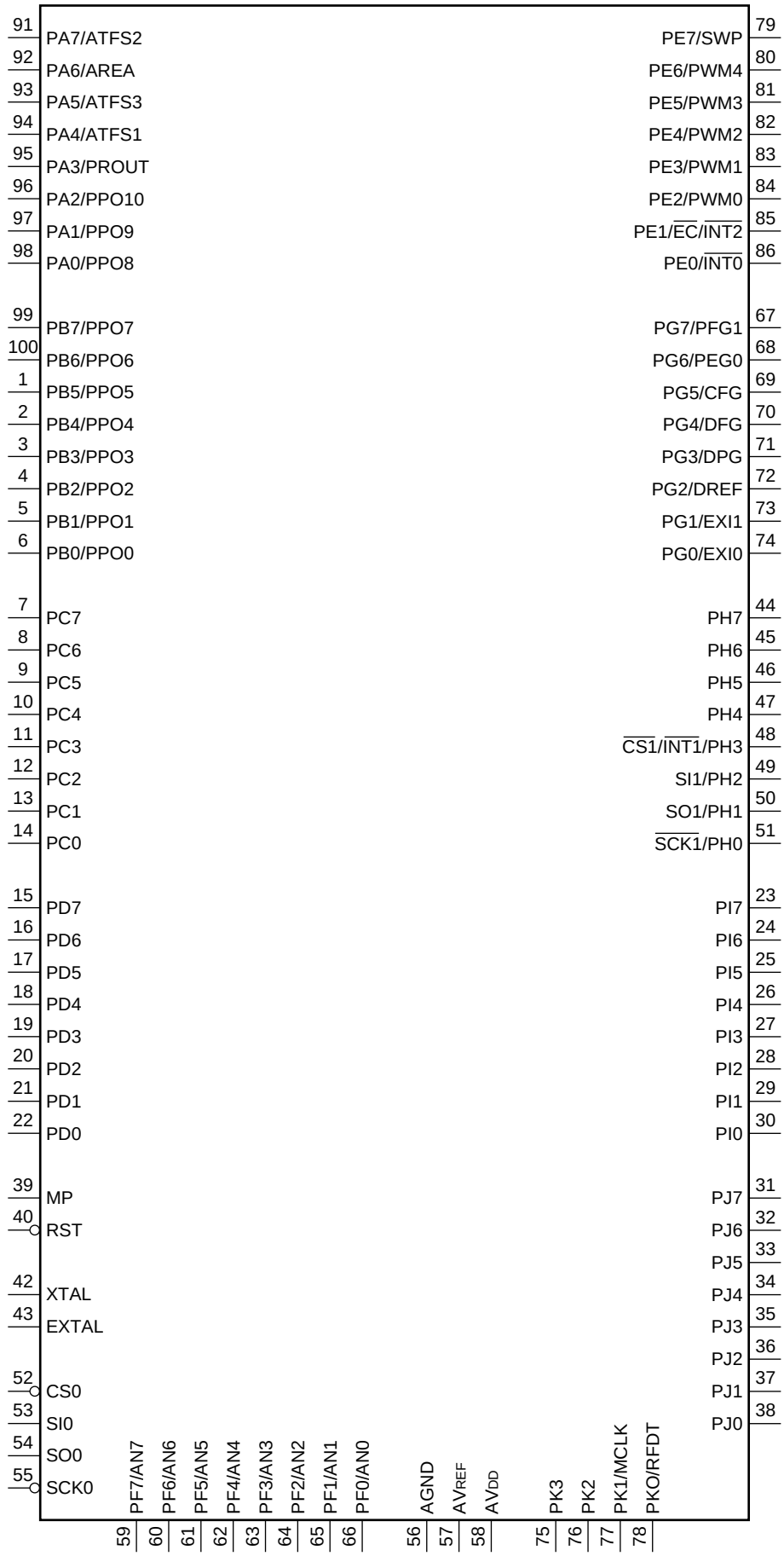
C-MOS 8-BIT MICROCOMPUTER

–TOP VIEW–




91 PA7/ATFS2
92 PA6/AREA
93 PA5/ATFS3
94 PA4/ATFS1
95 PA3/PROUT
96 PA2/PPO10
97 PA1/PPO9
98 PA0/PPO8
99 PB7/PPO7
100 PB6/PPO6
1 PB5/PPO5
2 PB4/PPO4
3 PB3/PPO3
4 PB2/PPO2
5 PB1/PPO1
6 PB0/PPO0
7 PC7
8 PC6
9 PC5
10 PC4
11 PC3
12 PC2
13 PC1
14 PC0
15 PD7
16 PD6
17 PD5
18 PD4
19 PD3
20 PD2
21 PD1
22 PD0
39 MP
40 RST
42 XTAL
43 EXTAL
52 CS0
53 SI0
54 SO0
55 SCK0
59 PF7/AN7
60 PF6/AN6
61 PF5/AN5
62 PF4/AN4
63 PF3/AN3
64 PF2/AN2
65 PF1/AN1
66 PF0/AN0
56 AGND
57 AVREF
58 AVDD
75 PK3
76 PK2
77 PK1/MCLK
78 PKO/RFDT
79 PE7/SWP
80 PE6/PWM4
81 PE5/PWM3
82 PE4/PWM2
83 PE3/PWM1
84 PE2/PWM0
85 PE1/EC/INT2
86 PE0/INT0
67 PG7/PFG1
68 PG6/PEG0
69 PG5/CFG
70 PG4/DFG
71 PG3/DPG
72 PG2/DREF
73 PG1/EXI1
74 PG0/EXI0
44 PH7
45 PH6
46 PH5
47 PH4
48 CS1/INT1/PH3
49 SI1/PH2
50 SO1/PH1
51 SCK1/PH0
23 PI7
24 PI6
25 PI5
26 PI4
27 PI3
28 PI2
29 PI1
30 PI0
31 PJ7
32 PJ6
33 PJ5
34 PJ4
35 PJ3
36 PJ2
37 PJ1
38 PJ0

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	PB5/PPO5	26	I/O	PI4	51	I/O	$\overline{SCK1}$/PH0	76	I/O	PK2
2	O	PB4/PPO4	27	I/O	PI3	52	I	$\overline{CS0}$	77	I/O	PK1/MCLK
3	O	PB3/PPO3	28	I/O	PI2	53	I	SI0	78	I/O	PK0/RFDT
4	O	PB2/PPO2	29	I/O	PI1	54	O	SO0	79	O	PE7/SWP
5	O	PB1/PPO1	30	I/O	PI0	55	I/O	$\overline{SCK0}$	80	O	PE6/PWM4
6	O	PB0/PPO0	31	O	PJ7	56	—	AGND	81	O	PE5/PWM3
7	I/O	PC7	32	O	PJ6	57	—	AV_{REF}	82	O	PE4/PWM2
8	I/O	PC6	33	O	PJ5	58	—	AV_{DD}	83	O	PE3/PWM1
9	I/O	PC5	34	O	PJ4	59	I	PF7/AN7	84	O	PE2/PWM0
10	I/O	PC4	35	O	PJ3	60	I	PF6/AN6	85	I	PE1/$\overline{EC}$/$\overline{INT2}$
11	I/O	PC3	36	O	PJ2	61	I	PF5/AN5	86	I	PE0/$\overline{INT0}$
12	I/O	PC2	37	O	PJ1	62	I	PF4/AN4	87	I	$\overline{NMI}$
13	I/O	PC1	38	O	PJ0	63	I	PF3/AN3	88	—	GND
14	I/O	PC0	39	I	MP	64	I	PF2/AN2	89	—	V_{DD}
15	I/O	PD7	40	I/O	$\overline{RST}$	65	I	PF1/AN1	90	—	NC
16	I/O	PD6	41	—	GND	66	I	PF0/AN0	91	O	PA7/ATFS2
17	I/O	PD5	42	—	XTAL	67	I	PG7/PFG1	92	O	PA6/AREA
18	I/O	PD4	43	I	EXTAL	68	I	PG6/PFG0	93	O	PA5/ATFS3
19	I/O	PD3	44	O	PH7	69	I	PG5/CFG	94	O	PA4/ATFS1
20	I/O	PD2	45	O	PH6	70	I	PG4/DFG	95	O	PA3/PROUT
21	I/O	PD1	46	O	PH5	71	I	PG3/DPG	96	O	PA2/PPO10
22	I/O	PD0	47	I/O	PH4	72	I	PG2/DREF	97	O	PA1/PPO9
23	I/O	PI7	48	I	$\overline{OS1}$/$\overline{INT1}$/PH3	73	I	PG1/EXI1	98	O	PA0/PPO8
24	I/O	PI6	49	I	SI0/PH2	74	I	PG0/EXI0	99	O	PB7/PPO7
25	I/O	PI5	50	I/O	SO1/PH1	75	I/O	PK3	100	O	PB6/PPO6

INPUT

AN0 - AN7	;	A - D CONVERTOR'S ANALOG INPUTS
CFG	;	CAPSTAN FG INPUT
$\overline{CS0}$	;	SERIAL INTERFACE CHIP SELECT INPUT
RFG0, RFG1	;	REEL FG INPUTS
DPG, DFG	;	DURM PG AND FG INPUTS
DREF	;	DRUM REF, SIGNAL INPUT
$\overline{EC}$	;	EVENT CONTROL INPUT FOR TIMER/COUNTER
EXI0, EXI1	;	EXTERNAL INPUTS
EXTAL	;	CRYSTAL CONNECTION OR EXTERNAL CLOCK INPUT FOR SYSTEM CLOCK GENERATOR
$\overline{INT0}$ - $\overline{INT2}$	;	INTERRUPT REQUEST INPUTS (FALLING EDGE ACTIVE)
MCLK	;	CHANNEL CLOCK INPUT
$\overline{MP}$	;	TEST MODE INPUT
$\overline{NMI}$	;	NON-MASKABLE INTERRUPT REQUEST
RFDT	;	PLAY BACK DATA INPUT
PE0, PE1	;	INPUTS OF PORT E (PE2 - PE7 ; OUTPUTS)
PF0 - PF7	;	INPUTS OF PORT F
PG0 - PG7	;	INPUTS OF PORT G
PH0 - PH3	;	INPUTS OF PORT H (PH4 - PH7 ; OUTPUTS)
SI0, SI1	;	SERIAL DATA INPUTS

OUTPUT

ATFS1 - ATFS3, AREA	;	MONITOR OUTPUTS FOR ATF
PA0 - PA7	;	OUTPUTS OF PORT A
PB0 - PB7	;	OUTPUTS OF PORT B
PE2 - PE7	;	OUTPUTS OF PORT E (PE0, PE1 ; INPUTS)
PH4 - PH7	;	OUTPUTS OF PORT H (N-CH OPEN DRAIN OUTPUTS)
PPO00 - PPO10, PROUT	;	PROGRAMMABLE PATTERN GENERATOR OUTPUTS
PWM0 - PWM4	;	PULSE WIDTH MODULATION OUTPUT
SO0, SO1	;	SERIAL DATA OUTPUTS
SWP	;	SWP OUTPUT
XTAL	;	CRYSTAL CONNECTION
PK0 - PK3	;	INPUTS/OUTPUTS OF PORT K

INPUT/OUTPUT

PC0 - PC7	;	INPUTS/OUTPUTS OF PORT C
PD0 - PD7	;	INPUTS/OUTPUTS OF PORT D
PI0 - PI7	;	INPUTS/OUTPUTS OF PORT I
PJ0 - PJ7	;	INPUTS/OUTPUTS OF PORT J
$\overline{RST}$	;	SYSTEM RESET INPUT AND POWER ON RESET OUTPUT
$\overline{SCK0}$, $\overline{SCK1}$	;	SERIAL CLOCK INPUTS/OUTPUTS

OTHER

AGND	;	A - D CONVERTOR'S GROUND
AV_{DD} (+5V)	;	A - D CONVERTOR'S V_{DD}
AV_{REF}	;	A - D CONVERTOR'S REFERENCE VOLTAGE INPUT