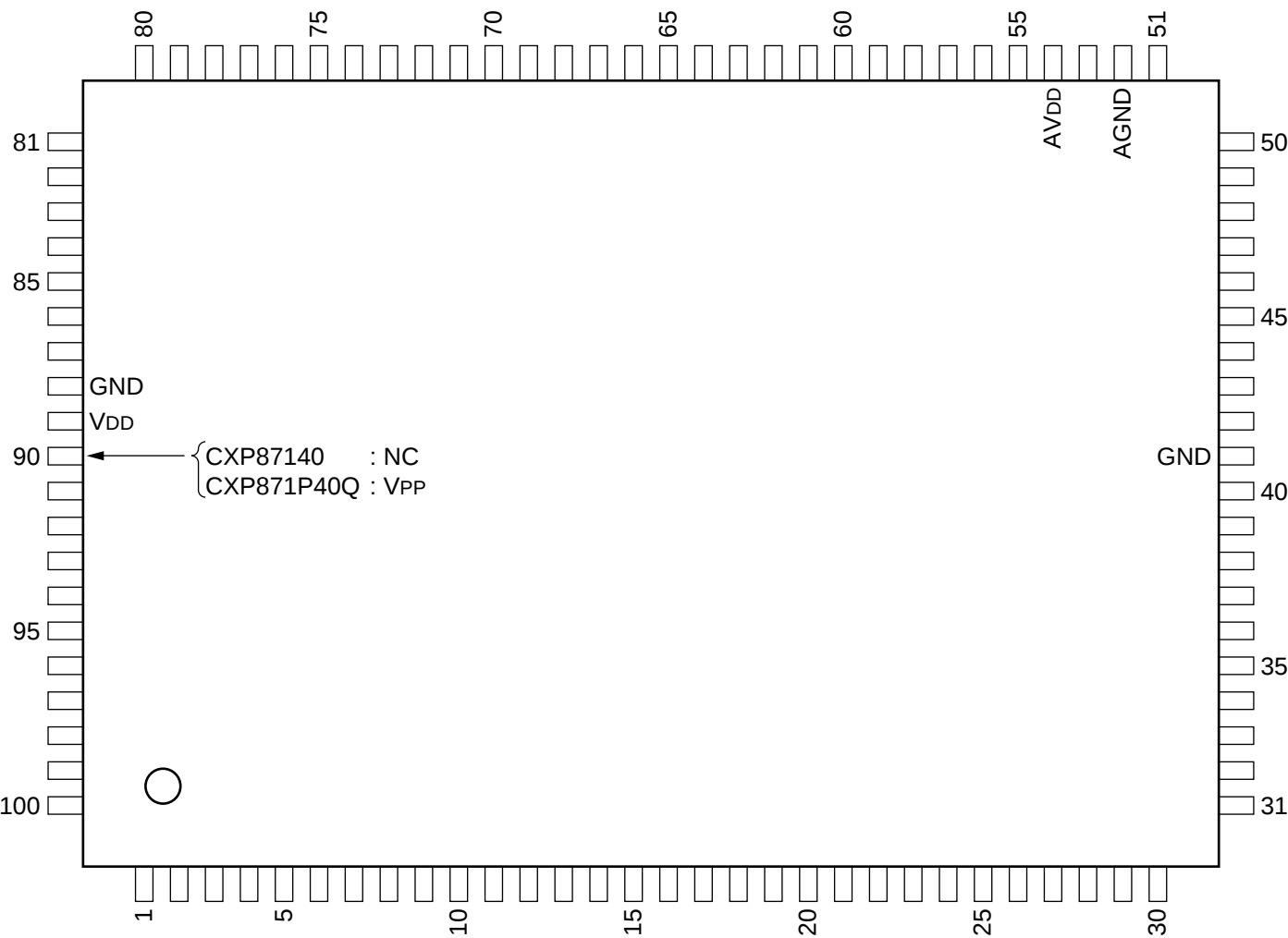


C-MOS 8-BIT MICROCOMPUTER

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	PB5/PPO013/PPO113	26	I/O	PD4	51	I/O	PF4/AN8	76	O	PE2/PWM0
2	O	PB4/PPO012/PPO112	27	I/O	PD3	52	—	AGND	77	I	PE1/EC/INT2
3	O	PB3/PPO011	28	I/O	PD2	53	—	AVREF	78	I	PE0/INT0/XOUT
4	O	PB2/PPO010	29	I/O	PD1	54	—	AVDD	79	I/O	PI7/SI1
5	O	PB1/PPO009	30	I/O	PD0	55	I	PF3/AN7	80	I/O	PI6/SO1
6	O	PB0/PPO008	31	O	PH7	56	I	PF2/AN6	81	I/O	PI5/SCK1
7	I/O	PC7/RTO7	32	O	PH6	57	I	PF1/AN5	82	I/O	PI4/INT1
8	I/O	PC6/RTO6	33	O	PH5	58	I	PF0/AN4	83	I/O	PI3/TO
9	I/O	PC5/RTO5	34	O	PH4	59	I	AN3	84	I/O	PI2/PWM
10	I/O	PC4/RTO4	35	O	PH3	60	I	AN2	85	I/O	PI1/P0
11	I/O	PC3/RTO3	36	O	PH2	61	I	AN1	86	I	PI0/PCK/OSC1
12	I/O	PC2/PPO018	37	O	PH1	62	I	AN0	87	I	PK0/OSC0
13	I/O	PC1/PPO017	38	O	PH0	63	I	PG7/EXI1/PMSK	88	—	GND
14	I/O	PC0/PPO016	39	I	MP	64	I	PG6/EXI0	89	—	VDD
15	I/O	PJ7	40	I/O	RST	65	I	PG5/SYNC1	90	—	* below
16	I/O	PJ6	41	—	GND	66	I	PG4/SYNC0/PMI	91	O	PA7/PPO007/PPO107
17	I/O	PJ5	42	—	XTAL	67	I	PG3/PBCTL	92	O	PA6/PPO006/PPO106
18	I/O	PJ4	43	I	EXTAL	68	I	PG2/DPG	93	O	PA5/PPO005/PPO105
19	I/O	PJ3	44	I	CS0	69	I	PG1/DFG	94	O	PA4/PPO004/PPO104
20	I/O	PJ2	45	I	SI0	70	I	PG0/CFG	95	O	PA3/PPO003/PPO103
21	I/O	PJ1	46	O	SO0	71	O	PE7/DAB1	96	O	PA2/PPO002/PPO102
22	I/O	PJ0	47	I/O	SCK0	72	O	PE6/DAB0	97	O	PA1/PPO001/PPO101
23	I/O	PD7	48	I/O	PF7/AN11	73	O	PE5/DAA1	98	O	PA0/PPO000/PPO100
24	I/O	PD6	49	I/O	PF6/AN10	74	O	PE4/DAA0	99	O	PB7/PPO015
25	I/O	PD5	50	I/O	PF5/AN9	75	O	PE3/PWM1	100	O	PB6/PPO014

* CXP87140 : NC
CXP871P40Q : VPP

INPUT

AN0 - AN11	; A-D CONVERTER'S ANALOG
CFG	; CAPSTAN FG
CS0	; SERIAL INTERFACE CHANNEL 0 SELECT
DPG, DFG	; DRUM PG AND FG
EC	; EVENT CONTROL FOR TIMER/COUNTER
EXI0, EXI1	; FRC CAPTURE UNIT'S EXTERNAL
EXTAL	; CRYSTAL CONNECTION OR EXTERNAL CLOCK FOR SYSTEM CLOCK GENERATOR
INT0 - INT2	; INTERRUPT REQUEST (FALLING EDGE ACTIVE)
MP	; MICROPROCESSOR MODE
PBCTL	; PLAYBACK CTL PULSE
PCK	; PRESCALER'S EXTERNAL CLOCK
PE0, PE1	; PORT E (PE2 - PE7 ; OUTPUTS)
PF0 - PF3	; PORT F (PF4 - PF7 ; OUTPUTS)
PG0 - PG7	; PORT G
PI0	; PORT I (PI1 - PI7 ; INPUTS/OUTPUTS)
PK0	; PORT K
PMI	; INSTRUMENTATION PULSE OF PULSE MEASURE UNIT
PMSK	; INSTRUMENTATION ENABLE OF PULSE MEASURE UNIT
SI0, SI1	; SERIAL DATA (CHANNEL 0 AND CHANNEL 1)
SYNC0, SYNC1	; COMPOSITE SYNC SIGNAL

OUTPUT

DAA0, DAB0	; DA GATE PULSE (12 BITS PWM CHANNEL 0)
DAA1, DAB1	; DA GATE PULSE (12 BITS PWM CHANNEL 1)
PA0 - PA7	; PORT A
PB0 - PB7	; PORT B
PE2 - PE7	; PORT E (PE0, PE1 ; INPUTS)
PF4 - PF7	; PORT F (PF0 - PF3 ; INPUTS)
PH0 - PH7	; PORT H (N-CH OPEN DRAIN OUTPUTS)
PPO000 - PPO018, PPO100 - PPO107, PPO112, PPO113	; PROGRAMMABLE PATTERN GENERATOR
PO	; PRESCALER
PWM	; PULSE WIDTH MODULATION
PWM0, PWM1	; 12 BITS PULSE WIDTH MODULATION (CHANNEL 0 AND CHANNEL 1)
RTO3 - RTO7	; REALTIME PULSE GENERATOR
SO0, SO1	; SERIAL DATA (CHANNEL 0 AND CHANNEL 1)
TO	; TIME/COUNTER (DUTY 50%)
XTAL	; CRYSTAL CONNECTION

INPUT/OUTPUT

PC0 - PC7	; PORT C (BIT PROGRAMMABLE)
PD0 - PD7	; PORT D (4 BITS PROGRAMMABLE)
PI1 - PI7	; PORT I (BIT PROGRAMMABLE, PI0 ; INPUT)
PJ0 - PJ7	; PORT J (BIT PROGRAMMABLE)
RST	; SYSTEM RESET INPUT AND POWER ON RESET OUTPUT
SCK0, SCK1	; SERIAL CLOCK (CHANNEL 0 AND CHANNEL 1)

OTHER

AGND	; A-D CONVERTER'S GROUND
AVDD	; A-D CONVERTER'S V _{DD}
AVREF	; A-D CONVERTER'S REFERENCE VOLTAGE INPUT

