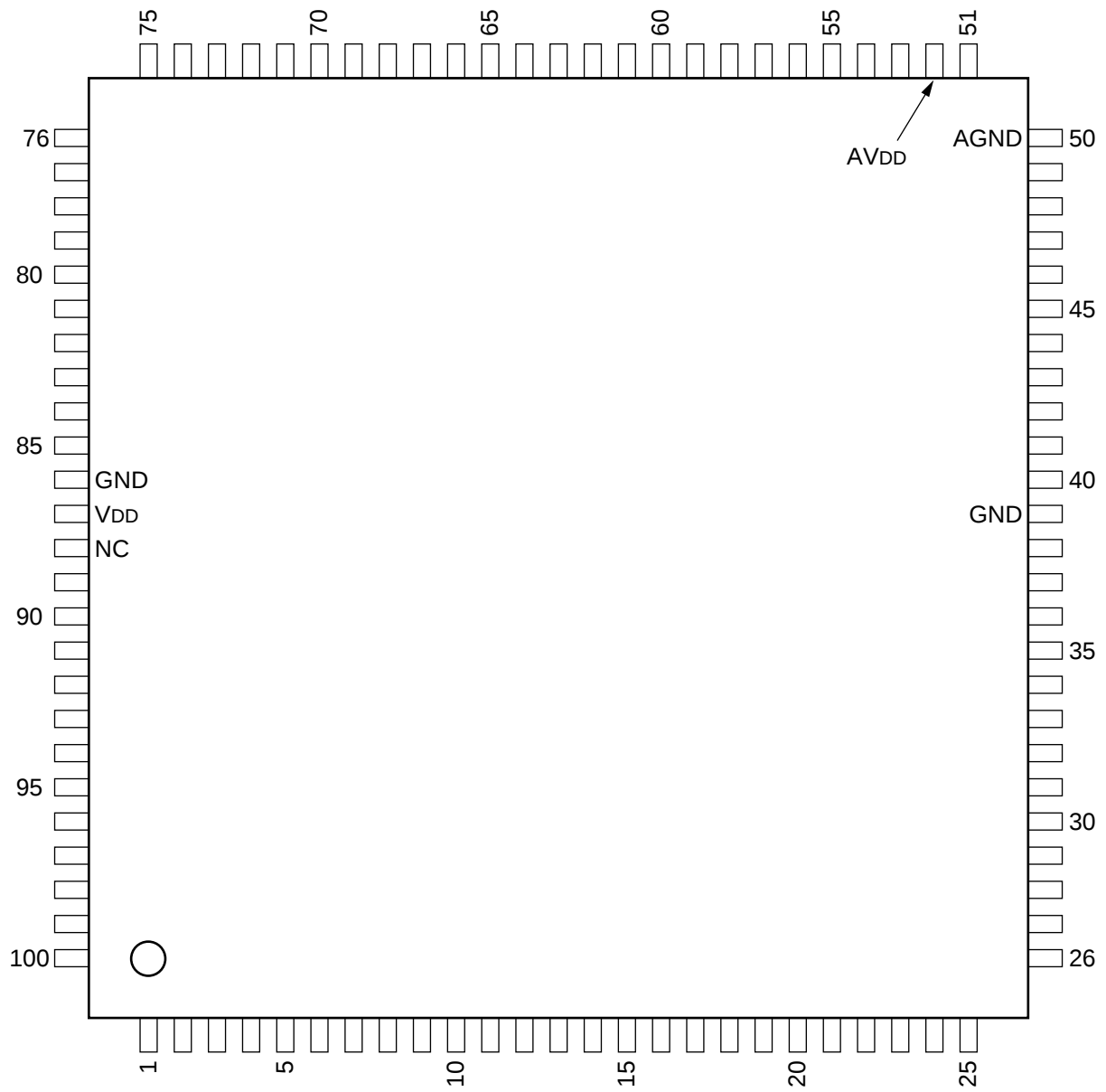


C-MOS 8-BIT 1-CHIP MICROCOMPUTER

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	PB3/PPO011	26	I/O	PD2	51	—	AVREF	76	I/O	PE0/INT0/XOUT
2	O	PB2/PPO010	27	I/O	PD1	52	—	AVDD	77	I/O	PI7/SI1
3	O	PB1/PPO009	28	I/O	PD0	53	I	PF3/AN7	78	I/O	PI6/SO1
4	O	PB0/PPO008	29	O	PH7	54	I	PF2/AN6	79	I/O	PI5/SCK1
5	I/O	PC7/RTO7	30	O	PH6	55	I	PF1/AN5	80	I/O	PI4/INT1
6	I/O	PC6/RTO6	31	O	PH5	56	I	PF0/AN4	81	I/O	PI3/TO
7	I/O	PC5/RTO5	32	O	PH4	57	I	AN3	82	I/O	PI2/PWM
8	I/O	PC4/RTO4	33	O	PH3	58	I	AN2	83	I/O	PI1/PO
9	I/O	PC3/RTO3	34	O	PH2	59	I	AN1	84	I	PIO/PCK/OSCI
10	I/O	PC2/PPO018	35	O	PH1	60	I	AN0	85	I/O	PK0/OSCO
11	I/O	PC1/PPO017	36	O	PH0	61	I	PG7/EXI1/PMSK	86	—	GND
12	I/O	PC0/PPO016	37	I	MP	62	I	PG6/EXI0	87	—	VDD
13	I/O	PJ7	38	I/O	RST	63	I	PG5/SYNC1	88	—	NC
14	I/O	PJ6	39	—	GND	64	I	PG4/SYNC0/PMI	89	O	PA7/PPO007/PPO107
15	I/O	PJ5	40	—	XTAL	65	I	PG3/PBCTL	90	O	PA6/PPO006/PPO106
16	I/O	PJ4	41	I	EXTAL	66	I	PG2/DPG	91	O	PA5/PPO005/PPO105
17	I/O	PJ3	42	I	CS0	67	I	PG1/DFG	92	O	PA4/PPO004/PPO104
18	I/O	PJ2	43	I	SI0	68	I	PG0/CFG	93	O	PA3/PPO003/PPO103
19	I/O	PJ1	44	O	SO0	69	O	PE7/DAB1	94	O	PA2/PPO002/PPO102
20	I/O	PJ0	45	I/O	SCK0	70	O	PE6/DAB0	95	O	PA1/PPO001/PPO101
21	I/O	PD7	46	I/O	PF7/AN11	71	O	PE5/DAA1	96	O	PA0/PPO000/PPO100
22	I/O	PD6	47	I/O	PF6/AN10	72	O	PE4/DAA0	97	O	PB7/PPO015
23	I/O	PD5	48	I/O	PF5/AN9	73	O	PE3/PWM1	98	O	PB6/PPO014
24	I/O	PD4	49	I/O	PF4/AN8	74	O	PE2/PWM0	99	O	PB5/PPO013/PPO113
25	I/O	PD3	50	—	AGND	75	I	PE1/EC/INT2	100	O	PB4/PPO012/PPO112

INPUT

AN0 - AN11	: A/D CONVERTER'S ANALOG INPUTS
CFG	: CAPSTAN FG INPUT
CS0	: SERIAL INTERFACE CHANNEL 0 SELECT INPUT
DPG, DFG	: DRUM PG AND FG INPUTS
EC	: EVENT CONTROL INPUT FOR TIMER/COUNTER
EXI0, EXI1	: FRC CAPTURE UNIT'S EXTERNAL INPUTS
EXTAL	: CRYSTAL CONNECTION OR EXTERNAL CLOCK INPUT FOR SYSTEM CLOCK GENERATOR
INT0 - INT2	: INTERRUPT REQUEST INPUTS (FALLING EDGE ACTIVE)
MP	: MICROPROCESSOR MODE INPUT
OSCI	: CRYSTAL CONNECTION OF OSCILLATION CIRCUIT FOR PRESCALER
PBCTL	: PLAYBACK CTL PULSE INPUT
PCK	: PRESCALER'S EXTERNAL CLOCK INPUT
PE0, PE1	: INPUTS OF PORT E (PE2 - PE7 ; OUTPUTS)
PF0 - PF3	: INPUTS OF PORT F (PF4 - PF7 ; OUTPUTS)
PG0 - PG7	: INPUTS OF PORT G
PI0	: INPUT OF PORT I (PI1 - PI7 ; INPUTS/OUTPUTS)
PK0	: INPUT OF PORT K
PMI	: INSTRUMENTATION PULSE INPUT OF PULSE MEASURE UNIT
PMSK	: INSTRUMENTATION ENABLE INPUT OF PULSE MEASURE UNIT
SI0, SI1	: SERIAL DATA INPUTS (CHANNEL 0 AND CHANNEL 1)
SYNC0, SYNC1	: COMPOSITE SYNC SIGNAL INPUTS

OUTPUT

DAA0, DAB0	: DA GATE PULSE OUTPUTS (12 BITS PWM CHANNEL 0)
DAA1, DAB1	: DA GATE PULSE OUTPUTS (12 BITS PWM CHANNEL 1)
OSCO	: CRYSTAL CONNECTION OF OSCILLATION CIRCUIT FOR PRESCALER
PA0 - PA7	: OUTPUTS OF PORT A
PB0 - PB7	: OUTPUTS OF PORT B
PE2 - PE7	: OUTPUTS OF PORT E (PE0, PE1 ; INPUTS)
PF4 - PF7	: OUTPUTS OF PORT F (PF0 - PF3 ; INPUTS)
PH0 - PH7	: OUTPUTS OF PORT H (N-CH OPEN DRAIN OUTPUTS)
PPO000 - PPO018	: PPO100 - PPO107, PPO112, PPO113
	: PROGRAMMABLE PATTERN GENERATOR OUTPUTS
PO	: PRESCALER OUTPUT
PWM	: PULSE WIDTH MODULATION OUTPUT
PWM0, PWM1	: 12 BITS PULSE WIDTH MODULATION OUTPUTS (CHANNEL 0 AND CHANNEL 1)
RTO3 - RTO7	: REALTIME PULSE GENERATOR OUTPUTS
SO0, SO1	: SERIAL DATA OUTPUTS (CHANNEL 0 AND CHANNEL 1)
TO	: TIMER/COUNTER OUTPUT (DUTY 50%)
XOUT	: 1/2 DIVISION FREQUENCY OUTPUT OF XTAL OR OSCO
XTAL	: CRYSTAL CONNECTION FOR SYSTEM CLOCK GENERATOR

INPUT/OUTPUT

PC0 - PC7	: INPUTS/OUTPUTS OF PORT C (BIT PROGRAMMABLE)
PD0 - PD7	: INPUTS/OUTPUTS OF PORT D (4 BITS PROGRAMMABLE)
PI1 - PI7	: INPUTS/OUTPUTS OF PORT I (BIT PROGRAMMABLE, PI0 ; INPUT)
PJ0 - PJ7	: INPUTS/OUTPUTS OF PORT J (BIT PROGRAMMABLE)
RST	: SYSTEM RESET INPUT AND POWER ON RESET OUTPUT
SCK0, SCK1	: SERIAL CLOCK INPUTS/OUTPUTS (CHANNEL 0 AND CHANNEL 1)

OTHER

AGND	: A/D CONVERTER'S GROUND
AVDD	: A/D CONVERTER'S V _{DD}
AVREF	: A/D CONVERTER'S REFERENCE VOLTAGE INPUT

