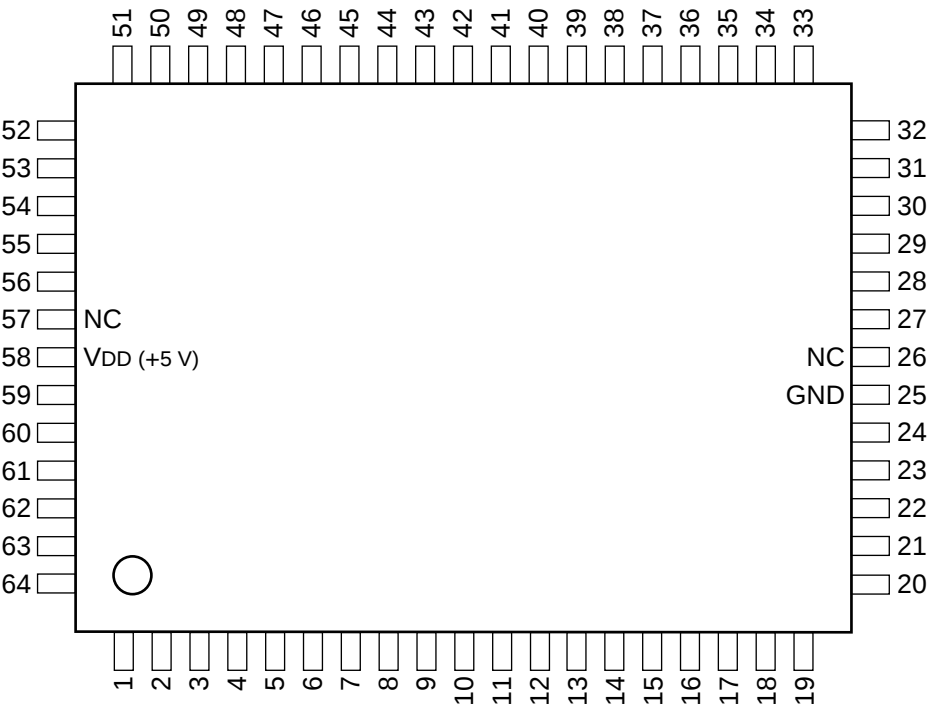


C-MOS 4-BIT SINGLE CHIP MICROCOMPUTER

—TOP VIEW—



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I/O	PB1	17	I/O	PD1	33	I/O	PH1	49	I/O	PI0/AD0
2	I/O	PB2	18	I/O	PD2	34	I/O	PH2	50	I/O	PI1/AD1
3	I/O	PB3	19	I/O	PD3	35	I/O	PH3	51	I/O	PI2/AD2
4	I/O	PE0	20	I/O	PG0	36	O	PY0	52	I/O	PI3/AD3
5	I/O	PE1	21	I/O	PG1	37	O	PY1	53	I/O	AD4/PJ0
6	I/O	PE2	22	I/O	PG2	38	I	PY2	54	I/O	AD5/PJ1
7	I/O	PE3	23	I/O	PG3	39	I	PY3	55	I/O	AD6/PJ2
8	I/O	PF0	24	I	CNT	40	I/O	PX0/SC	56	I/O	AD7/PJ3
9	I/O	PF1	25	—	GND	41	I/O	PX1/SOB	57	—	NC
10	I/O	PF2	26	—	NC	42	I/O	PX2/SOA	58	—	VDD
11	I/O	PF3	27	I	EC/RMC	43	I	PX3/SI	59	I	INT
12	I/O	PC0	28	I	WP	44	I	SEC	60	I/O	PA0
13	I/O	PC1	29	O	XTAL	45	I/O	SCL0	61	I/O	PA1
14	I/O	PC2	30	I	EXTAL	46	I/O	SCL1	62	I/O	PA2
15	I/O	PC3	31	I/O	RST	47	I/O	SDA0	63	I/O	PA3
16	I/O	PD0	32	I/O	PH0	48	I/O	SDA1	64	I/O	PB0

30	EXTAL	PA0	60
59	INT	PA1	61
28	WP	PA2	62
44	SEC	PA3	63
24	CNT		
27	$\overline{\text{EC/RMC}}$	PB0	64
		PB1	1
42	PX2/SOA	PB2	2
43	PX3/SI	PB3	3
38	PY2	PC0	12
39	PY3	PC1	13
		PC2	14
32	PH0	PC3	15
33	PH1		
34	PH2	PD0	16
35	PH3	PD1	17
		PD2	18
49	PI0/AD0	PD3	19
50	PI1/AD1		
51	PI2/AD2	PE0	4
52	PI3/AD3	PE1	5
		PE2	6
53	PJ0/AD4	PE3	7
54	PJ1/AD5		
55	PJ2/AD6	PF0	8
56	PJ3/AD7	PF1	9
		PF2	10
31	RST	PF3	11
45	SCL0	PG0	20
46	SCL1	PG1	21
47	SDA0	PG2	22
48	SDA1	PG3	23
40	PX0/ $\overline{\text{SC}}$	PY0	36
41	PX1/SOB	PY1	37
		XTAL	29

INPUT

CNT	: COUNTER
$\overline{\text{EC/RMC}}$	: EVENT COUNTER
EXTAL	: CLOCK
INT	: EXTERNAL INTERRUPTION
RY2, RY3	: PORT Y
SEC	: SYNC CLOCK
PX3/SI	: PORT X/SERIAL INPUT
WP	: WAKE-UP

OUTPUT

PY0, PY1	: PORT Y
XTAL	: CLOCK

INPUT/OUTPUT

PA0 - PA3	: PORT A (4-BIT)
PB0 - PB3	: PORT B (4-BIT)
PC0 - PC3	: PORT C (4-BIT)
PD0 - PD3	: PORT D (4-BIT)
PE0 - PE3	: PORT E (4-BIT)
PF0 - PF3	: PORT F (4-BIT)
PG0 - PG3	: PORT G (4-BIT)
PH0 - PH3	: PORT H (4-BIT)
PI0/AD0 - PI3/AD3	: PORT I (4-BIT) OR ANALOG VOLTAGE INPUT
PJ0/AD4 - PJ3/AD7	: PORT J (4-BIT) OR ANALOG VOLTAGE INPUT
$\overline{\text{RST}}$	: RESET
PX0/ $\overline{\text{SC}}$	: PORT X/SERIAL CLOCK
SCL0, SCL1	: I ² C BUS I/F TRANSFER CLOCK
SDA0, SDA1	: I ² C BUS I/F TRANSFER DATA
PX2/SOA, PX1/SOB	: PORT X/SERIAL OUTPUT

