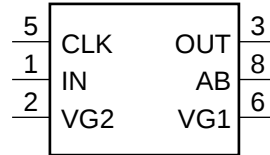
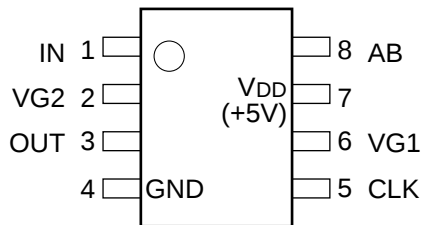

C-MOS CCD 1H DELAY LINE FOR PAL

-TOP VIEW-



INPUT

CLK ; CLOCK
IN ; SIGNAL
VG2 ; GATE BIAS 2 DC

OUTPUT

AB ; AUTO-BIAS DC
OUT ; SIGNAL
VG1 ; GATE BIAS 1 DC

