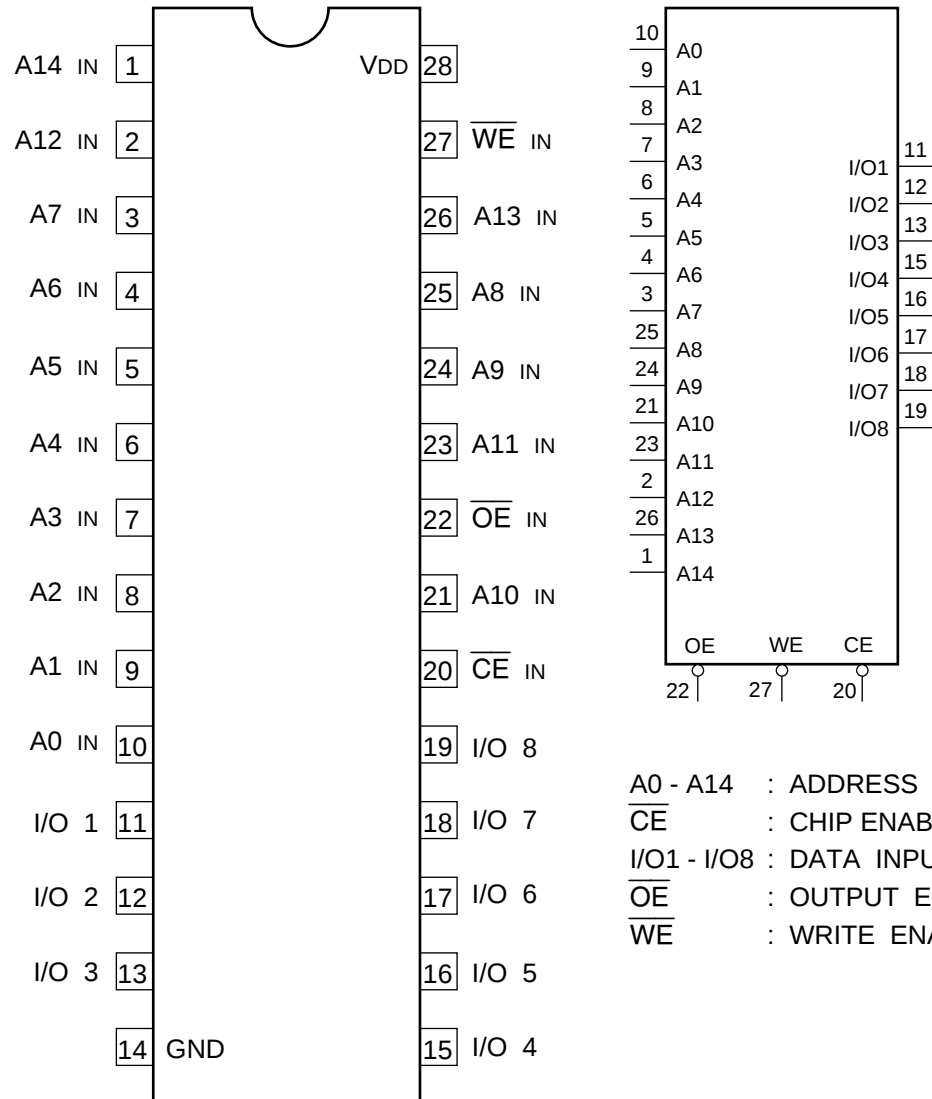


## C-MOS 256 K (32,768 × 8)-BIT SRAM

—TOP VIEW—



| CE | OE | WE | MODE           | I/O TERMINAL   |
|----|----|----|----------------|----------------|
| 1  | x  | x  | NOT SELECT     | HIGH IMPEDANCE |
| 0  | 1  | 1  | OUTPUT DISABLE | HIGH IMPEDANCE |
| 0  | 0  | 1  | READ           | OUTPUT DATA    |
| 0  | x  | 0  | WRITE          | INPUT DATA     |

0 : LOW LEVEL  
1 : HIGH LEVEL  
x : DON'T CARE

