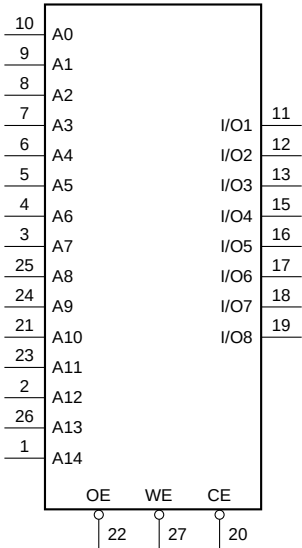
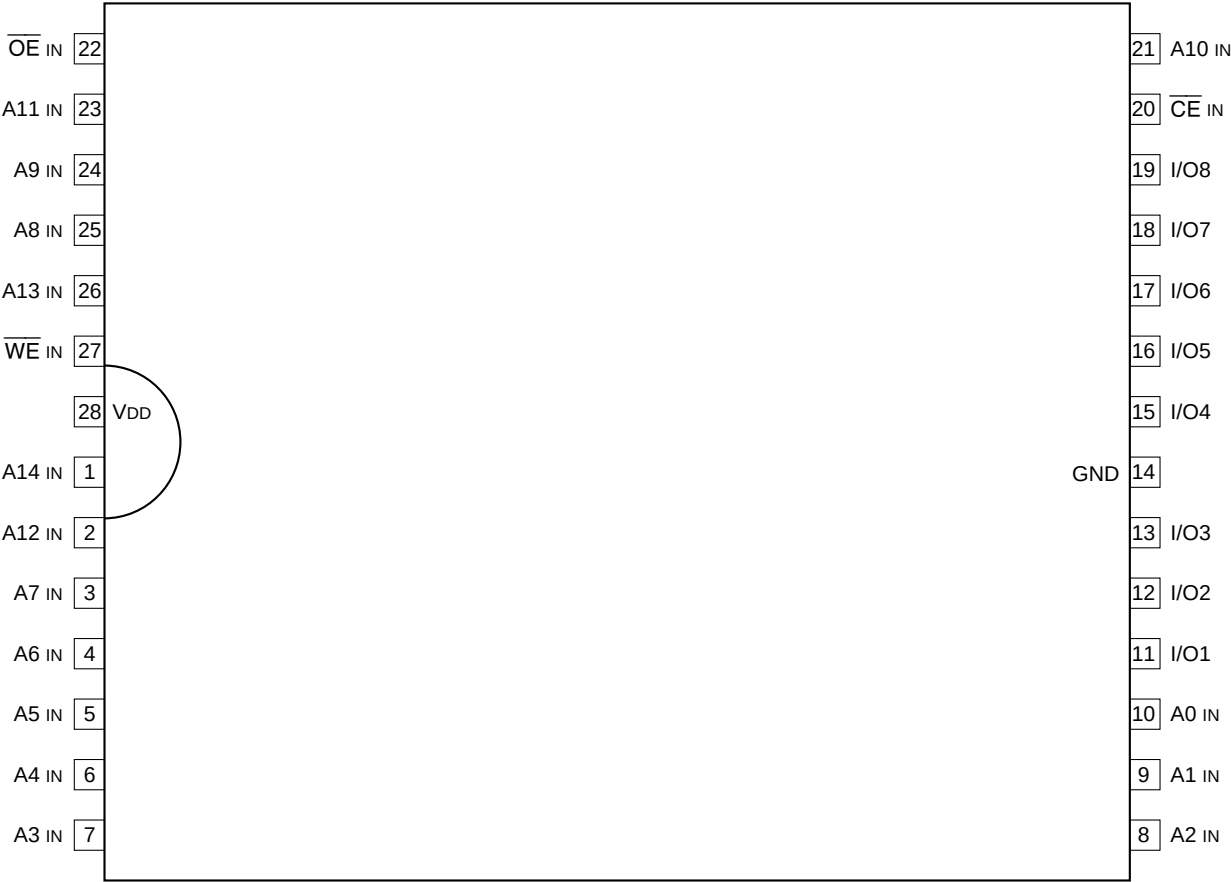


C-MOS 256 K (32,768 × 8)-BIT SRAM

—TOP VIEW—



A0 - A14 : ADDRESS INPUTS
CE : CHIP ENABLE INPUT
I/O1 - I/O8 : DATA INPUTS/OUTPUTS
OE : OUTPUT ENABLE INPUT
WE : WRITE ENABLE INPUT

CE	OE	WE	MODE	I/O TERMINAL
1	x	x	NOT SELECT	HIGH IMPEDANCE
0	1	1	OUTPUT DISABLE	HIGH IMPEDANCE
0	0	1	READ	OUTPUT DATA
0	x	0	WRITE	INPUT DATA

0 ; LOW LEVEL
1 ; HIGH LEVEL
x ; DON'T CARE

