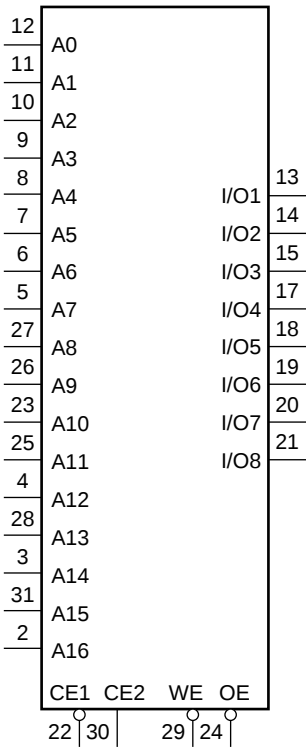
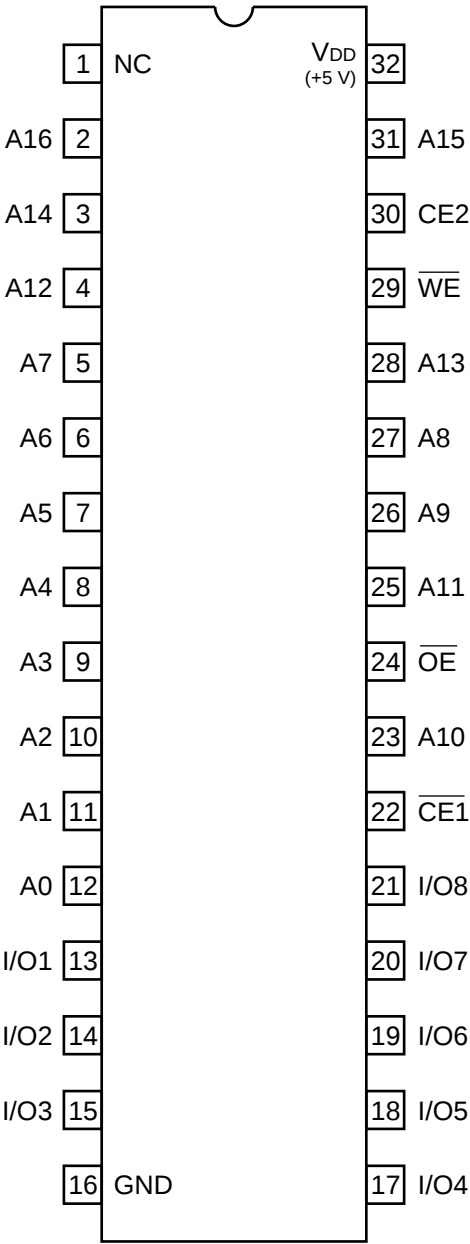

C-MOS 1M (131,072×8)-BIT STATIC RAM
- TOP VIEW -



A0 - A16 ; ADDRESS INPUT
I/O1 - I/O8 ; DATA OUTPUTS
CE1, CE2 ; CHIP ENABLE INPUTS
WE ; WRITE ENABLE INPUT
OE ; OUTPUT ENABLE INPUT

CE1	CE2	OE	WE	MODE	I/O TERMINAL
1	X	X	X	NOT SELECT	HI-Z
X	0	X	X	NOT SELECT	HI-Z
0	1	1	1	OUTPUT DISABLE	HI-Z
0	1	0	1	READ	DATA OUTPUT
0	1	X	0	WRITE	DATA INPUT

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE

