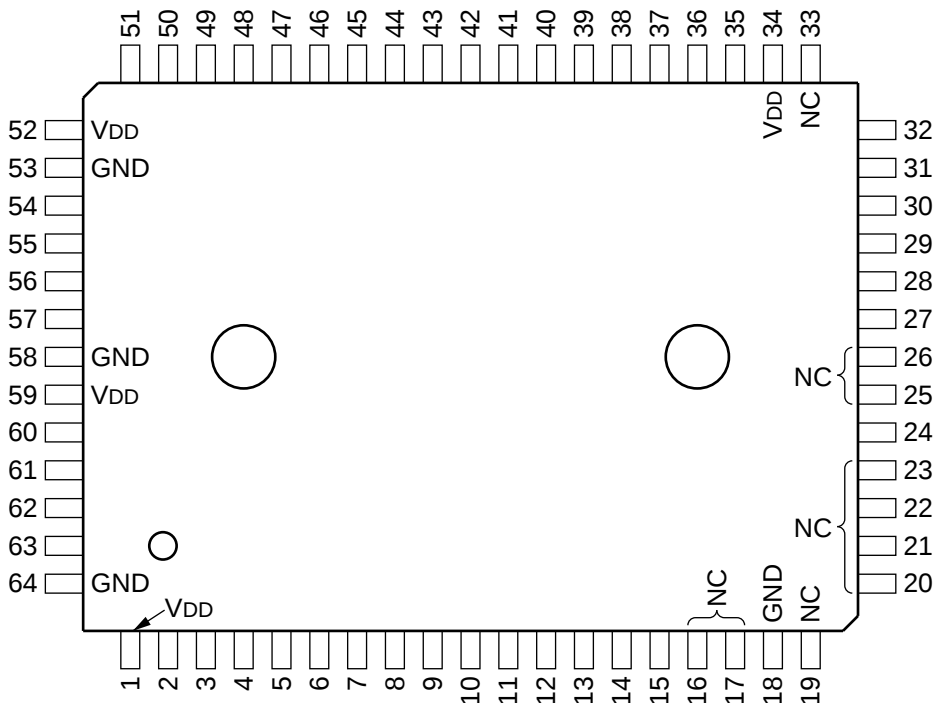
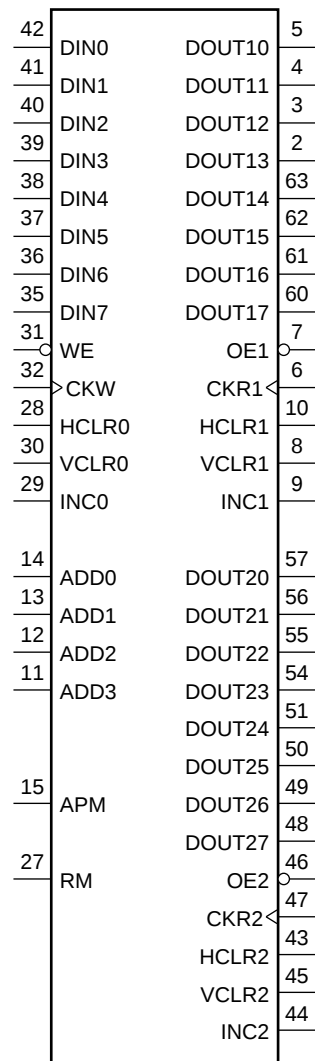


C-MOS 2.4 M (960 × 306 × 8)-BIT 3 PORTS VIDEO FIELD MEMORY

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	VDD	23	—	NC	45	I	VCLR2
2	O	DO13	24	I	TE	46	I	OE2
3	O	DO12	25	—	NC	47	I	CKR2
4	O	DO11	26	—	NC	48	O	DO27
5	O	DO10	27	I	RM	49	O	DO26
6	I	CKR1	28	I	HCLR0	50	O	DO25
7	I	OE1	29	I	INC0	51	O	DO24
8	I	VCLR1	30	I	VCLR0	52	—	VDD
9	I	INC1	31	I	WE	53	—	GND
10	I	HCLR1	32	I	CKW	54	O	DO23
11	I	ADD3	33	—	NC	55	O	DO22
12	I	ADD2	34	—	VDD	56	O	DO21
13	I	ADD1	35	I	DIN7	57	O	DO20
14	I	ADD0	36	I	DIN6	58	—	GND
15	I	APM	37	I	DIN5	59	—	VDD
16	—	NC	38	I	DIN4	60	O	DO17
17	—	NC	39	I	DIN3	61	O	DO16
18	—	GND	40	I	DIN2	62	O	DO15
19	—	NC	41	I	DIN1	63	O	DO14
20	—	NC	42	I	DIN0	64	—	GND
21	—	NC	43	I	HCLR2			
22	—	NC	44	I	INC2			



INPUT

ADD - ADD3 ; ADDRESS
 APM ; ADDRESS PRESET MODE ENABLE
 CKR1, CKR2 ; PORT 1 OR 2 SHIFT SIGNAL
 CKW ; PORT 0 SHIFT SIGNAL
 DIN0 - DIN7 ; PORT 0 DATA
 HCLR0 - HCLR2 ; PORT 0 - 2 HORIZONTAL CLEAR
 INC0 - INC2 ; PORT 0 - 2 LINE INCREMENT
 RM ; RECURSIVE MODE ENABLE
 TE ; TEST MODE ENABLE
 VCLR0 - VCLR2 ; PORT 0 - 2 VERTICAL CLEAR
 OE1, OE2 ; PORT 1 OR 2 OUTPUT ENABLE
 WE ; PORT 0 WRITE ENABLE

OUTPUT

DO10 - DO17 ; PORT 1 DATA
 DO20 - DO27 ; PORT 2 DATA

