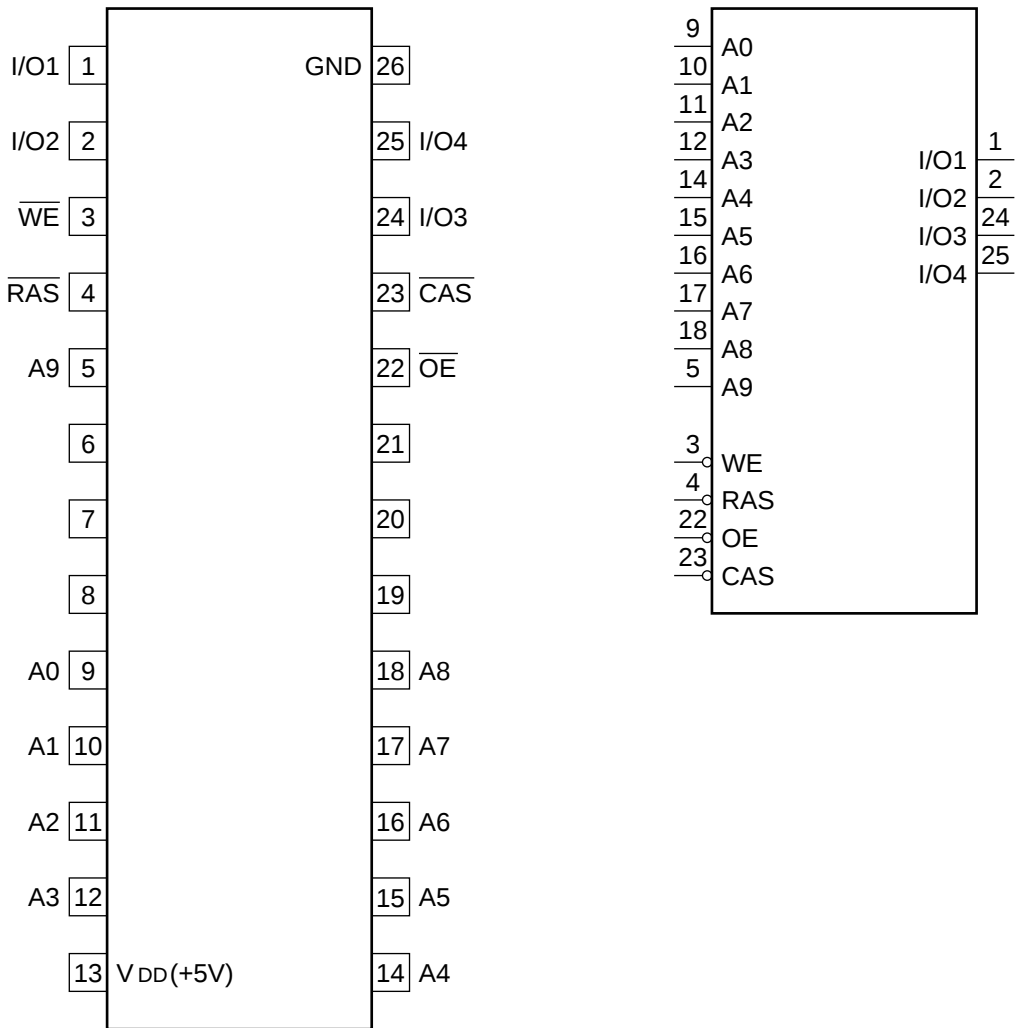

C-MOS 4M-BIT (1M x 4) DYNAMIC RAM
- TOP VIEW -



A0~A9 ; ADDRESS INPUTS
RAS ; ROW ADDRESS STROBE INPUT
CAS ; COLUMN ADDRESS STROBE INPUT
OE ; OUTPUT ENABLE INPUT
WE ; READ/WRITE INPUT
I/O1~I/O4 ; DATA INPUTS/OUTPUTS

