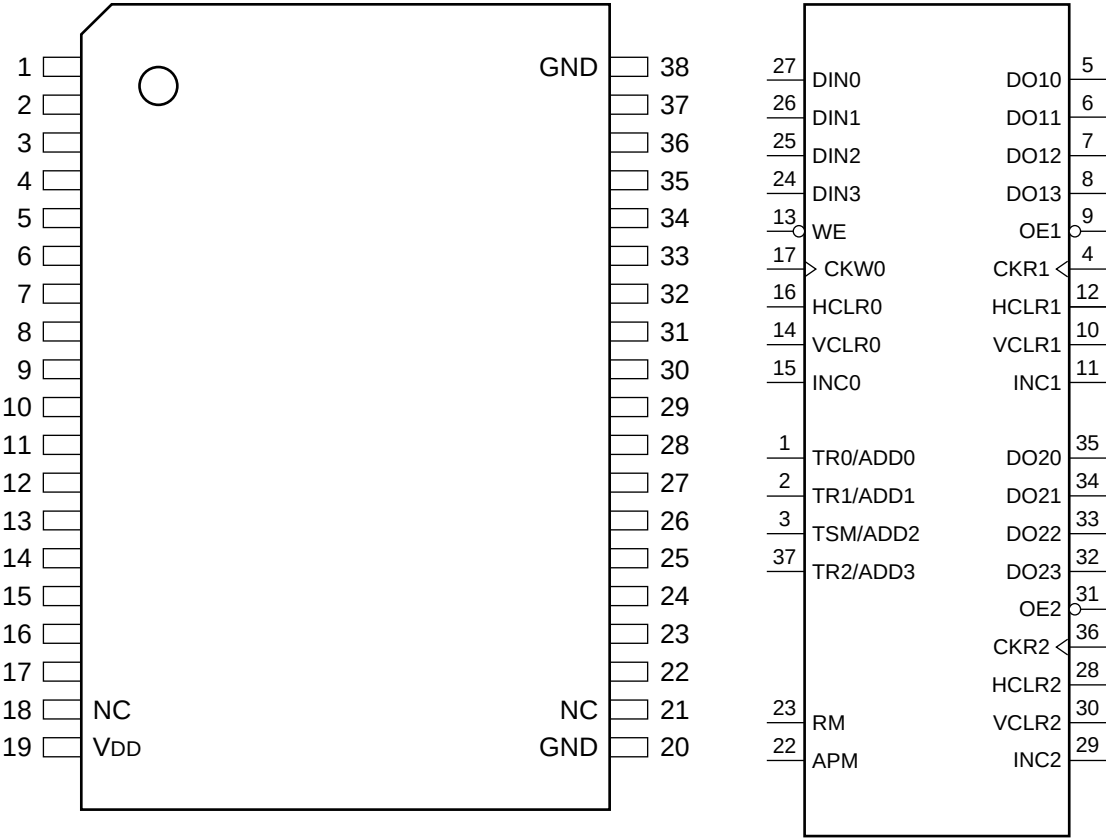


C-MOS VIDEO FIELD MEMORY (960-COLUMN × 306-ROW × 4-BIT)
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	TR0/ADD0	20	—	GND
2	I/O	TR1/ADD1	21	—	NC
3	I	TSM/ADD2	22	I	APM
4	I	CKR1	23	I	PM
5	O	DO10	24	I	DIN3
6	O	DO11	25	I	DIN2
7	O	DO12	26	I	DIN1
8	O	DO13	27	I	DIN0
9	I	OE1	28	I	HCLR2
10	I	VCLR1	29	I	INC2
11	I	INC1	30	I	VCLR2
12	I	HCLR1	31	I	OE2
13	I	WE	32	O	DO23
14	I	VCLR0	33	O	DO22
15	I	INC0	34	O	DO21
16	I	HCLR0	35	O	DO20
17	I	CKW0	36	I	CKR2
18	—	NC	37	I/O	TR2/ADD3
19	—	VDD	38	—	GND

INPUT

APM : ADDRESS PRESET MODE
 CKR1 : R PORT 1 SHIFT SIGNAL
 CKR2 : R PORT 2 SHIFT SIGNAL
 CKW0 : W PORT 0 SHIFT SIGNAL
 DIN0 - DIN3 : W PORT 0 DATA 0 - 3
 HCLR0 : W PORT 0 HORIZONTAL CLEAR
 HCLR1 : R PORT 1 HORIZONTAL CLEAR
 HCLR2 : R PORT 2 HORIZONTAL CLEAR
 INC0 : W PORT 0 LINE INCREMENT
 INC1 : R PORT 1 LINE INCREMENT
 INC2 : R PORT 2 LINE INCREMENT
 $\overline{OE1}$: R PORT 1 OUTPUT ENABLE
 $\overline{OE2}$: R PORT 2 OUTPUT ENABLE
 RM : RECURSIVE MODE ENABLE
 TSM/ADD2 : TRANSFER SYNCHRONOUS MODE, ADDRESS 2 INPUT
 VCLR0 : W PORT 0 VERTICAL CLEAR
 VCLR1 : R PORT 1 VERTICAL CLEAR
 VCLR2 : R PORT 2 VERTICAL CLEAR
 $\overline{WE}$: W PORT 0 WRITE ENABLE

OUTPUT

DO10 - DO13 : R PORT 1 DATA 0 - 3
 DO20 - DO23 : R PORT 2 DATA 0 - 3

INPUT/OUTPUT

TR0/ADD0 : W PORT 0 TRANSFER SYNC I/O, ADDRESS 0 INPUT
 TR1/ADD1 : R PORT 1 TRANSFER SYNC I/O, ADDRESS 1 INPUT
 TR2/ADD3 : R PORT 2 TRANSFER SYNC I/O, ADDRESS 3 INPUT

MODE SELECTION

CONTROL INPUTS			TS, TR/ADD		MODE
RM	APM	TSM	TR 0 - 2	ADD 0 - 3	
0	0	0	OUTPUT	—	NON RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE OUTPUT
0	0	1	INPUT	—	NON RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE INPUT
0	1	—	—	INPUT	NON RECURSIVE MODE, ADDRESS PRESET MODE
1	0	0	OUTPUT	—	RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE OUTPUT
1	0	1	INPUT	—	RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE INPUT

0 : LOW LEVEL
 1 : HIGH LEVEL