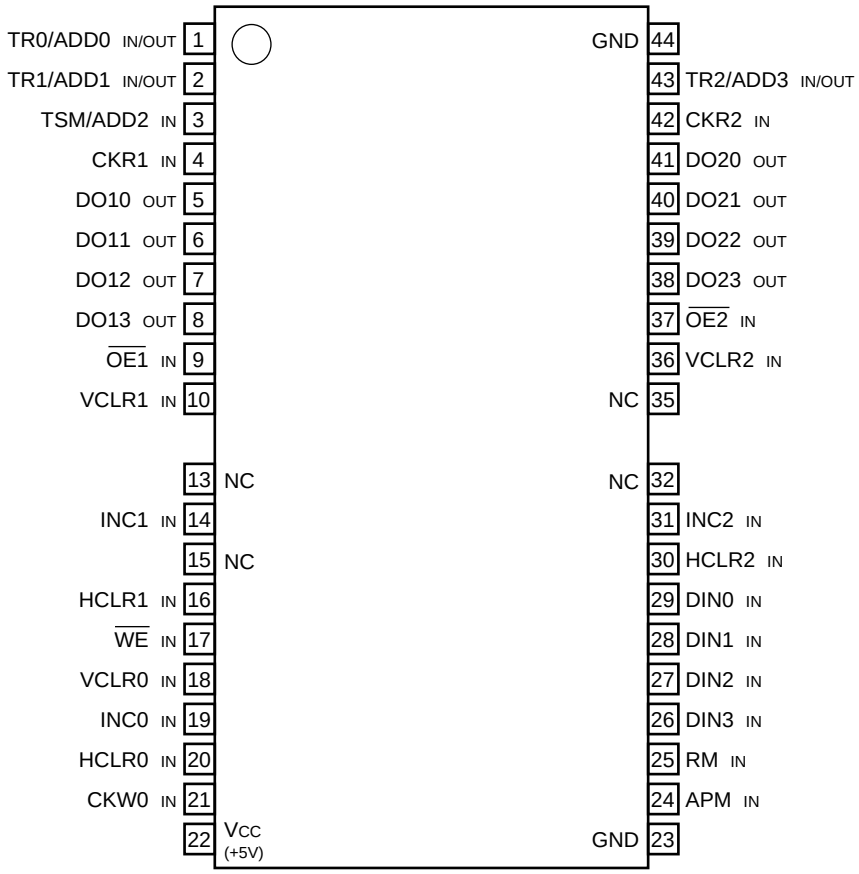
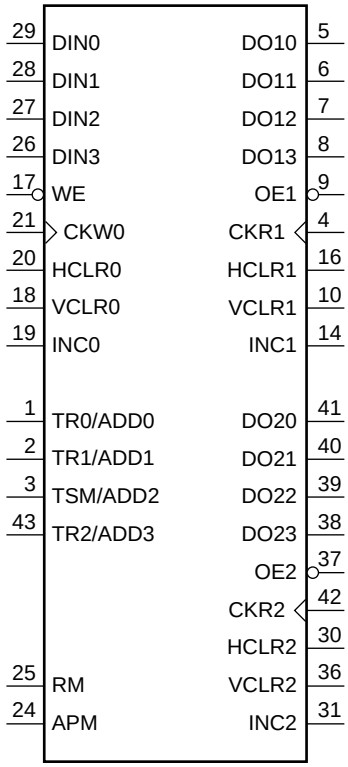
C-MOS VIDEO FIELD MEMORY (960-COLUMNx306-ROWx4-BIT)

- TOP VIEW -



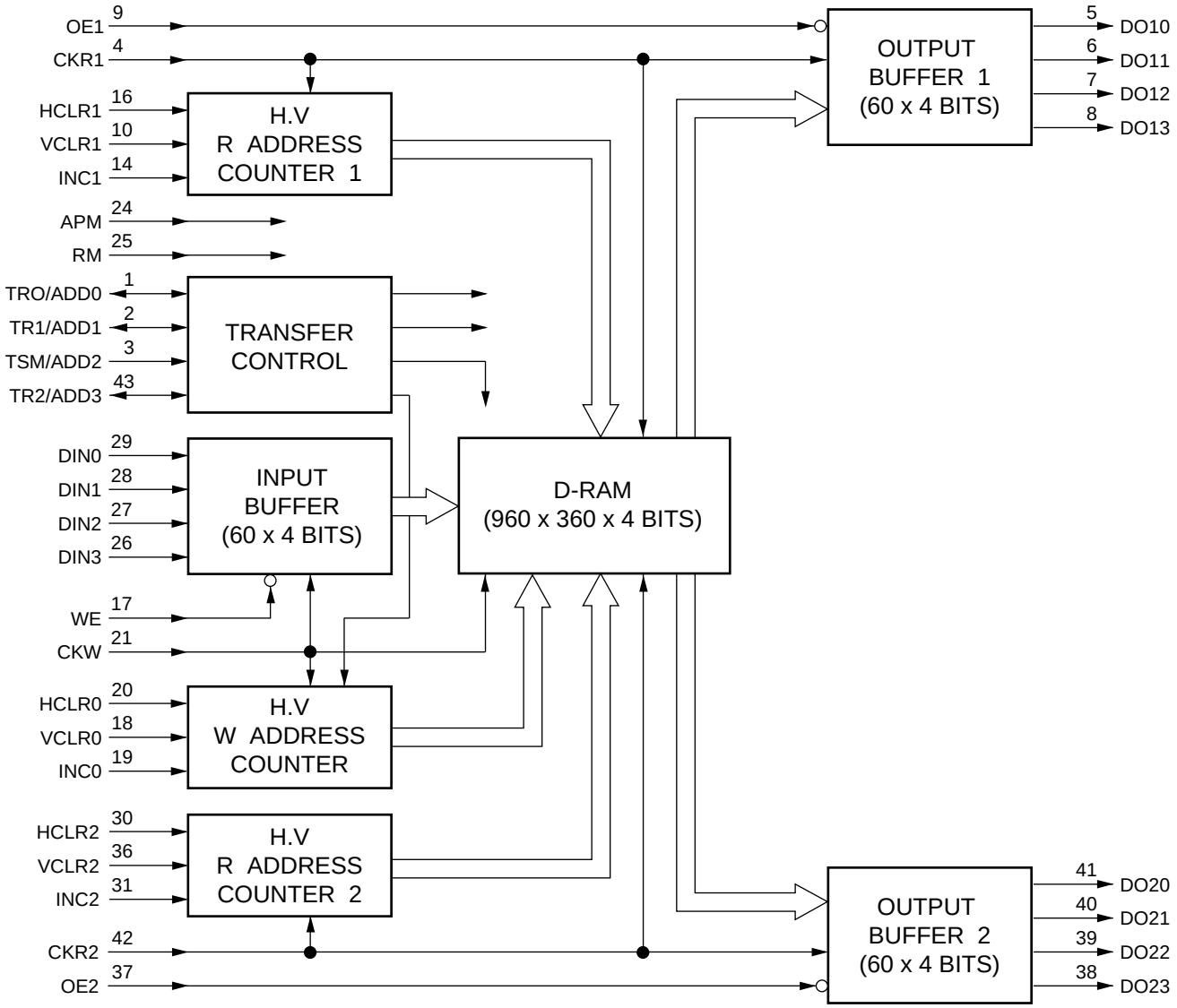


MODE SELECTION

CONTOROL INPUTS		TS, TR/ADD			MODE
RM	APM	TSM	TR0-2	ADD0-3	
0	0	0	OUTPUT	–	NON RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE OUTPUT
0	0	1	INPUT	–	NON RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE INPUT
0	1	–	–	INPUT	NON RECURSIVE MODE, ADDRESS PRESET MODE
1	0	0	OUTPUT	–	RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE OUTPUT
1	0	1	INPUT	–	RECURSIVE MODE, TRANSFER SYNCHRONOUS MODE INPUT

0 ; LOW LEVEL 1 ; HIGH LEVEL

APM ; ADDRESS PRESET MODE INPUT
CKR1 ; R PORT 1 SHIFT SIGNAL INPUT
CKR2 ; R PORT 2 SHIFT SIGNAL INPUT
CKW0 ; W PORT 0 SHIFT SIGNAL INPUT
DIN0 - 3 ; W PORT 0 DATA 0-3 INPUTS
DO10 - 13 ; R PORT 1 DATA 0-3 OUTPUTS
DO20 - 23 ; R PORT 2 DATA 0-3 OUTPUTS
HCLR0 ; W PORT 0 HOLIZONTAL CLEAR INPUT
HCLR1 ; R PORT 1 HOLIZONTAL CLEAR INPUT
HCLR2 ; R PORT 2 HOLIZONATAL CLEAR INPUT
INC0 ; W PORT 0 LINE INCREMENT INPUT
INC1 ; R PORT 1 LINE INCREMENT INPUT
INC2 ; R PORT 2 LINE INCREMENT INPUT
$\overline{OE1}$; R PORT 1 OUTPUT ENABLE INPUT
$\overline{OE2}$; R PORT 2 OUTPUT ENABLE INPUT
RM ; RECURSIVE MODE ENABLE INPUT
TR0/ADD0 ; W PORT 0 TRANSFER SYNC I/O, ADDRESS 0 INPUT
TR1/ADD1 ; R PORT 1 TRANSFER SYNC I/O, ADDRESS 1 INPUT
TR2/ADD3 ; R PORT 2 TRANSFER SYNC I/O, ADDRESS 3 INPUT
TSM/ADD2 ; TRANSFER SYNCHRONOUS MODE, ADDRESS 2 INPUT
VCLR0 ; W PORT 0 VERTICAL CLEAR INPUT
VCLR1 ; R PORT 1 VERTICAL CLEAR INPUT
VCLR2 ; R PORT 2 VERTICAL CLEAR INPUT
$\overline{WE}$; W PORT 0 WRITE ENABLE INPUT


OE1
9
CKR1
4
HCLR1
16
VCLR1
10
INC1
14
H.V
R ADDRESS
COUNTER 1
APM
24
RM
25
TRO/ADD0
1
TR1/ADD1
2
TSM/ADD2
3
TR2/ADD3
43
TRANSFER
CONTROL
DIN0
29
DIN1
28
DIN2
27
DIN3
26
INPUT
BUFFER
(60 x 4 BITS)
WE
17
CKW
21
D-RAM
(960 x 360 x 4 BITS)
OUTPUT
BUFFER 1
(60 x 4 BITS)
5
DO10
6
DO11
7
DO12
8
DO13
HCLR0
20
VCLR0
18
INC0
19
H.V
W ADDRESS
COUNTER
HCLR2
30
VCLR2
36
INC2
31
H.V
R ADDRESS
COUNTER 2
CKR2
42
OE2
37
OUTPUT
BUFFER 2
(60 x 4 BITS)
41
DO20
40
DO21
39
DO22
38
DO23