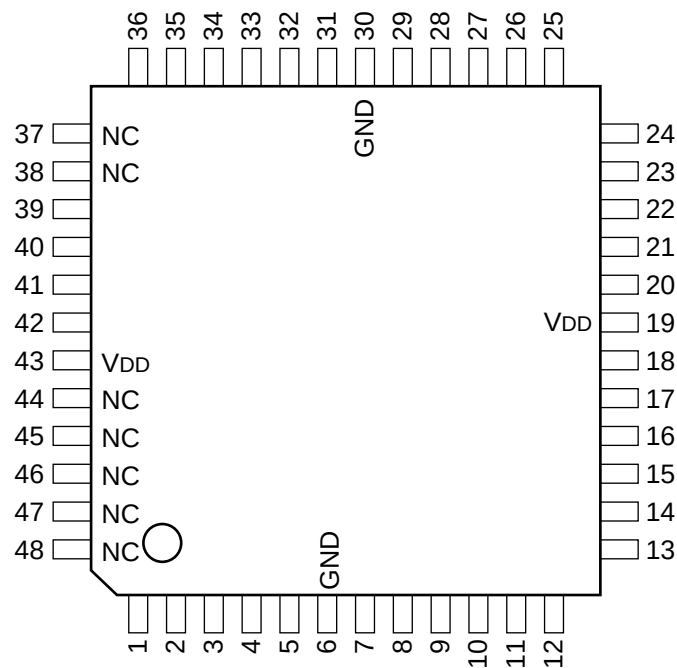


C-MOS DIGITAL LINE MEMORY

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	D0	13	I	PSW7	25	I	OEN	37	—	NC
2	I	D1	14	I	PSW6	26	O	DOT9	38	—	NC
3	I	D2	15	I	PSW5	27	O	DOT8	39	I	AEN
4	I	D3	16	I	PSW4	28	O	DOT7	40	I	N/P
5	I	D4	17	I	PSW3	29	O	DOT6	41	I	SCLK
6	—	GND	18	I	PSW2	30	—	GND	42	I	CLK
7	I	D5	19	—	VDD	31	O	DOT5	43	—	VDD
8	I	D6	20	I	PSW1	32	O	DOT4	44	—	NC
9	I	D7	21	I	PSW0	33	O	DOT3	45	—	NC
10	I	D8	22	I	PSB2	34	O	DOT2	46	—	NC
11	I	D9	23	I	PSB1	35	O	DOT1	47	—	NC
12	I	TINT	24	I	PSB0	36	O	DOT0	48	—	NC

AEN : LINE MEMORY SELECT
 CLK : CLOCK
 DIN0 - DIN9 : VIDEO DATA INPUT
 DOT0 - DOT9 : VIDEO DATA OUTPUT
 N/P : NTSC/PAL/SECAM SELECT
 OEN : OUTPUT ENABLE
 PSB0 - PSB2 : DELAY STEP SELECT (1-BITxN)
 PSW0 - PSW7 : DELAY STEP SELECT (8-BITxN)
 SCLK : CLOCK EDGE SELECT
 TINT : TEST

