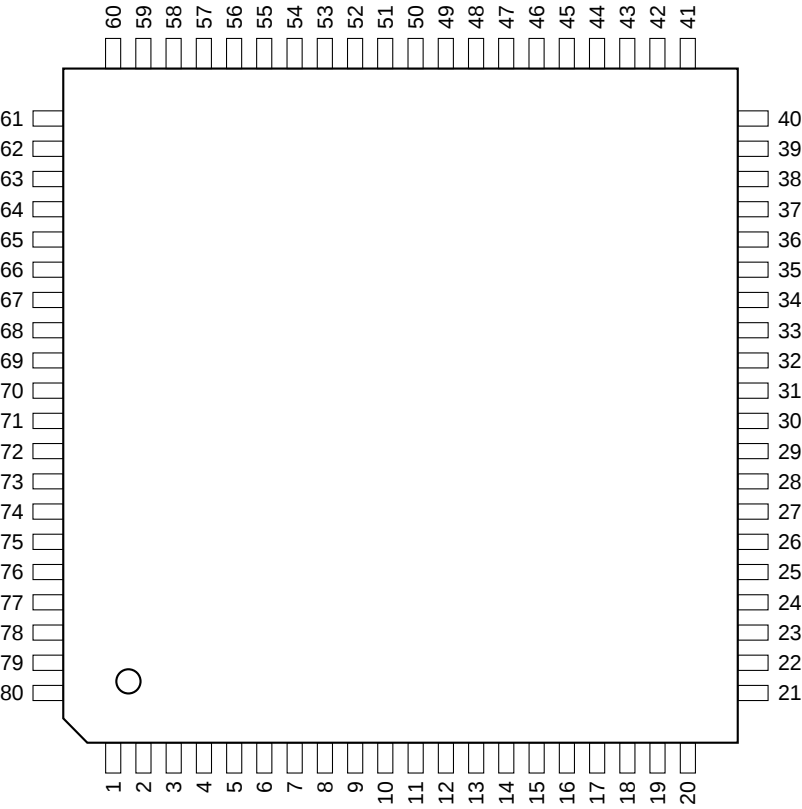


TIMING GENERATOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	21	—	Vcc	41	—	GND	61	—	Vcc
2	I	SCK	22	O	OSCO	42	O	$\overline{V3}$	62	O	PBLK
3	I	AMC	23	I	OSCI	43	O	$\overline{V2}$	63	O	ID
4	I	NAI1A	24	—	GND	44	O	$\overline{V1}$	64	O	WEN
5	I	NAI1B	25	I	PS	45	O	$\overline{SG}$	65	—	NC
6	O	NAO1	26	I	SMD1	46	O	$\overline{SUB}$	66	I	GRST
7	I	NAI2A	27	I	SMD2	47	I	TSTMD	67	I	STDBY
8	I	NAI2B	28	I	STRB	48	I	WM	68	I	VD
9	O	NAO2	29	I	DCLK	49	I	IVIC	69	I	HD
10	I	NAI3A	30	I	DATA	50	O	IVOC	70	I	NOI1A
11	I	NAI3B	31	I	IVIA	51	I	IVID	71	I	NOI1B
12	O	NAO3	32	O	IVOA	52	O	IVOD	72	O	NOO1
13	—	GND	33	I	IVIB	53	I	IVIE	73	I	NOI2A
14	O	CLD	34	O	IVOB	54	O	IVOE	74	I	NOI2B
15	I	SMC	35	—	NC	55	O	$\overline{SHP}$	75	O	NOO2
16	I	SIN	36	—	GND	56	O	$\overline{SHD}$	76	I	NOI3A
17	O	SOT	37	O	RG	57	O	$\overline{RS}$	77	I	NOI3B
18	O	CL	38	O	H1	58	O	$\overline{CPDM}$	78	O	NOO3
19	O	CKO	39	O	H2	59	O	$\overline{CPOB}$	79	—	NC
20	—	GND	40	—	Vcc	60	—	GND	80	—	Vcc