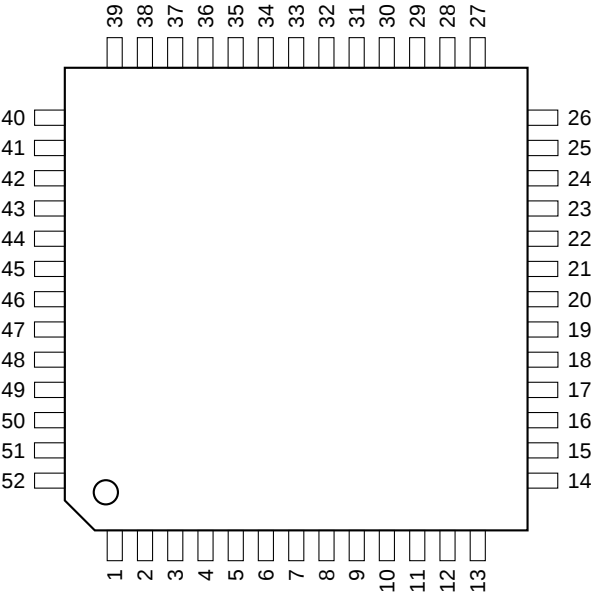


VERTICAL CLOCK DRIVER FOR CCD
—TOP VIEW—



- INPUTS**
- TEST : TEST TERMINAL (STANDARD: -9 V)
 - VH1 - VH4 : VH POWER (STANDARD: +15 V)
 - VL1 - VL4 : VL POWER (STANDARD: -9 V)
 - VM1 - VM6 : VM POWER (STANDARD: 0 V)
 - IM01 - IM04 : OUTPUT CONTROL INVERT
 - SG1, SG2 : OUTPUT CONTROL INVERT
 - ST01 - ST08 : OUTPUT CONTROL INVERT
- OUTPUTS**
- IM01 - IM04 : HIGH-VOLTAGE
 - ST01 - ST08 : HIGH-VOLTAGE

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	TEST	14	I	VH2	27	I	ST06	40	I	ST01
2	I	VL3	15	I	VL3	28	I	ST07	41	I	VH1
3	O	IM02	16	O	ST08	29	I	ST08	42	I	VL1
4	I	VM3	17	I	VM5	30	I	IM04	43	I	VL3
5	O	IM01	18	O	ST07	31	I	SG2	44	O	ST01
6	I	VH3	19	I	VL3	32	I	IM03	45	I	VM1
7	I	VL3	20	O	ST06	33	I	VL2	46	O	ST02
8	I	VH4	21	I	VM6	34	I	IM01	47	I	VL3
9	O	IM03	22	O	ST05	35	I	SG1	48	O	ST03
10	I	VM4	23	I	VL3	36	I	IM02	49	I	VM2
11	O	IM04	24	—	GND	37	I	ST04	50	O	ST04
12	I	VL3	25	—	VCC	38	I	ST03	51	I	VL3
13	I	VL4	26	I	ST05	39	I	ST02	52	I	VH2

