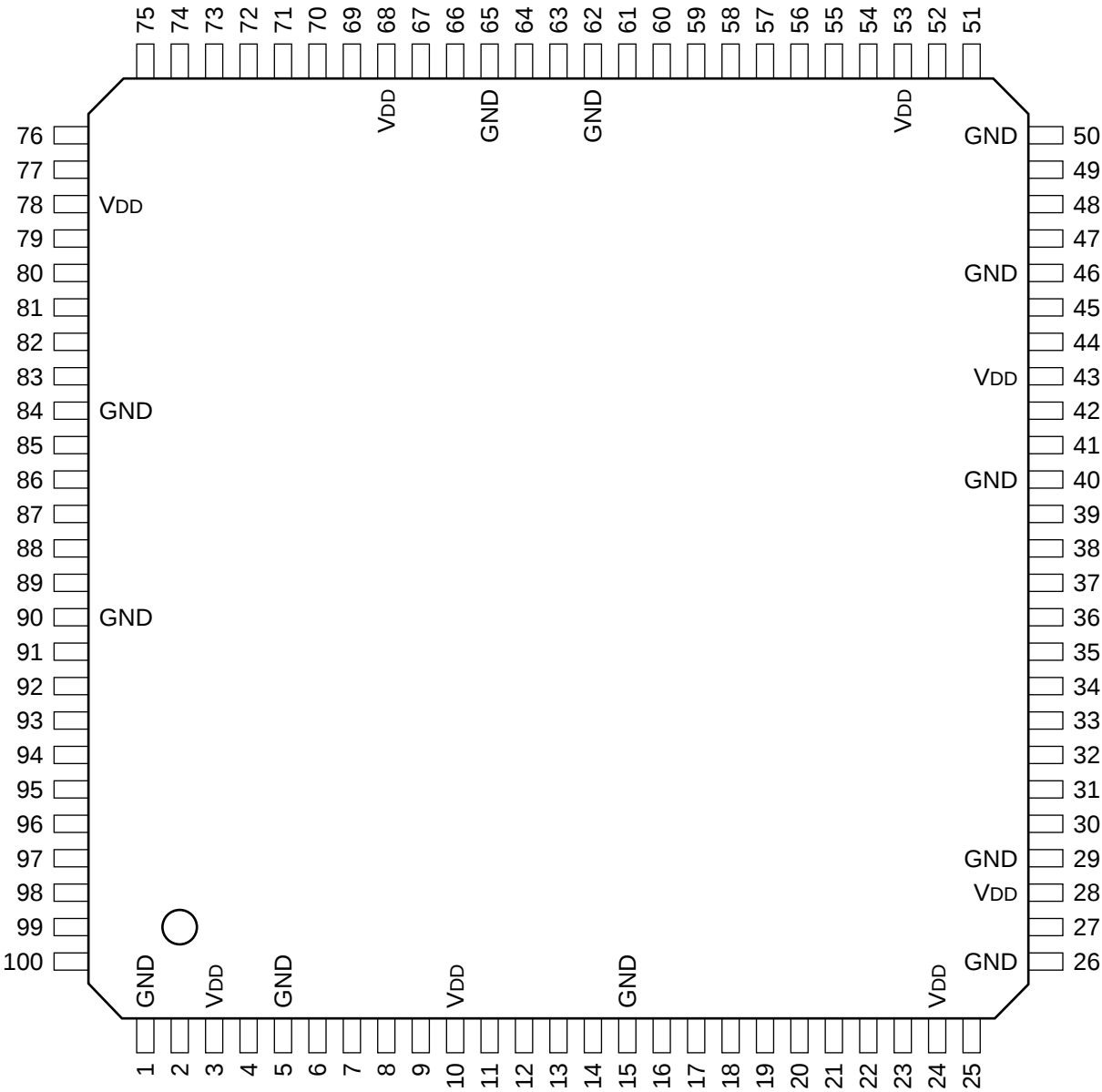


C-MOS AES/EBU ENCODER/DECODER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	26	—	GND	51	I	BCK0	76	I	TMODE
2	I	FRMLOCK0	27	I	PLLCK1	52	I	BCK1	77	I	SMODE
3	—	VDD	28	—	VDD	53	—	VDD	78	—	VDD
4	I	FRMLOCK1	29	—	GND	54	I/O	D7	79	I	F2I1
5	—	GND	30	O	ENCID0	55	I/O	D6	80	O	SAO1
6	I	REFLR0	31	O	AESO0	56	I/O	D5	81	O	RLR1
7	I	REFLR1	32	O	ENCID1	57	I/O	D4	82	O	RCK1
8	I	REFCK0	33	O	AESO1	58	I/O	D3	83	O	F2OUT1
9	I	REFCK1	34	O	DECID0	59	I/O	D2	84	—	GND
10	—	VDD	35	O	RBICK0	60	I/O	D1	85	I	F2I0
11	O	VO0	36	O	DECID1	61	I/O	D0	86	O	SAO0
12	O	VO1	37	O	RBICK1	62	—	GND	87	O	RLR0
13	O	UO0	38	I	AESI0	63	I	A8	88	O	RCK0
14	O	UO1	39	I	AESI1	64	I	A7	89	O	F2OUT0
15	—	GND	40	—	GND	65	—	GND	90	—	GND
16	I/O	PIO0	41	I	LRCK0	66	I	BICK0	91	I	F1I1
17	I/O	PIO1	42	I	LRCK1	67	I	BICK1	92	O	F1OUT1
18	I/O	PIO2	43	—	VDD	68	—	VDD	93	I	SAI1
19	I/O	PIO3	44	O	MUTE1	69	I	A6	94	I	VI1
20	I/O	PIO4	45	O	MUTE0	70	I	A5	95	I	UI1
21	I/O	PIO5	46	—	GND	71	I	A4	96	I	F1I0
22	I	TESTRAM	47	I	WR	72	I	A3	97	O	F1OUT0
23	I	CLR	48	I	RD	73	I	A2	98	I	SAI0
24	—	VDD	49	I	CS	74	I	A1	99	I	VI0
25	I	PLLCK0	50	—	GND	75	I	A0	100	I	UI0

INPUT

A0 - A8	: ADDRESS BUS
AESI0, AESI1	: AES/EBU
BCK0, BCK1	: 64FS
BICK0, BICK1	: 128FS
$\overline{\text{CLR}}$	: RESET
$\overline{\text{CS}}$	: CHIP SELECT
F1I0, F1I1	: FROM AP1 TO FORMATTER
F2I0, F2I1	: FROM RATE CONVERTER TO FORMATTER
FRMLOCK0, FRMLOCK1	: FRAME LOCK
LRCK0, LRCK1	: FS
PLLCK0, PLLCK1	: 512FS
$\overline{\text{RD}}$	: REGISTER READ SIGNAL
REFCK0, REFCK1	: REFERENCE 64FS
REFLR0, REFLR1	: REFERENCE FS
SAI0, SAI1	: SERIAL AUDIO
SMODE	: TEST PIN
TESTRAM	: TEST PIN
TMODE	: TEST PIN
UI0, UI1	: USER DATA
VI0, VI1	: VALIDITY FLAG
$\overline{\text{WR}}$	: REGISTER WRITE SIGNAL

OUTPUT

AESO0, AESO1	: AES/EBU
DECID0, DECID1	: DECODE BLOCK ID MONITOR
ENCID0, ENCID1	: ENCODE BLOCK ID MONITOR
F1OUT0, F1OUT1	: FROM FORMATTER TO RATE CONVERTER
F2OUT0, F2OUT2	: FROM FORMATTER TO AP1
MUTE0, MUTE1	: MUTE CONTROL SIGNAL
RBICK0, RBICK1	: DEMODULATED 128FS
RCK0, RCK1	: DEMODULATED 64FS
RLR0, RLR1	: DEMODULATED FS
SAO0, SAO1	: SERIAL AUDIO
UO0, UO1	: DEMODULATED USER DATA
VO0, VO1	: DEMODULATED VALIDITY FLAG

INPUT/OUTPUT

D0 - D7	: DATA BUS
PIO0 - PIO5	: PARALLEL I/O BUS

