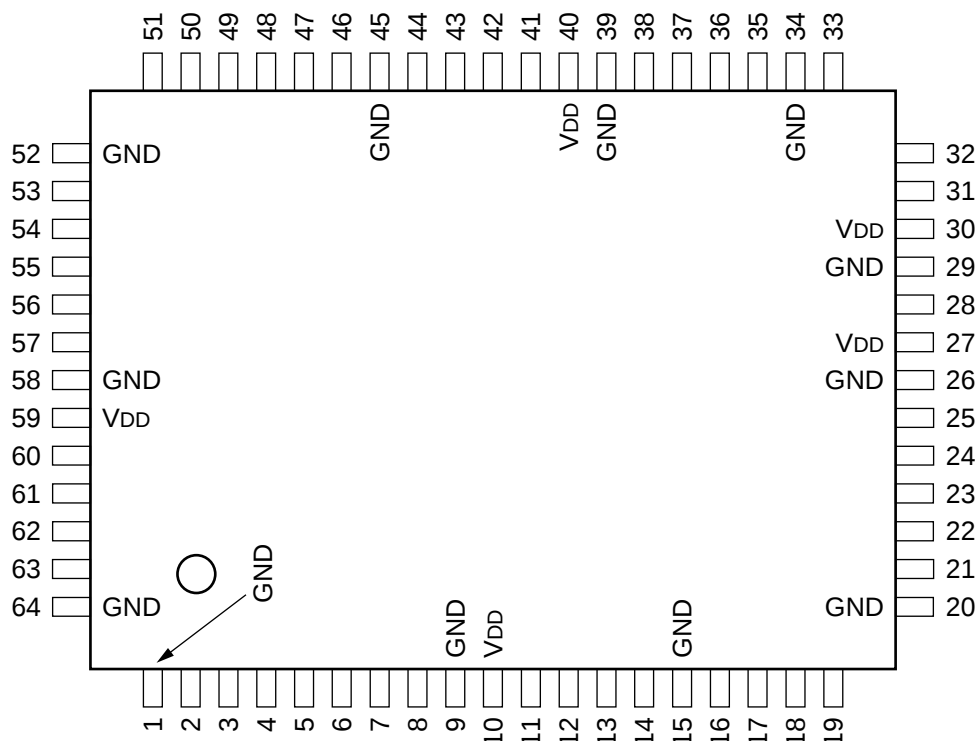


—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	17	I/O	CPU D5	33	O	DACK	49	O	WF OE2
2	I	RESET	18	I/O	CPU D6	34	—	GND	50	O	WF RE1
3	I	SPCFPGA	19	I/O	CPU D7	35	O	SPCRST	51	O	WF RE2
4	I	WR	20	—	GND	36	O	SCSIPD	52	—	GND
5	I	ADR2	21	I	UDS	37	O	TP	53	O	RL CK
6	I	ADR3	22	I	LDS	38	I	TERMPWR	54	O	RF WE1
7	I	ADR4	23	I	CS1	39	—	GND	55	O	RF WE2
8	I	ADR5	24	I	DREQ	40	—	VDD	56	O	RF WCK11
9	—	GND	25	O	CK20M	41	O	WL OE	57	O	RF WCK12
10	—	VDD	26	—	GND	42	O	WL CK1	58	—	GND
11	I/O	CPU D0	27	—	VDD	43	O	WF RCK1	59	—	VDD
12	I/O	CPU D1	28	I	CK40M	44	O	WF RCK2	60	O	RF WCK21
13	I/O	CPU D2	29	—	GND	45	—	GND	61	O	RF WCK22
14	I/O	CPU D3	30	—	VDD	46	O	WF RRS1	62	O	RF WRS1
15	—	GND	31	O	DMDS	47	O	WF RRS2	63	O	RF WRS2
16	I/O	CPU D4	32	O	DM RW	48	O	WF OE1	64	—	GND

