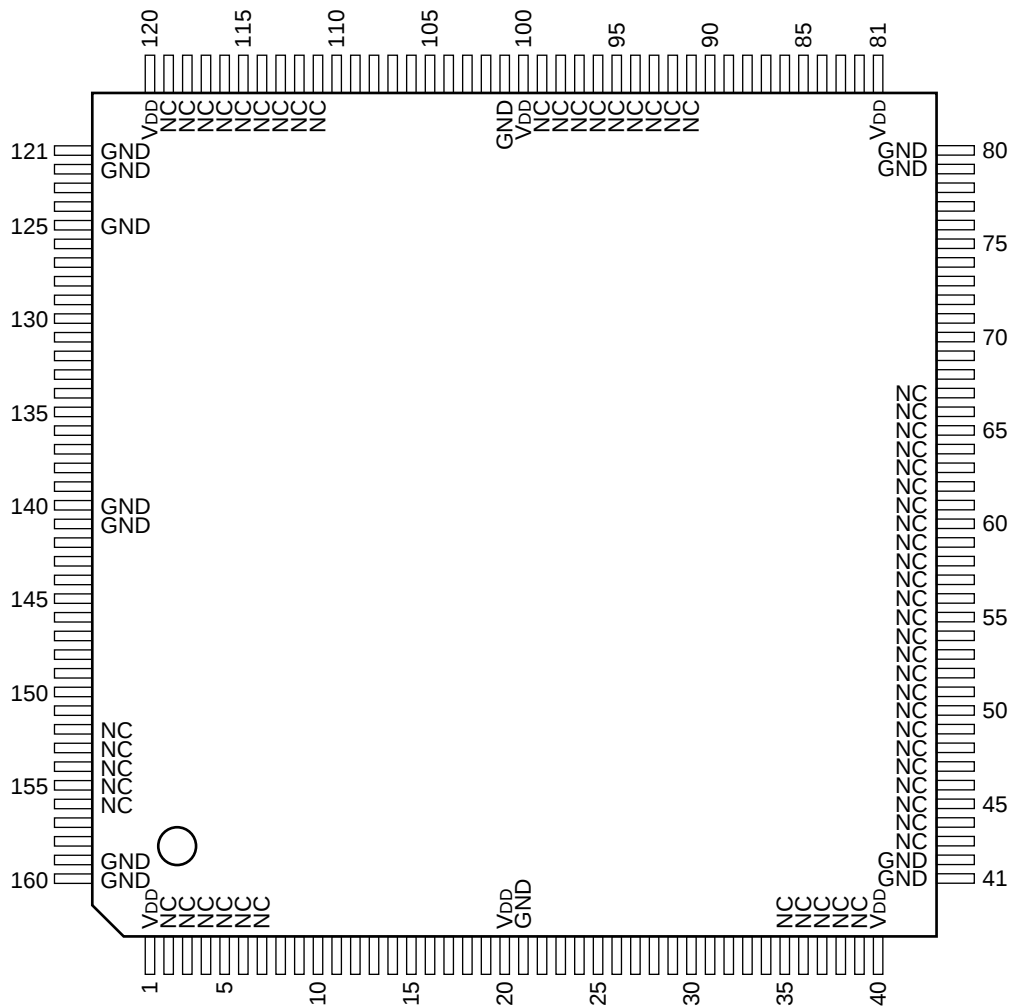


C-MOS DATA TRANSFER CONTROLLER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	41	—	GND	81	—	VDD	121	—	GND
2	—	NC	42	—	GND	82	I	SF DAT0	122	—	GND
3	—	NC	43	—	NC	83	I	SF DAT1	123	I	ICLK
4	—	NC	44	—	NC	84	I	SF DAT2	124	O	OCLK
5	—	NC	45	—	NC	85	I	SF DAT3	125	—	GND
6	—	NC	46	—	NC	86	I	SF DAT4	126	I	RDAT0
7	—	NC	47	—	NC	87	I	SF DAT5	127	I	RDAT1
8	I/O	CPUDAT0	48	—	NC	88	I	SF DAT6	128	I	RDAT2
9	I/O	CPUDAT1	49	—	NC	89	I	SF DAT7	129	I	RDAT3
10	I/O	CPUDAT2	50	—	NC	90	O	SFEN	130	I	RDAT4
11	I/O	CPUDAT3	51	—	NC	91	—	NC	131	I	RDAT5
12	I/O	CPUDAT4	52	—	NC	92	—	NC	132	I	RDAT6
13	I/O	CPUDAT5	53	—	NC	93	—	NC	133	I	RDAT7
14	I/O	CPUDAT6	54	—	NC	94	—	NC	134	O	REN
15	I/O	CPUDAT7	55	—	NC	95	—	NC	135	O	RADD0
16	I	CPUAD0	56	—	NC	96	—	NC	136	O	RADD1
17	I	CPUAD1	57	—	NC	97	—	NC	137	O	RADD2
18	I	CPUAD2	58	—	NC	98	—	NC	138	O	RADD3
19	I	CPUAD3	59	—	NC	99	—	NC	139	O	RADD4
20	—	VDD	60	—	NC	100	—	VDD	140	—	GND
21	—	GND	61	—	NC	101	—	GND	141	—	GND
22	I	CLR	62	—	NC	102	I	CDDAT0	142	O	RADD5
23	I	RD	63	—	NC	103	I	CDDAT1	143	O	RADD6
24	I	WR	64	—	NC	104	I	CDDAT2	144	O	RADD7
25	I	CS	65	—	NC	105	I	CDDAT3	145	O	RADD8
26	O	IRQ	66	—	NC	106	I	CDDAT4	146	O	RADD9
27	O	CPUTEST0	67	—	NC	107	I	CDDAT5	147	O	RADD10
28	O	CPUTEST1	68	O	UDAT0	108	I	CDDAT6	148	O	RADD11
29	O	CPUTEST2	69	O	UDAT1	109	I	CDDAT7	149	O	RADD12
30	O	CPUTEST3	70	O	UDAT2	110	O	CDEN	150	O	RADD13
31	O	CPUTEST4	71	O	UDAT3	111	—	NC	151	O	RADD14
32	O	CPUTEST5	72	O	UDAT4	112	—	NC	152	—	NC
33	O	CPUTEST6	73	O	UDAT5	113	—	NC	153	—	NC
34	O	CPUTEST7	74	O	UDAT6	114	—	NC	154	—	NC
35	—	NC	75	O	UDAT7	115	—	NC	155	—	NC
36	—	NC	76	O	UDREQ	116	—	NC	156	—	NC
37	—	NC	77	I	UDACK	117	—	NC	157	I	TEST C
38	—	NC	78	I	MCK	118	—	NC	158	I	TEST F
39	—	NC	79	—	GND	119	—	NC	159	—	GND
40	—	VDD	80	—	GND	120	—	VDD	160	—	GND

