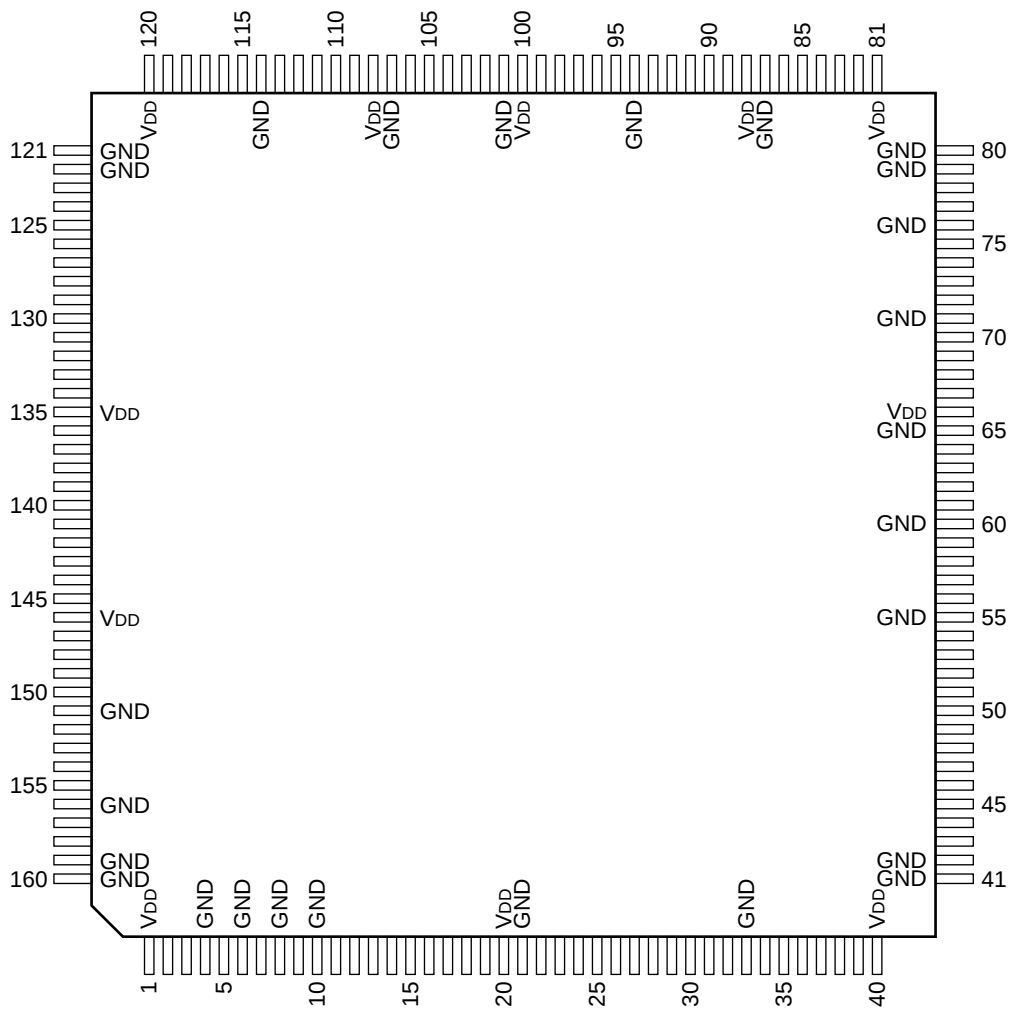


C-MOS ASPECT RATIO CONVERTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	41	—	GND	81	—	V _{DD}	121	—	GND
2	I	DLI1	42	—	GND	82	O	SCO09	122	—	GND
3	I	DLI0	43	I	SCK	83	O	SCO08	123	I	SCI11
4	—	GND	44	I	SMC	84	O	SCO07	124	I	SCI10
5	I	CK1I	45	I	SIN	85	O	SCO06	125	I	SCI09
6	—	GND	46	O	SOT	86	O	SCO05	126	I	SCI08
7	I	HRI	47	I	AMC	87	—	GND	127	I	SCI07
8	—	GND	48	I	TEB	88	—	V _{DD}	128	I	SCI06
9	I	CK2I	49	I	TIN	89	O	SCO04	129	I	SCI05
10	—	GND	50	I	SCLI	90	O	SCO03	130	I	SCI04
11	I	PSS	51	I	SDAI	91	O	SCO02	131	I	SCI03
12	I	PWEB	52	O	SDAO	92	O	SCO01	132	I	SCI02
13	I	PREB	53	I	SLA1	93	O	SCO00	133	I	SCI01
14	I	PCEB	54	I	SLA0	94	—	GND	134	I	SCI00
15	I	PA7	55	—	GND	95	O	SBO09	135	—	V _{DD}
16	I	PA6	56	I	CCI3	96	O	SBO08	136	I	SABI09
17	I	PA5	57	I	CCI2	97	O	SBO07	137	I	SABI08
18	I	PA4	58	I	CCI1	98	O	SBO06	138	I	SABI07
19	I	PA3	59	I	CCI0	99	O	SBO05	139	I	SABI06
20	—	V _{DD}	60	—	GND	100	—	V _{DD}	140	I	SABI05
21	—	GND	61	I	CABI3	101	—	GND	141	I	SABI04
22	I	PA2	62	I	CABI2	102	O	SBO04	142	I	SABI03
23	I	PA1	63	I	CABI1	103	O	SBO03	143	I	SABI02
24	I	PA0	64	I	CABI0	104	O	SBO02	144	I	SABI01
25	I/O	PD7	65	—	GND	105	O	SBO01	145	I	SABI00
26	I/O	PD6	66	—	V _{DD}	106	O	SBO00	146	—	V _{DD}
27	I/O	PD5	67	O	CCO3	107	—	GND	147	O	VDCO
28	I/O	PD4	68	O	CCO2	108	—	V _{DD}	148	O	HDCO
29	I/O	PD3	69	O	CCO1	109	O	SAO09	149	O	VDBO
30	I/O	PD2	70	O	CCO0	110	O	SAO08	150	O	HDBO
31	I/O	PD1	71	—	GND	111	O	SAO07	151	—	GND
32	I/O	PD0	72	O	CABO3	112	O	SAO06	152	O	VDAO
33	—	GND	73	O	CABO2	113	O	SAO05	153	O	HDAO
34	I	TDI	74	O	CABO1	114	—	GND	154	O	DLO1
35	I	TCK	75	O	CABO0	115	O	SAO04	155	O	DLO0
36	I	TMS	76	—	GND	116	O	SAO03	156	—	GND
37	I	TRST	77	O	SCO11	117	O	SAO02	157	I	VDI
38	O	TDO	78	O	SCO10	118	O	SAO01	158	I	HDI
39	I	PORB	79	—	GND	119	O	SAO00	159	—	GND
40	—	V _{DD}	80	—	GND	120	—	V _{DD}	160	—	GND

INPUT

AMC	: SCAN TEST ATG MODE
CABIO - CABI3	: RINGING CONTROL COEFFICIENT
CCIO - CCI3	: RINGING CONTROL COEFFICIENT
CK1I, CK2I	: CLOCK
DLIO, DLI1	: DELAY SIGNAL
HDI	: HORIZONTAL DRIVE
HRI	: HORIZONTAL RESET
PA0 - PA7	: PARALLEL INTERFACE ADDRESS BUS
PCEB	: PARALLEL INTERFACE BUS CHIP ENABLE PULSE
PORB	: POWER ON RESET
PREB	: PARALLEL INTERFACE BUS READ ENABLE PULSE
PSS	: CPU INTERFACE BUS SELECT
PWEB	: PARALLEL INTERFACE BUS WRITE ENABLE PULSE
SABIO0 - SABI09	: VIDEO SIGNAL
SCIO0 - SCI11	: VIDEO SIGNAL
SCK	: SCAN TEST CLOCK
SCLI	: IIC SERIAL INTERFACE BUS CLOCK
SDAI	: IIC SERIAL INTERFACE BUS DATA
SIN	: SCAN TEST SHIFT REGISTER
SLA0, SLA1	: IIC SERIAL INTERFACE CUS ADDRESS
SMC	: SCAN TEST MODE
TCK	: BOUNDARY SCAN (JTAG) CLOCK
TDI	: BOUNDARY SCAN (JTAG) DATA
TEB	: INTERNAL RAM TEST
TIN	: INTERNAL RAM TEST
TMS	: BOUNDARY SCAN (JTAG) MODE
TRST	: BOUNDARY SCAN (JTAG) RESET
VDI	: VERTICAL DRIVE

OUTPUT

CABO0 - CABO3	: RINGING CONTROL COEFFICIENT
CCO0 - CCO3	: RINGING CONTROL COEFFICIENT
DLO0, DLO1	: DELAY SIGNAL
HDAO	: HORIZONTAL DRIVE
HDBO	: HORIZONTAL DRIVE
HDCO	: HORIZONTAL DRIVE
SAO00 - SAO09	: VIDEO SIGNAL
SBO00 - SBO09	: VIDEO SIGNAL
SCO00 - SCO11	: VIDEO SIGNAL
SDAO	: IIC SERIAL INTERFACE BUS DATA
SOT	: SCAN TEST SHIFT REGISTER
TDO	: BOUNDARY SCAN (JTAG) DATA
VDAO	: VERTICAL DRIVE
VDBO	: VERTICAL DRIVE
VDCO	: VERTICAL DRIVE

INPUT/OUTPUT

PD0 - PD7	: PARALLEL INTERFACE DATA BUS
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