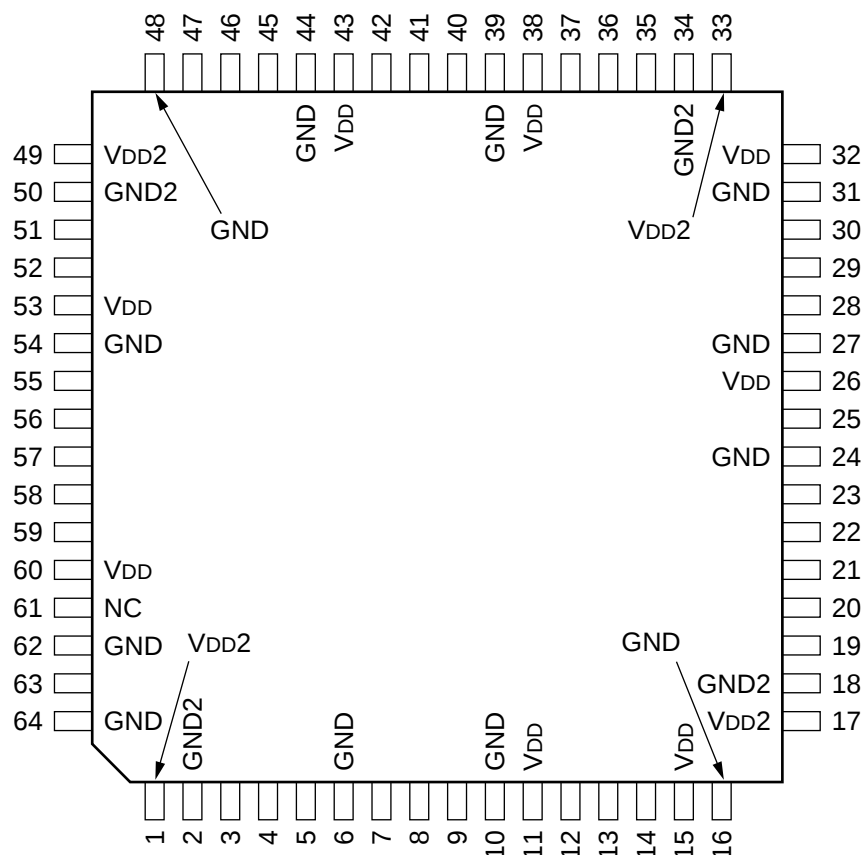


C-MOS HIPS

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD2	17	—	VDD2	33	—	VDD2	49	—	VDD2
2	—	GND2	18	—	GND2	34	—	GND2	50	—	GND2
3	I	RXDATA11	19	I	RXDATA2	35	O	TXDATA2	51	O	TXDATA11
4	I	RXDATA10	20	I	RXDATA1	36	O	TXDATA3	52	O	TXCLK
5	I	RXDATA9	21	I	RXDATA0	37	O	TXDATA4	53	—	VDD
6	—	GND	22	I	RESET	38	—	VDD	54	—	GND
7	I	RXDATA8	23	I	SYNC	39	—	GND	55	I	DI
8	I	RXDATA7	24	—	GND	40	O	TXDATA5	56	I	SADD
9	I	RXDATA6	25	I	CLOCK	41	O	TXDATA6	57	I	CS
10	—	GND	26	—	VDD	42	O	TXDATA7	58	I	CKD
11	—	VDD	27	—	GND	43	—	VDD	59	I	CKX
12	I	RXDATA5	28	O	CLK MON	44	—	GND	60	—	VDD
13	I	RXDATA4	29	O	TXDATA0	45	O	TXDATA8	61	—	NC
14	I	RXDATA3	30	O	TXDATA1	46	O	TXDATA9	62	—	GND
15	—	VDD	31	—	GND	47	O	TXDATA10	63	I	RXCLK
16	—	GND	32	—	VDD	48	—	GND	64	—	GND

INPUT

CKD : SERIAL INTERFACE DATA CLOCK
CKX : REGISTER ENABLE CLOCK
CLOCK : SYSTEM CLOCK
 $\overline{\text{CS}}$: CHIP SELECT FOR SERIAL INTERFACE
 $\overline{\text{DI}}$: SERIAL DATA
 $\overline{\text{RESET}}$: ASYNCHRONOUS RESET
RXCLK : RECEIVE CLOCK, 37.5 MHz
RXDATA0 : DATA BIT 0/CYCLIC PULSE GENERATOR 1 LOAD
RXDATA1 : DATA BIT 1/CYCLIC PULSE GENERATOR 1 ENABLE
RXDATA2 : DATA BIT 2
RXDATA3 : DATA BIT 3/CYCLIC PULSE GENERATOR 2 LOAD
RXDATA4 : DATA BIT 4/CYCLIC PULSE GENERATOR 2 ENABLE
RXDATA5 : DATA BIT 5
RXDATA6 : DATA BIT 6/CYCLIC PULSE GENERATOR 3 LOAD
RXDATA7 : DATA BIT 7/CYCLIC PULSE GENERATOR 3 ENABLE
RXDATA8 : DATA BIT 8
RXDATA9 : DATA BIT 9/CYCLIC PULSE GENERATOR 4 LOAD
RXDATA10 : DATA BIT 10/CYCLIC PULSE GENERATOR 4 ENABLE
RXDATA11 : DATA BIT 11
SADD : SERIAL ADDRESS BUS
SYNC : FRAME SYNC

OUTPUT

CLK MON : MONITOR DELAYED CLOCK
TXCLK : TRANSMIT CLOCK, 37.5 MHz
TXDATA0 : OUTPUT DATA BIT 0
TXDATA1 : OUTPUT DATA BIT 1
TXDATA2 : OUTPUT DATA BIT 2/CYCLIC PULSE GENERATOR 1 PULSE
TXDATA3 : OUTPUT DATA BIT 3
TXDATA4 : OUTPUT DATA BIT 4
TXDATA5 : OUTPUT DATA BIT 5/CYCLIC PULSE GENERATOR 2 PULSE
TXDATA6 : OUTPUT DATA BIT 6
TXDATA7 : OUTPUT DATA BIT 7
TXDATA8 : OUTPUT DATA BIT 8/CYCLIC PULSE GENERATOR 3 PULSE
TXDATA9 : OUTPUT DATA BIT 9
TXDATA10 : OUTPUT DATA BIT 10
TXDATA11 : OUTPUT DATA BIT 11/CYCLIC PULSE GENERATOR 4 PULSE

