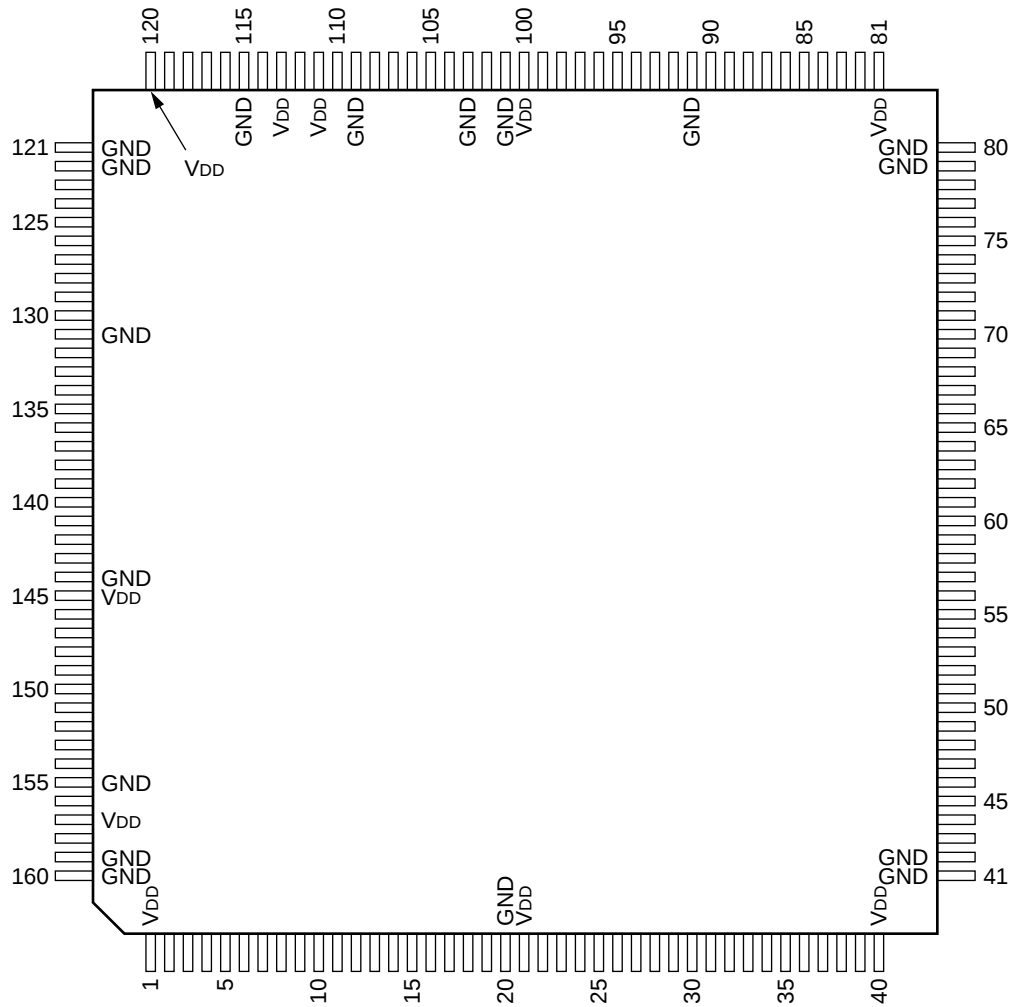


C-MOS SYNC GENERATOR DETECTOR WITH FLYWHEEL

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	41	—	GND	81	—	V _{DD}	121	—	GND
2	O	RC20M0	42	—	GND	82	I/O	CPU D0	122	—	GND
3	O	RC20M1	43	I	RESET	83	I/O	CPU D1	123	I	TESTEN
4	O	RC20M2	44	I	SMC	84	I/O	CPU D2	124	I	T SYSL0
5	O	RC20M3	45	I	SIN	85	I/O	CPU D3	125	I	T SYSL1
6	O	RC20M4	46	O	SOT	86	I/O	CPU D4	126	I	T TOPLD
7	O	RC20M5	47	O	RES OUT	87	I/O	CPU D5	127	I	T PRELD
8	O	RC20M6	48	I	S RES	88	I/O	CPU D6	128	I	TST SY1
9	O	RC20M7	49	I	P RES	89	I/O	CPU D7	129	O	T S8OUT
10	O	PB20M0	50	I	B32	90	I/O	CPU D8	130	O	F CTST
11	O	PB20M1	51	I	MODEL0	91	—	GND	131	O	NOV DI
12	O	PB20M2	52	I	MODEL1	92	O	CK RATE0	132	I	NOV DO
13	O	PB20M3	53	I	DIV4	93	O	CK RATE1	133	O	NOV CK
14	O	PB20M4	54	O	CS12	94	O	CK RATE2	134	O	NOV CS
15	O	PB20M5	55	O	CS11	95	O	RF D	135	I	SYNC
16	O	PB20M6	56	O	CS10	96	O	EXT	136	—	GND
17	O	PB20M7	57	O	CS9	97	O	16B	137	O	SYS SYNC
18	—	GND	58	O	CS8	98	O	512M	138	O	REC SYNC
19	I	CLK20	59	O	CS7	99	I	PLLOK	139	O	PB SYNC
20	—	V _{DD}	60	O	CS6	100	—	V _{DD}	140	O	VERI SYNC
21	—	GND	61	O	CS5	101	—	GND	141	O	RECT SYNC
22	O	RC16M0	62	O	CS4	102	I	CK16	142	O	PM SYNC
23	O	RC16M1	63	O	CS3	103	—	GND	143	O	DBG SY
24	O	RC16M2	64	O	CS2	104	I	SCK	144	—	GND
25	O	RC16M3	65	O	CS1	105	I	AMC	145	—	V _{DD}
26	O	RC16M4	66	O	CS0	106	I	T S8D0	146	I	PBT SY
27	O	RC16M5	67	I	S XCS	107	I	T S8D1	147	I	RCT SY
28	O	RC16M6	68	I	BD XCS	108	I	CLK64	148	I	PARI CHK
29	O	RC16M7	69	I	CPU XOE	109	—	GND	149	O	CKDTC
30	O	PB16M0	70	I	CPU XWE	110	I	CLK39	150	I	CKDTC
31	O	PB16M1	71	I	CPU A0	111	—	V _{DD}	151	O	P PLL
32	O	PB16M2	72	I	CPU A1	112	O	FB CK	152	O	I PLL
33	O	PB16M3	73	I	CPU A2	113	—	V _{DD}	153	O	ISY E
34	O	PB16M4	74	I	CPU A3	114	O	REF CK	154	O	SY LK
35	O	PB16M5	75	I	CPU A4	115	—	GND	155	—	GND
36	O	PB16M6	76	I	CPU A5	116	O	PLL CK	156	I	FREQ CK
37	O	PB16M7	77	I	CPU A6	117	O	T PRE0	157	—	V _{DD}
38	O	EE	78	I	CPU A7	118	O	T PRE1	158	I	CK32
39	O	XEE	79	—	GND	119	O	T JUDG	159	—	GND
40	—	V _{DD}	80	—	GND	120	—	V _{DD}	160	—	GND

INPUT

AMC	: FOR SCAN PATH TEST
B32	: OPEN=DIR-1000H, OTHERS=GND
BD XCS	: SELECT SIGNAL FOR THE PIF-29 BOARD
CK16	: 16 MHz CLOCK
CK32	: CLOCK FOR DETECTING THE FREQUENCY
CKDTC	: DIVIDED 16 MHz CLOCK
CLK20	: MEMORY READ CLOCK (REC, PB)
CLK39	: 39 MHz (MAX) PLL CLOCK
CLK64	: 64 MHz FIXED PLL CLOCK
CPU A0 - CPU A7	: CPU ADDRESS BUS
CPU XOE	: CPU BUS OUTPUT ENABLE
CPU XWE	: CPU BUS WRITE ENABLE
<u>DIV4</u>	: NORMALLY OPEN (GND=DIVISION OF 64 MHz TO 16 MHz)
FREQ CK	: FREQUENCY DETECTION CLOCK
MODEL0, MODEL1	: MODEL SELECT
NOV DO	: NOV RAM DATA
P RES	: POWER ON RESET
PARI CHK	: PARITY ERROR (HIGH=ERROR)
PBT SY	: TIMING SYNC FOR PB
PLLOK	: IF-655 BOARD PLL OK (HIGH=ERROR)
RCT SY	: TIMING SYNC FOR REC
<u>RESET</u>	: RESET
S RES	: SYSTEM RESET (LOW=ACTIVE)
S XCS	: CHIP SELECT
SCK	: FOR SCAN PATH TEST
SIN	: FOR SCAN PATH TEST
SMC	: FOR SCAN PATH TEST
SYNC	: INPUT SYNC
T PRELD	: FOR TEST
T S8D0, T S8D1	: FOR TEST
T SYSL0, T SYSL1	: FOR TEST
T TOPLD	: FOR TEST
<u>TESTEN</u>	: FOR TEST
TST SY1	: FOR TEST

OUTPUT

16B	: BUS SELECT ((LOW=16 BITS)
512M	: 512/400 SELECT (LOW=512)
CK RATE0 - CK RATE3	: RATE SELECT
CKDTC	: DIVIDED 16 MHz CLOCK
CS0 - CS12	: CHIP SELECT
DBG SY	: FOR TEST
EE	: EE/NORMAL SELECT (HIGH=EE, LOW=NORMAL)
EXT	: EXTERNAL/INTERNAL SIGNAL SELECT
F CTST	: FOR TEST
FB CK	: 2 MHz (MAX) FEEDBACK CLOCK
I PLL	: IF PLL LOCK
ISY E	: INPUT SYNC ERROR
NOV CK	: NOV RAM CLOCK
NOV CS	: NOV RAM CHIP SELECT
NOV DI	: NOV RAM DATA
PB SYNC	: OUTPUT SYNC FOR PB
PB16M0 PB16M7	: MEMORY WRITE
PB20M0 - PB20M7	: MEMORY READ
P PLL	: PIF PLL CLOCK
PLL CK	: PLL LOCK
PM SYNC	: OUTPUT SYNC FOR PM
RC16M0 - RC16M7	: MEMORY WRITE
RC20M0 - RC20M7	: MEMORY READ
REC SYNC	: OUTPUT SYNC FOR REC
RECT SYNC	: OUTPUT SYNC FOR RECT
REF CK	: 2MHz (MAX) REFERENCE CLOCK
RES OUT	: RESET
RF D	: EXTERNAL CONNECTOR SELECT
SOT	: FOR SCAN PATH TEST
SY LK	: SYNC LOCK
SYS SYNC	: OUTPUT SYNC FOR SSP
T JUDG	: FOR TEST
T S80UT	: FOR TEST
T PRE0, T PRE1	: FOR TEST
VERI SYNC	: OUTPUT SYNC FOR VERIFY
XEE	: EE/NORMAL SELECT (LOW=EE, HIGH=NORMAL)

INPUT/OUTPUT

CPU D0 - CPU D8	: DATA BUS
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