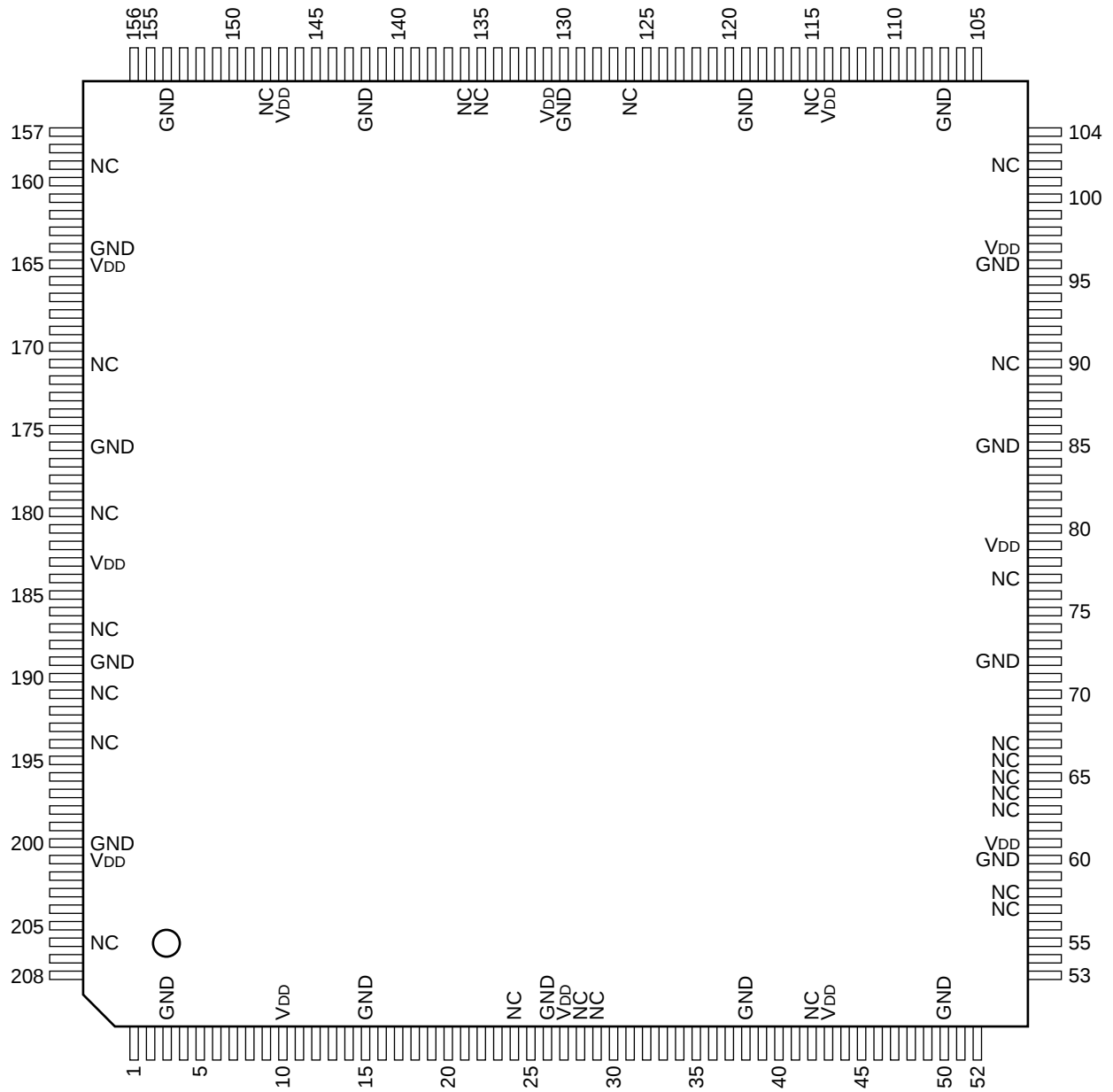


C-MOS ID1 FORMAT INNER ECC PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	CS PIO0	53	O	C2 DIV2	105	O	ENC BUSY	157	I/O	CF RDATA2
2	O	CS PIO1	54	O	C2 CLK MON	106	O	DEC BUSY	158	I/O	CF RDATA3
3	—	GND	55	O	C1 CLK MON	107	—	GND	159	—	NC
4	O	CS DA 0	56	O	PLL LOCK	108	O	DEC INIT	160	I/O	CF RDATA4
5	O	CS DA 1	57	—	NC	109	O	DEC STB	161	I/O	CF RDATA5
6	O	CS C2 A	58	—	NC	110	O	DEC BLK	162	I/O	CF RDATA6
7	O	CS C2 E	59	I	C2 CLK	111	O	RF VALID	163	I/O	CF RDATA7
8	O	CS C1 A	60	—	GND	112	O	DEC DO0	164	—	GND
9	O	CS C1 E	61	—	VDD	113	O	DEC DO1	165	—	VDD
10	—	VDD	62	I	RFCK OSC	114	—	VDD	166	I/O	CF RDATA8
11	I	CPU OE	63	—	NC	115	—	NC	167	O	CF WDATA0
12	I	CS IN	64	—	NC	116	O	DEC DO2	168	O	CF WDATA1
13	I	CPU CS	65	—	NC	117	O	DEC DO3	169	I	INV IN2
14	I	CPU WE	66	—	NC	118	O	DEC DO4	170	O	INV OUT2
15	—	GND	67	—	NC	119	—	GND	171	—	NC
16	I	CPU ADDR0	68	O	ENC 2T	120	O	DEC DO5	172	O	CF WDATA2
17	I	CPU ADDR1	69	O	ENC SD	121	O	DEC DO6	173	O	CF WDATA3
18	I	CPU ADDR2	70	O	ENC RFCK	122	O	DEC DO7	174	O	CF WDATA4
19	I	CPU ADDR3	71	O	ENC PDO0	123	O	DEC DO8	175	O	CF WDATA5
20	I	CPU ADDR4	72	—	GND	124	I	INV IN1	176	—	GND
21	I	CPU ADDR5	73	O	ENC PDO1	125	O	INV OUT1	177	O	CF WDATA6
22	I	CPU ADDR6	74	O	ENC PDO2	126	—	NC	178	O	CF WDATA7
23	I	CPU ADDR7	75	O	ENC PDO3	127	O	BLK LAMP	179	O	CF WDATA8
24	—	NC	76	O	ENC PDO4	128	O	ERR BLK	180	—	NC
25	I	CLK10E	77	—	NC	129	O	ERR FLAG	181	O	CF WR
26	—	GND	78	I	LD	130	—	GND	182	O	CLK10D
27	—	VDD	79	—	VDD	131	—	VDD	183	—	VDD
28	—	NC	80	I	SM	132	O	PI MODE	184	I	ACK
29	—	NC	81	I	TST	133	O	SFW WORK	185	I	BCK
30	I	REC HEAD	82	I	SDI0	134	O	RAW SYNC	186	I	TCK
31	I	RH POLAR	83	I	SDI1	135	—	NC	187	—	NC
32	I	RST IN1	84	I	MLT	136	—	NC	188	I	DEC RFCK
33	I	RST IN2	85	—	GND	137	O	DF RR	189	—	GND
34	I	ECC TST	86	O	ENC PDO5	138	O	DF WR	190	I	DEC SD
35	I	TEST	87	O	ENC PDO6	139	I	DF RDATA0	191	—	NC
36	I	CKRSTIN	88	O	ENC PDO7	140	I	DF RDATA1	192	O	SDO0
37	I	RESET	89	O	ENC PDO8	141	I	DF RDATA2	193	O	SDO1
38	—	GND	90	—	NC	142	—	GND	194	—	NC
39	I	C2 HALF	91	I	ENC DI0	143	I	DF RDATA3	195	I/O	CPU DATA0
40	I	PB HEAD	92	I	ENC DI1	144	I	DF RDATA4	196	I/O	CPU DATA1
41	I	PH POLAR	93	I	ENC DI2	145	I	DF RDATA5	197	I/O	CPU DATA2
42	—	NC	94	I	ENC DI3	146	I	DF RDATA6	198	I/O	CPU DATA3
43	—	VDD	95	I	ENC DI4	147	—	VDD	199	I/O	CPU DATA4
44	O	RST OUT	96	—	GND	148	—	NC	200	—	GND
45	O	CK DIV D0	97	—	VDD	149	I	DF RDATA7	201	—	VDD
46	O	CK DIV D1	98	I	ENC DI5	150	I	DF RDATA8	202	I/O	CPU DATA5
47	O	CK DIV D2	99	I	ENC DI6	151	O	CF RR	203	I/O	CPU DATA6
48	O	CK DIV D3	100	I	ENC DI7	152	O	CF OE0	204	I/O	CPU DATA7
49	O	EXT DIV	101	I	ENC DI8	153	O	CF OE	205	I/O	CPU DATA8
50	—	GND	102	—	NC	154	—	GND	206	—	NC
51	O	PARA CLK	103	O	ENC INIT	155	I/O	CF RDATA0	207	I	VPD
52	O	REC 400	104	O	ENC STB	156	I/O	CF RDATA1	208	O	CS AUX

