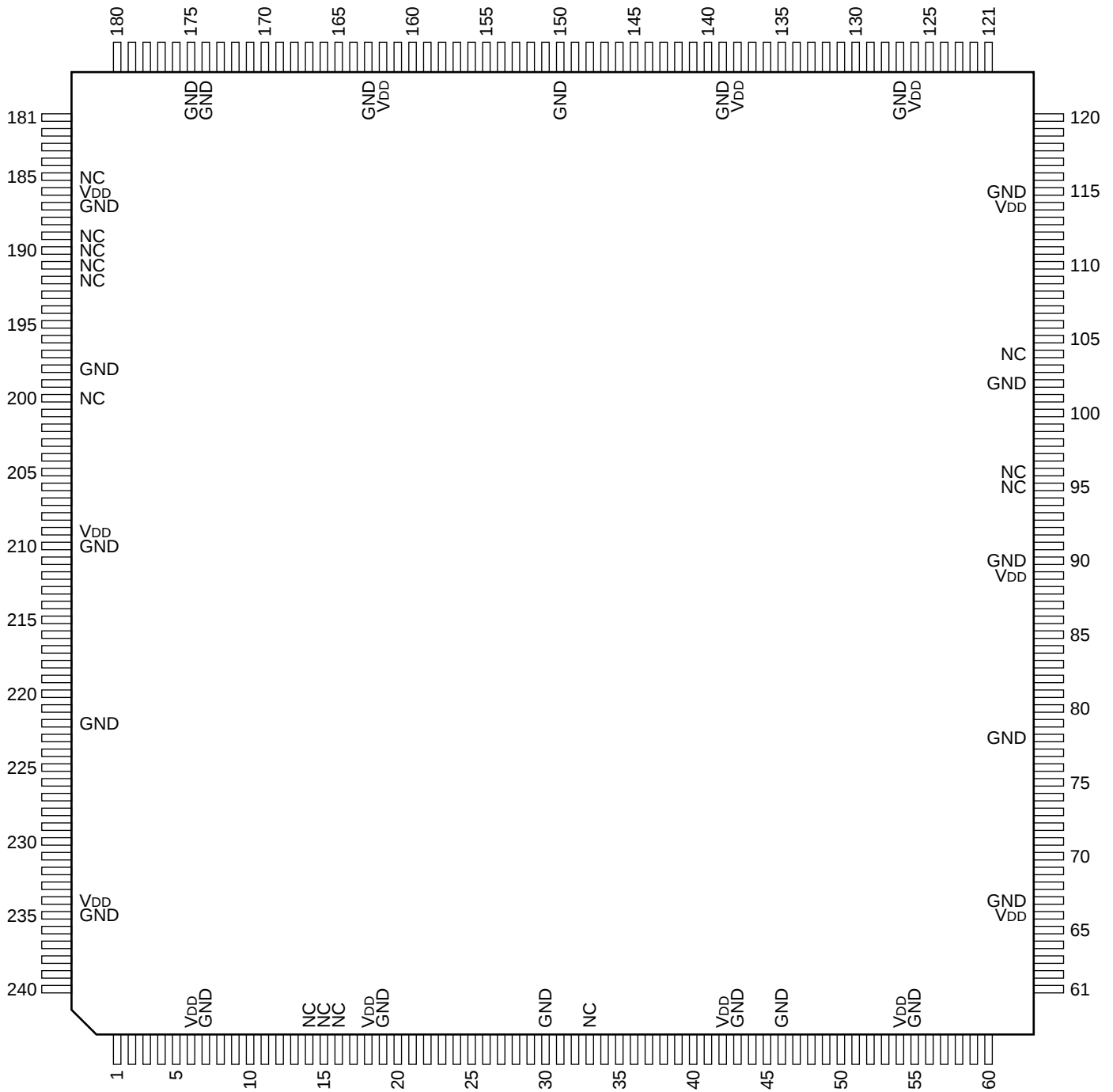


C-MOS COLOR CORRECTION (GATE ARRAY)

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	PROE	61	I	TEST0	121	O	VRO0	181	O	TOUTY12
2	O	EXTCTL2	62	I	WSRC113	122	O	VRO1	182	O	TOUTY13
3	O	EXTCTL1	63	I	WSRC112	123	O	VRO2	183	O	TOUTY14
4	O	EXTCTL0	64	I	WSRC111	124	O	VRO3	184	O	TOUTY15
5	I/O	SDAT	65	I	WSRC110	125	O	VRO4	185	—	NC
6	—	VDD	66	—	VDD	126	—	VDD	186	—	VDD
7	—	GND	67	—	GND	127	—	VSS	187	—	GND
8	I	CKD	68	I	WSRC19	128	O	VRO5	188	I	TRST
9	I	SADD	69	I	WSRC18	129	O	VRO6	189	—	NC
10	I	CS0	70	I	WSRC17	130	O	VRO7	190	—	NC
11	I	CS1	71	I	WSRC16	131	O	VRO8	191	—	NC
12	I	CS2	72	I	WSRC15	132	O	VRO9	192	—	NC
13	I	CS3	73	I	WSRC14	133	O	VRO10	193	I	TEST
14	—	NC	74	I	WSRC13	134	O	VRO11	194	I	XSM
15	—	NC	75	I	WSRC12	135	I	OEVR	195	I	XTST
16	—	NC	76	I	WSRC11	136	O	TOUTV12	196	I	TESTRAM
17	I	RST	77	I	WSRC10	137	O	TOUTV13	197	I	MTESTC
18	—	VDD	78	—	GND	138	—	VDD	198	—	GND
19	—	GND	79	I	WSRC213	139	—	GND	199	I	MTESTB
20	I	YGI11	80	I	WSRC212	140	O	TOUTV14	200	—	NC
21	I	YGI10	81	I	WSRC211	141	O	TOUTV15	201	I	SP0
22	I	YGI9	82	I	WSRC210	142	O	TEST1	202	I	SP1
23	I	YGI8	83	I	WSRC29	143	O	TEST2	203	I	SP2
24	I	YGI7	84	I	WSRC28	144	O	UBO0	204	I	SP3
25	I	YGI6	85	I	WSRC27	145	O	UBO1	205	I	PRE
26	I	YGI5	86	I	WSRC26	146	O	UBO2	206	I	PWE
27	I	YGI4	87	I	WSRC25	147	O	UBO3	207	I	PCS
28	I	YGI3	88	I	WSRC24	148	O	UBO4	208	I	MTESTA
29	I	YGI2	89	—	VDD	149	O	UBO5	209	—	VDD
30	—	GND	90	—	GND	150	—	GND	210	—	GND
31	I	YGI1	91	I	WSRC23	151	O	UBO6	211	I	MTEST
32	I	YGI0	92	I	WSRC22	152	O	UBO7	212	I	PADD8
33	I	UBI11	93	I	WSRC21	153	O	UBO8	213	I	PADD7
34	I	UBI10	94	I	WSRC20	154	O	UBO9	214	I	PADD6
35	I	UBI9	95	—	NC	155	O	UBO10	215	I	PADD5
36	I	UBI8	96	—	NC	156	O	UBO11	216	I	PADD4
37	I	UBI7	97	I	SC	157	O	OEUB	217	I	PADD3
38	I	UBI6	98	I	BLANK	158	O	TOUTU12	218	I	PADD2
39	I	UBI5	99	I	VD	159	O	TOUTU13	219	I	PADD1
40	I	UBI4	100	I	HD	160	O	TOUTU14	220	I	PADD0
41	I	UBI3	101	I	CKX	161	O	TOUTU15	221	I	PCK
42	—	VDD	102	—	GND	162	—	VDD	222	—	GND
43	—	GND	103	I	CK	163	—	GND	223	I/O	PDAT15
44	I	UBI2	104	—	NC	164	O	TEST3	224	I/O	PDAT14
45	I	UBI1	105	I	WSRC313	165	O	TEST4	225	I/O	PDAT13
46	I	UBI0	106	I	WSRC312	166	O	YGO0	226	I/O	PDAT12
47	I	VRI11	107	I	WSRC311	167	O	YGO1	227	I/O	PDAT11
48	I	VRI10	108	I	WSRC310	168	O	YGO2	228	I/O	PDAT10
49	I	VRI9	109	I	WSRC39	169	O	YGO3	229	I/O	PDAT9
50	I	VRI8	110	I	WSRC38	170	O	YGO4	230	I/O	PDAT8
51	I	VRI7	111	I	WSRC37	171	O	YGO5	231	I/O	PDAT7
52	I	VRI6	112	I	WSRC36	172	O	YGO6	232	I/O	PDAT6
53	I	VRI5	113	I	WSRC35	173	O	YGO7	233	I/O	PDAT5
54	—	VDD	114	—	VDD	174	—	VDD	234	—	VDD
55	—	GND	115	—	GND	175	—	GND	235	—	GND
56	I/O	VRI4	116	I	WSRC34	176	O	YGO8	236	I/O	PDAT4
57	I	VRI3	117	I	WSRC33	177	O	YGO9	237	I/O	PDAT3
58	I	VRI2	118	I	WSRC32	178	O	YGO10	238	I/O	PDAT2
59	I	VRI1	119	I	WSRC31	179	O	YGO11	239	I/O	PDAT1
60	I	VRI0	120	I	WSRC30	180	I	OEYG	240	I/O	PDAT0

INPUT

BLANK	; BLANKING PULSE
CKD	; CLOCK FOR SERIAL INTERFACE (SIF)
CKX	; REGISTER SWITCHING CLOCK FOR SIF/PIF
CS3 - CS0	; CHIP SELECT FOR SIF
HD	; HORIZONTAL DRIVE PULSE
MTEST	; TEST MODE PIN (H: TEST, L: NORMAL)
MTESTA	; TEST PIN
MTESTB	; TEST PIN
MTESTC	; TEST PIN
OEUB	; OUTPUT ENABLE FOR UBO
OEVR	; OUTPUT ENABLE FOR VRO
OEYG	; OUTPUT ENABLE FOR YGO
PADD8 - PADD0	; PARALLEL ADDRESS FOR PIF
PCK	; CLOCK FOR PARALLEL INTERFACE (PIF)
PCS	; CHIP SELECT FOR PIF (L: ACTIVE)
PRE	; READ ENABLE FOR PIF (L: ENABLE)
PWE	; WRITE ENABLE FOR PIF (L: ENABLE)
SADD	; SERIAL ADDRESS FOR SIF
SC	; SUBCARRIER FOR COMPOSITE SYSTEM
SP3 - SP0	; SIF/PIF MODE SELECT
TEST	; TEST FOR PULL-UP/DOWN
TESTRAM	; TEST PIN FOR RAM TEST
TRST	; ALL CLEAR
UBI11 - UBI0	; VIDEO INPUT
VD	; VERTICAL DRIVE PULSE
VRI11 - VRI0	; VIDEO INPUT
WSRC110 - WSRC 10	; MASK KEY INPUT FOR PRIMARY CCR
WSRC113 - WSRC 111	; TEST
WSRC210 - WSRC 20	; MASK KEY INPUT FOR SECONDARY CCR
WSRC213 - WSRC 211	; TEST
WSRC310 - WSRC 30	; MASK KEY INPUT FOR YUV CCR
WSRC313 - WSRC 311	; TEST
XSM	; TEST PIN FOR SCAN TEST
XTST	; TEST PIN FOR SCAN TEST
YGI11 - YGI0	; VIDEO INPUT

OUTPUT

EXCTL2 - EXCTL0	; EXTERNAL DEVICE CONTROL
PROE	; PIF READ STATUS (H: READ)
TEST3 - TEST0	; TEST OUTPUTS FOR SCAN TEST
TOUTU15 - TOUTU12	; TEST
TOUTV15 - TOUTV12	; TEST
TOUTY15 - TOUTY12	; TEST
UBO11 - VBO0	; VIDEO OUTPUT
VRO11 - VRO0	; VIDEO OUTPUT
YGO11 - YGO0	; VIDEO OUTPUT

INPUT/OUTPUT

PDAT15 - PDAT0	; PARALLEL DATA BUS FOR PIF
SDAT	; SERIAL DATA BUS FOR SIF

