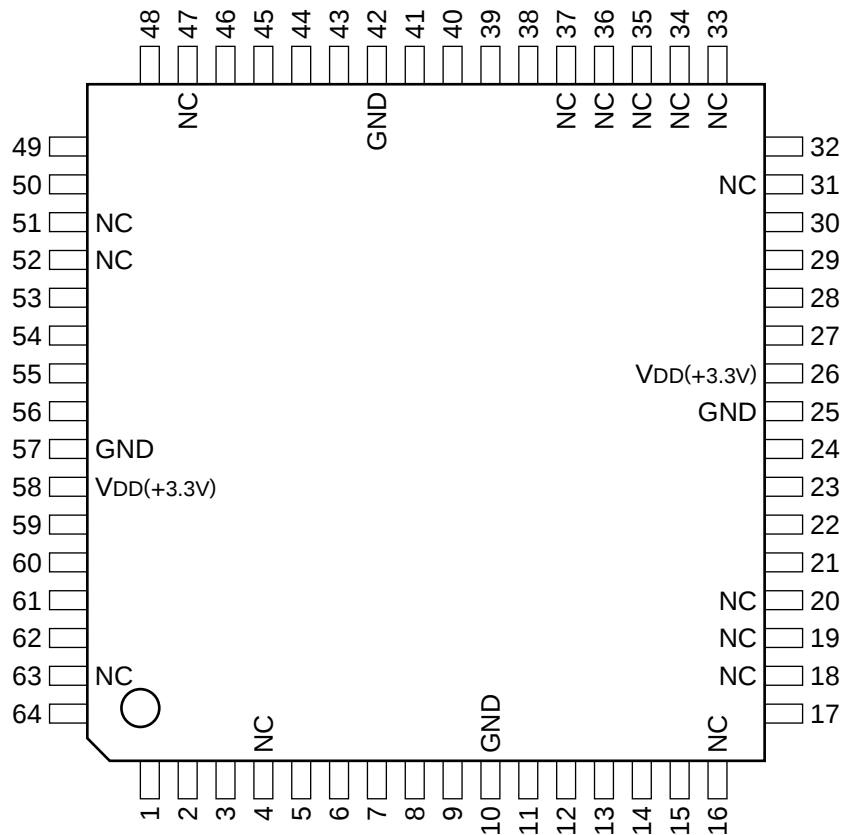

C-MOS AUDIO DATA RATE CONVERTER
—TOP VIEW—



(VDD = +3.3V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	F2LRCK	17	I	DLBCK	33	—	NC	49	I	F1LRCK
2	O	LATCH2	18	—	NC	34	—	NC	50	O	LATCH1
3	O	LOAD	19	—	NC	35	—	NC	51	—	NC
4	—	NC	20	—	NC	36	—	NC	52	—	NC
5	I	DL1IN	21	I	S4IN1	37	—	NC	53	I	F2IN1
6	O	DL1TAP1	22	O	S4OUT1	38	I	F1IN1	54	O	F2OUT1
7	O	DL1TAP2	23	I	S4IN2	39	O	F1OUT1	55	I	F2IN2
8	O	DL1TAP3	24	O	S4OUT2	40	I	F1IN2	56	O	F2OUT2
9	O	DL1TAP4	25	—	GND	41	O	F1OUT2	57	—	GND
10	—	GND	26	—	VDD	42	—	GND	58	—	VDD
11	I	DL2IN	27	I	S4IN3	43	I	F1IN3	59	I	F2IN3
12	O	DL2TAP1	28	O	S4OUT3	44	O	F1OUT3	60	O	F2OUT3
13	O	DL2TAP2	29	I	S4IN4	45	I	F1IN4	61	I	F2IN4
14	O	DL2TAP3	30	O	S4OUT4	46	O	F1OUT4	62	O	F2OUT4
15	O	DL2TAP4	31	—	NC	47	—	NC	63	—	NC
16	—	NC	32	I	S4BCK	48	I	F1BCK	64	I	F2BCK

