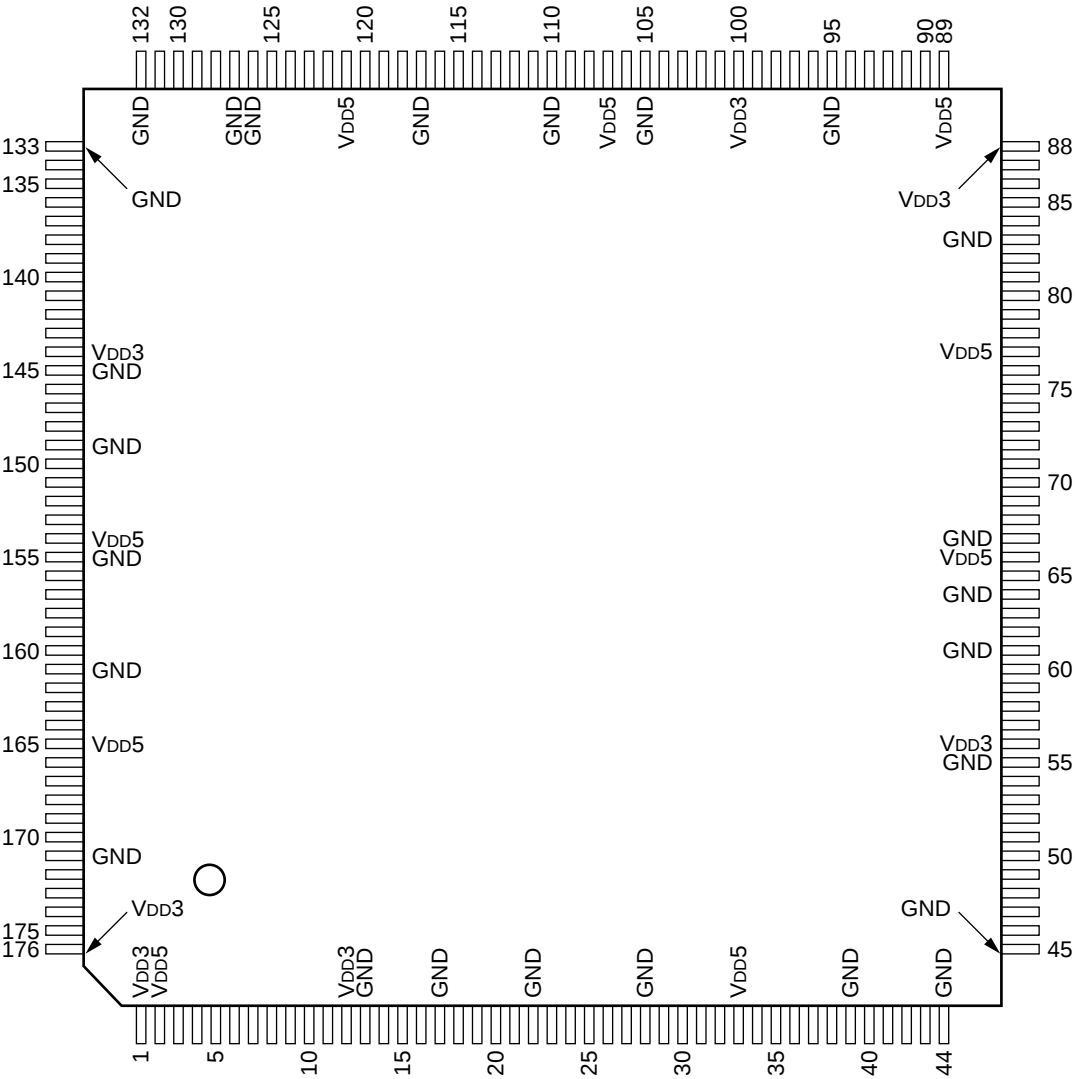


C-MOS VIDEO MODIFIER
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD3	45	—	GND	89	—	VDD5	133	—	GND
2	—	VDD5	46	I	IN31	90	I	LD1	134	I	IN09
3	I	IN2B	47	I	IN30	91	I	EN1	135	I	IN08
4	I	IN2A	48	I	IN4B	92	I	LD2	136	I	IN07
5	I	IN29	49	I	IN4A	93	I	EN2	137	I	IN06
6	I	IN28	50	I	IN49	94	I	TNIN	138	I	IN05
7	I	IN27	51	I	IN48	95	—	GND	139	I	IN04
8	I	IN26	52	I	IN47	96	O	DT1CO	140	I	IN03
9	I	IN25	53	I	IN46	97	O	DT1YO	141	I	IN02
10	I	IN24	54	I	IN45	98	O	DT2O	142	I	IN01
11	I	IN23	55	—	GND	99	I/O	OUT3B	143	I	IN00
12	—	VDD3	56	—	VDD3	100	—	VDD3	144	—	VDD3
13	—	GND	57	O	OUT2B	101	I/O	OUT3A	145	—	GND
14	O	OUT1B	58	O	OUT2A	102	I/O	OUT39	146	I/O	OUT0B
15	O	OUT1A	59	O	OUT29	103	I/O	OUT38	147	I/O	OUT0A
16	O	OUT19	60	O	OUT28	104	I/O	OUT37	148	I/O	OUT09
17	—	GND	61	—	GND	105	—	GND	149	—	GND
18	O	OUT18	62	O	OUT27	106	O	CKD	150	I/O	OUT08
19	O	OUT17	63	O	OUT26	107	—	VDD5	151	I/O	OUT07
20	O	OUT16	64	—	GND	108	I/O	SDAT	152	I/O	OUT06
21	O	OUT15	65	I	CK	109	I/O	OUT36	153	I/O	OUT05
22	—	GND	66	—	VDD5	110	—	GND	154	—	VDD5
23	O	OUT14	67	—	GND	111	I/O	OUT35	155	—	GND
24	O	OUT13	68	I	CKS	112	I/O	OUT34	156	I/O	OUT04
25	O	OUT12	69	O	OUT25	113	I/O	OUT33	157	I/O	OUT03
26	O	OUT11	70	O	OUT24	114	I/O	OUT32	158	I/O	OUT02
27	O	OUT10	71	O	OUT23	115	I/O	OUT31	159	I/O	OUT01
28	—	GND	72	O	OUT22	116	I/O	OUT30	160	I/O	OUT00
29	I	IN22	73	O	OUT21	117	—	GND	161	—	GND
30	I	IN21	74	O	OUT20	118	I	SADD	162	I	IN1B
31	I	IN20	75	I	IN44	119	I	CKX	163	I	IN1A
32	I	IN3B	76	I	IN43	120	I	CS3	164	I	IN19
33	—	VDD5	77	—	VDD5	121	—	VDD5	165	—	VDD5
34	I	IN3A	78	I	IN42	122	I	CS2	166	I	IN18
35	I	IN39	79	I	IN41	123	I	CS1	167	I	IN17
36	I	IN38	80	I	IN40	124	I	CS0	168	I	IN16
37	I	IN37	81	I	OE0	125	I	CTIM	169	I	IN15
38	I	IN36	82	I	OE1	126	—	GND	170	I	IN14
39	—	GND	83	—	GND	127	—	GND	171	—	GND
40	I	IN35	84	I	OE2	128	O	TSTO	172	I	IN13
41	I	IN34	85	I	OE3	129	I	RST	173	I	IN12
42	I	IN33	86	I	DT1I	130	I	IN0B	174	I	IN11
43	I	IN32	87	I	DT2I	131	I	IN0A	175	I	IN10
44	—	GND	88	—	VDD3	132	—	GND	176	—	VDD3

143	IN00	OUT00	160
142	IN01	OUT01	159
141	IN02	OUT02	158
140	IN03	OUT03	157
139	IN04	OUT04	156
138	IN05	OUT05	153
137	IN06	OUT06	152
136	IN07	OUT07	151
135	IN08	OUT08	150
134	IN09	OUT09	148
131	IN0A	OUT0A	147
130	IN0B	OUT0B	146
175	IN10	OUT10	27
174	IN11	OUT11	26
173	IN12	OUT12	25
172	IN13	OUT13	24
170	IN14	OUT14	23
169	IN15	OUT15	21
168	IN16	OUT16	20
167	IN17	OUT17	19
166	IN18	OUT18	18
164	IN19	OUT19	16
163	IN1A	OUT1A	15
162	IN1B	OUT1B	14
31	IN20	OUT20	74
30	IN21	OUT21	73
29	IN22	OUT22	72
11	IN23	OUT23	71
10	IN24	OUT24	70
9	IN25	OUT25	69
8	IN26	OUT26	63
7	IN27	OUT27	62
6	IN28	OUT28	60
5	IN29	OUT29	59
4	IN2A	OUT2A	58
3	IN2B	OUT2B	57
47	IN30	OUT30	116
46	IN31	OUT31	115
43	IN32	OUT32	114
42	IN33	OUT33	113
41	IN34	OUT34	112
40	IN35	OUT35	111
38	IN36	OUT36	109
37	IN37	OUT37	104
36	IN38	OUT38	103
35	IN39	OUT39	102
34	IN3A	OUT3A	101
32	IN3B	OUT3B	99
80	IN40	DT1Y0	97
79	IN41	DT1C0	140
78	IN42	DT20	98
76	IN43		
75	IN44	OE0	81
54	IN45	OE1	82
53	IN46	OE2	84
52	IN47	OE3	85
51	IN48		
50	IN49	DT11	86
49	IN4A	DT21	87
48	IN4B		
		TN IN	94
		TSTO	128
108	SDAT		
118	SADD		
65	CK		
68	CKS		
106	CKO		
119	CKX		
128	RST		
124	CS0		
123	CS1		
122	CS2		
120	CS3		
90	LD1		
92	LD2		
91	EN1		
93	EN2		

INPUT

CK ; SYSTEM CLOCK
 CKD ; CLOCK FOR CS0, CS1, CS2, SADD, SDAT
 CKS ; CLOCK FOR SAMPLE AND HOLD CIRCUIT IN OUT1
 CKX ; TIMING SIGNAL FOR SERIAL CONTROL EXECUTION
 CS0 - CS3 ; CHIP SELECT
 CTIM ; COLOR TIMING SPECIFIED
 DT1I, DT2I ; SAMPLE PULSE FOR SAMPLE AND HOLD CIRCUIT IN OUT1, OUT2
 EN1, EN2 ; NAMADD1, NAMADD2 CIRCUIT ENABLE (IN COUNTER MODE)
 IN00 - IN0B, IN10 - IN1B, ; DATA
 IN20 - IN2B, IN30 - IN3B,
 IN40 - IN4B
 LD1, LD2 ; NAMADD1, NAMADD2 CIRCUIT LOAD PIN (IN COUNTER MODE)
 OE0 - OE3 ; OUTPUT ENABLE FOR OUT0, OUT1, OUT2, OUT3
 SADD ; SERIAL ADDRESS
 RST ; POWER RESET
 TN IN ; TEST TERMINAL

OUTPUT

DT1YO, DT1CO, DT2O ; SAMPLE PULSE
 OUT10 - OUT1B, ; DATA
 OUT20 - OUT2B
 TSTO ; TEST TERMINAL

INPUT/OUTPUT

OUT00 - OUT0B, ; DATA
 OUT30 - OUT3B
 SDAT ; SERIAL DATA

