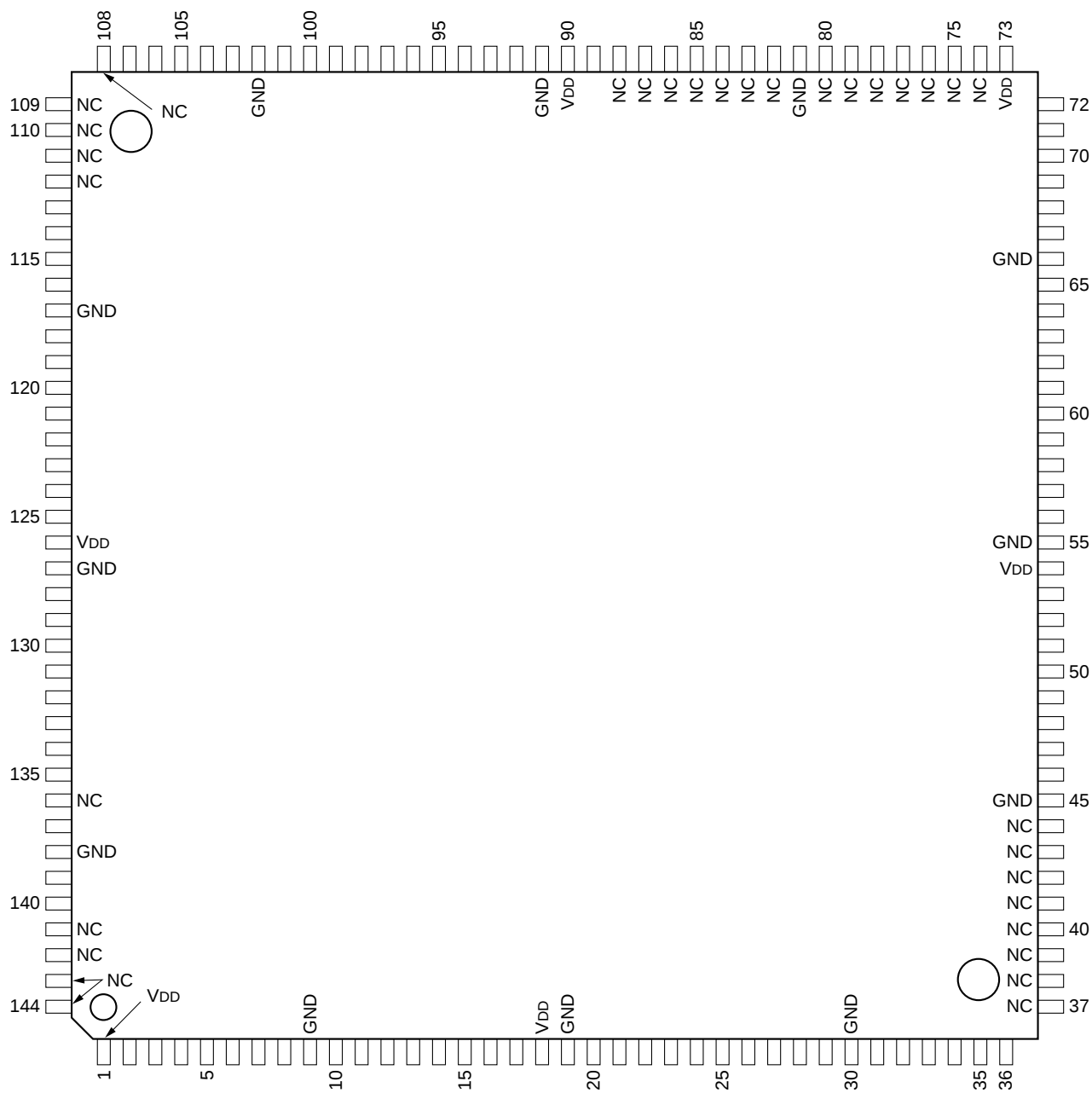


C-MOS FIR DIGITAL FILTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	37	—	NC	73	—	V _{DD}	109	—	NC
2	I	AIGOP	38	—	NC	74	—	NC	110	—	NC
3	I	AIPR	39	—	NC	75	—	NC	111	—	NC
4	I	AICF	40	—	NC	76	—	NC	112	—	NC
5	I	AIVD	41	—	NC	77	—	NC	113	I	$\overline{\text{CS}}$
6	I	AIHD	42	—	NC	78	—	NC	114	I	STRB
7	I	AIN9	43	—	NC	79	—	NC	115	I	STAT0
8	I	AIN8	44	—	NC	80	—	NC	116	I	STAT1
9	—	GND	45	—	GND	81	—	GND	117	—	GND
10	I	AIN7	46	O	MPL23	82	—	NC	118	I/O	SYIO0
11	I	AIN6	47	O	MPL22	83	—	NC	119	I/O	SYIO1
12	I	AIN5	48	O	MPL21	84	—	NC	120	I/O	SYIO2
13	I	AIN4	49	O	MPL20	85	—	NC	121	I/O	SYIO3
14	I	AIN3	50	O	MPL19	86	—	NC	122	I/O	SYIO4
15	I	AIN2	51	O	MPL18	87	—	NC	123	I/O	SYIO5
16	I	AIN1	52	O	MPL17	88	—	NC	124	I/O	SYIO6
17	I	AIN0	53	O	MPL16	89	I	$\overline{\text{CLR}}$	125	I/O	SYIO7
18	—	V _{DD}	54	—	V _{DD}	90	—	V _{DD}	126	—	V _{DD}
19	—	GND	55	—	GND	91	—	GND	127	—	GND
20	I	TUD	56	O	MPL15	92	O	OUT0	128	I	TEST0
21	I	BIGOP	57	O	MPL14	93	O	OUT1	129	I	TEST1
22	I	BIPR	58	O	MPL13	94	O	OUT2	130	I	TEST2
23	I	BICF	59	O	MPL12	95	O	OUT3	131	I	TEST3
24	I	BIVD	60	O	MPL11	96	O	OUT4	132	I	TEST4
25	I	BIHD	61	O	MPL10	97	O	OUT5	133	I	TEST5
26	I	BIN9	62	O	MPL9	98	O	OUT6	134	I	TEST6
27	I	BIN8	63	O	MPL8	99	O	OUT7	135	I	TEST7
28	I	BIN7	64	O	MPL7	100	O	OUT8	136	—	NC
29	I	BIN6	65	O	MPL6	101	O	OUT9	137	I	CK
30	—	GND	66	—	GND	102	—	GND	138	—	GND
31	I	BIN5	67	O	MPL5	103	O	OHD	139	I/O	CKH
32	I	BIN4	68	O	MPL4	104	O	OVD	140	I/O	CKQ
33	I	BIN3	69	O	MPL3	105	O	OCF	141	—	NC
34	I	BIN2	70	O	MPL2	106	O	OPR	142	—	NC
35	I	BIN1	71	O	MPL1	107	O	OGOP	143	—	NC
36	I	BIN0	72	O	MPL0	108	—	NC	144	—	NC

INPUT

AICF	: CH-A COLOR FRAMING INFORMATION
AIGOP	: CH-A GOP
AIHD	: CH-A HORIZONTAL DRIVE
AIN0 - AIN9	: CH-A VIDEO DATA
AIPR	: CH-A PARITY BIT
AIVD	: CH-A VERTICAL DRIVE
BICF	: CH-B COLOR FRAMING INFORMATION
BIGOP	: CH-B GOP
BIHD	: CH-B HORIZONTAL DRIVE
BIN0 - BIN9	: CH-B VIDEO DATA
BIPR	: CH-B PARITY BIT
BIVD	: CH-B VERTICAL DRIVE
CK	: 27 MHz SYSTEM CLOCK
$\overline{\text{CLR}}$	: INTERNAL FLIP FLOP CLEAR
$\overline{\text{CS}}$	: CHIP SELECT
STAT0, STAT1	: STATUS BITS
STRB	: STROBE PULSE
TEST0 - TEST7	: TEST SETTING BITS
TUD	: TEST

OUTPUT

MPL0 - MPL23	: MULTIPLIER DATA
OCF	: CF INFORMATION FOR SELECTED DATA
OGOP	: GOP FOR SELECTED DATA
OHD	: H-DRIVE FOR SELECTED DATA
OPR	: PARITY BIT FOR SELECTED DATA
OUT0 - OUT9	: SELECTED VIDEO DATA
OVD	: V-DRIVE FOR SELECTED DATA

INPUT/OUTPUT

CKH, CKQ	: COEFFICIENT ADDRESS SETTING
SYIO0 - SYIO7	: SYSTEM CONTROL DATA BUS

