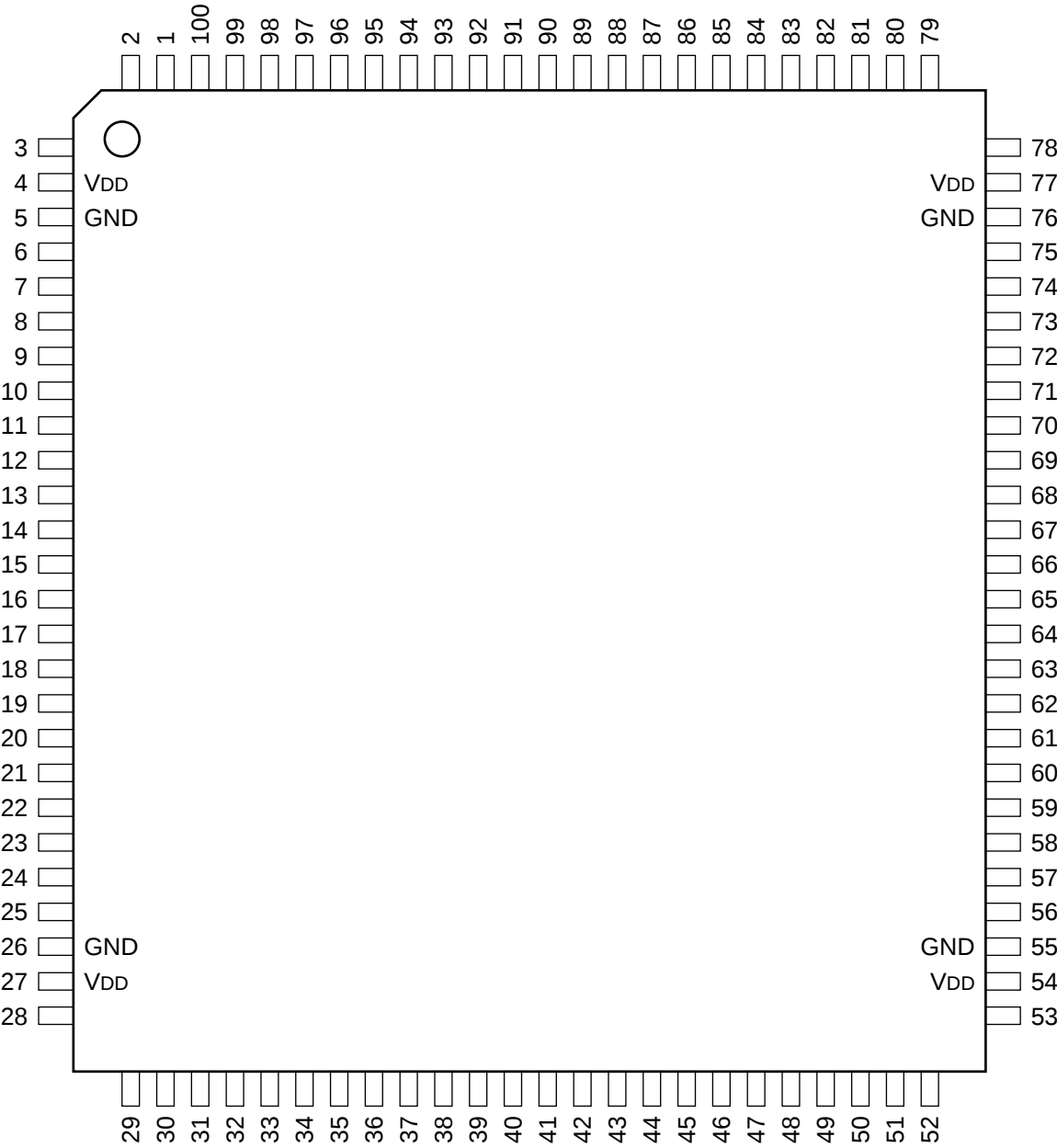


C-MOS DIGITAL FILTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	EI5	26	—	GND	51	O	EO8	76	—	GND
2	I	EI4	27	—	V _{DD}	52	O	EO9	77	—	V _{DD}
3	I	EI3	28	I	DI9	53	O	EO10	78	I	$\overline{\text{OE}}$
4	—	V _{DD}	29	I	DI8	54	—	V _{DD}	79	I	CK
5	—	GND	30	I	DI7	55	—	GND	80	O	DO5
6	I	EI2	31	I	DI6	56	O	EO11	81	O	DO6
7	I	EI1	32	I	DI5	57	O	SO0	82	O	DO7
8	I	EI0	33	I	DI4	58	O	SO1	83	O	DO8
9	I	SI13	34	I	DI3	59	O	SO2	84	O	DO9
10	I	SI12	35	I	DI2	60	O	SO3	85	O	DO10
11	I	SI11	36	I	DI1	61	O	SO4	86	O	DO11
12	I	SI10	37	I	DI0	62	O	SO5	87	O	PO
13	I	SI9	38	I	CKXI	63	O	SO6	88	O	CKXO
14	I	SI8	39	O	DATA	64	O	SO7	89	I	MOD1
15	I	SI7	40	I	CKD	65	O	SO8	90	I	MOD0
16	I	SI6	41	I	$\overline{\text{CS}}$	66	O	SO9	91	I	HD
17	I	SI5	42	I	OB2S	67	O	SO10	92	I	MYSTERY
18	I	SI4	43	O	EO0	68	O	SO11	93	I	LM1
19	I	SI3	44	O	EO1	69	O	SO12	94	I	LM0
20	I	SI2	45	O	EO2	70	O	SO13	95	I	EI11
21	I	SI1	46	O	EO3	71	O	DO0	96	I	EI10
22	I	SI0	47	O	EO4	72	O	DO1	97	I	EI9
23	I	RND	48	O	EO5	73	O	DO2	98	I	EI8
24	I	DI11	49	O	EO6	74	O	DO3	99	I	EI7
25	I	DI10	50	O	EO7	75	O	DO4	100	I	EI6

INPUT

CK	: DEVICE CLOCK - MAX 28.6 MHz
CKD	: SERIAL INTERFACE DATA CLOCK
CKXI	: SERIAL INTERFACE LATCH CLOCK
$\overline{\text{CS}}$	: SERIAL INTERFACE CHIP SELECT
DI0 - DI11	: VIDEO DATA
DIO	: SERIAL INTERFACE DATA
EI0 - EI11	: EXPANSION DATA
HD	: SYNC
LM0, LM1	: LIMITER CONTROL
MOD0, MOD1	: FILTER MODE CONTROL
MYSTERY	: CXD8055 EMULATION CONTROL
OB2S	: NUMBER FORMAT
$\overline{\text{OE}}$	: TRISTATE CONTROL FOR OUTPUT
RND	: PARTIAL SUM ROUNDING BIT
SI0 - SI13	: FILTER PARTIAL SUM

OUTPUT

CKXO	: LATCH CLOCK EXPANSION
DATA	: DATA
DO0 - DO11	: OUTPUT BUFFERS
EO0 - EO11	: EXPANSION BUS
PO	: PARAMETRIC OUTPUT - DEVICE TEST ONLY
SO0 - SO13	: TRISTATE FILTER SUM