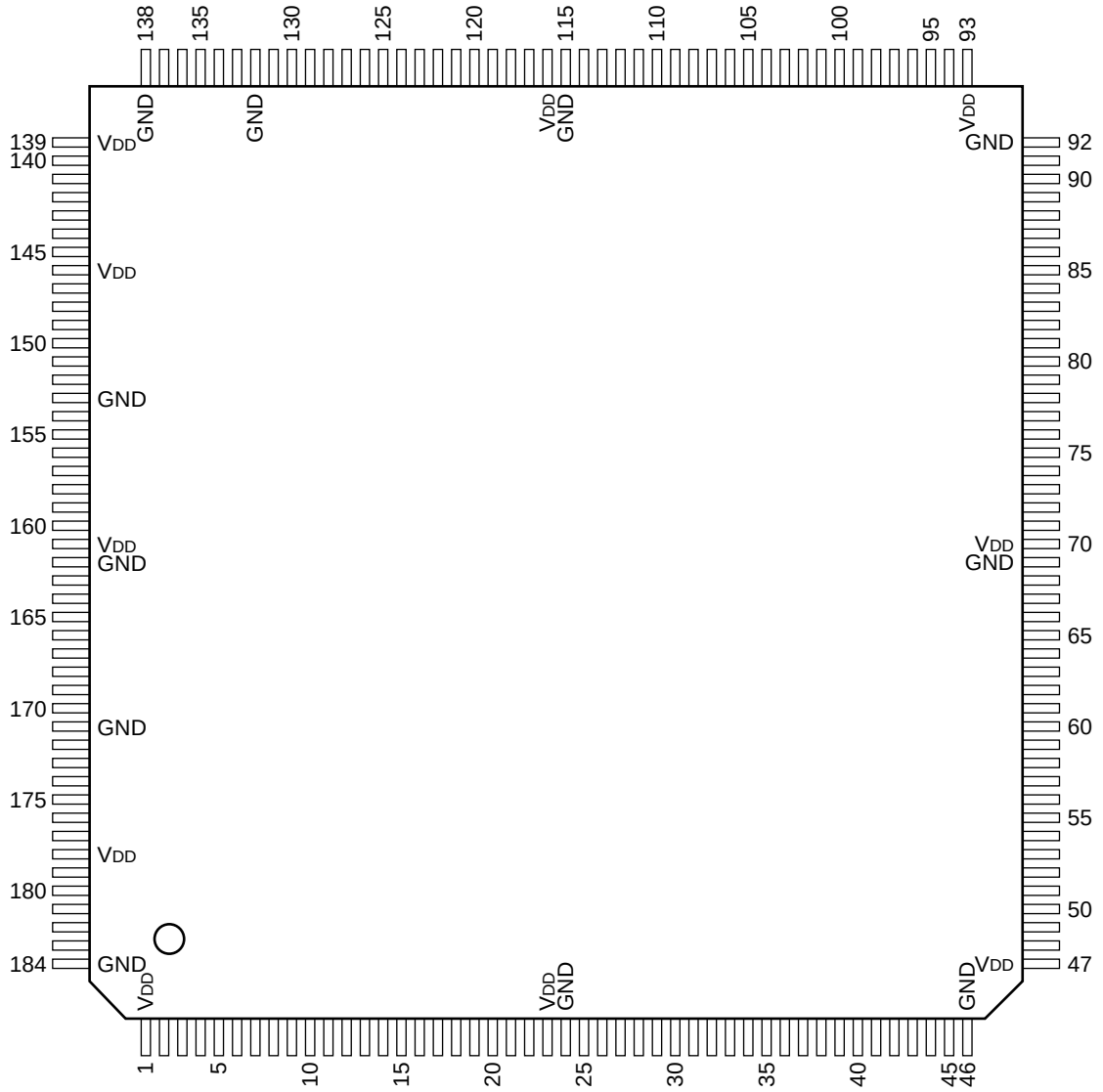


C-MOS MIX/EFFECT

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	47	—	V _{DD}	93	—	V _{DD}	139	—	V _{DD}
2	I	TRNS1 5	48	I	KYSR2 7	94	I	BGB 5	140	O	PRK 8
3	I	TRNS1 6	49	I	KYSR2 8	95	I	BGB 6	141	O	PRK 9
4	I	TRNS1 7	50	I	KYSR2 9	96	I	BGB 7	142	O	PRK 10
5	I	TRNS1 8	51	I	KYSR2 10	97	I	BGB 8	143	O	PRK 11
6	I	TRNS1 9	52	I	KYSR2 11	98	I	BGB 9	144	O	PGM 0
7	I	TRNS1 10	53	I	KYSR2 12	99	I	BGB 10	145	O	PGM 1
8	I	TRNS1 11	54	I/O	SAMP	100	I	BGB 11	146	—	V _{DD}
9	I	TRNS1 12	55	I/O	CKD	101	I	KF1 0	147	O	PGM 2
10	I	TRNS2 0	56	I/O	$\overline{\text{CS}}$	102	I	KF1 1	148	O	PGM 3
11	I	TRNS2 1	57	I/O	DIO	103	I	KF1 2	149	O	PGM 4
12	I	TRNS2 2	58	I/O	CKX	104	I	KF1 3	150	O	PGM 5
13	I	TRNS2 3	59	I/O	ADR	105	I	KF1 4	151	O	PGM 6
14	I	TRNS2 4	60	I	RST	106	I	KF1 5	152	O	PGM 7
15	I	TRNS2 5	61	I	BDR 0	107	I	KF1 6	153	—	GND
16	I	TRNS2 6	62	I	BDR 1	108	I	KF1 7	154	O	PGM 8
17	I	TRNS2 7	63	I	BDR 2	109	I	KF1 8	155	O	PGM 9
18	I	TRNS2 8	64	I	BDR 3	110	I	KF1 9	156	O	PGM 10
19	I	TRNS2 9	65	I	BDR 4	111	I	KF1 10	157	O	PGM 11
20	I	TRNS2 10	66	I	BDR 5	112	I	KF1 11	158	O	NAMO
21	I	TRNS2 11	67	I	BDR 6	113	I	KF2 0	159	O	BLANKO
22	I	TRNS2 12	68	I	BDR 7	114	I	KF2 1	160		TNEN
23	—	V _{DD}	69	—	GND	115	—	GND	161	—	V _{DD}
24	—	GND	70	—	V _{DD}	116	—	V _{DD}	162	—	GND
25	I	CLK	71	I	BDR 8	117	I	KF2 2	163	I	BLANK
26	I	KYSR1 0	72	I	BDR 9	118	I	KF2 3	164	I	HD
27	I	KYSR1 1	73	I	BDR 10	119	I	KF2 4	165	O	PVW 0
28	I	KYSR1 2	74	I	BDR 11	120	I	KF2 5	166	O	PVW 1
29	I	KYSR1 3	75	I	BGA 0	121	I	KF2 6	167	O	PVW 2
30	I	KYSR1 4	76	I	BGA 1	122	I	KF2 7	168	O	PVW 3
31	I	KYSR1 5	77	I	BGA 2	123	I	KF2 8	169	O	PVW 4
32	I	KYSR1 6	78	I	BGA 3	124	I	KF2 9	170	O	PVW 5
33	I	KYSR1 7	79	I	BGA 4	125	I	KF2 10	171	—	GND
34	I	KYSR1 8	80	I	BGA 5	126	I	KF2 11	172	O	PVW 6
35	I	KYSR1 9	81	I	BGA 6	127	I	NAMI	173	O	PVW 7
36	I	KYSR1 10	82	I	BGA 7	128	O	PRK 0	174	O	PVW 8
37	I	KYSR1 11	83	I	BGA 8	129	O	PRK 1	175	O	PVW 9
38	I	KYSR1 12	84	I	BGA 9	130	O	PRK 2	176	O	PVW 10
39	I	KYSR2 0	85	I	BGA 10	131	O	PRK 3	177	O	PVW 11
40	I	KYSR2 1	86	I	BGA 11	132	—	GND	178	—	V _{DD}
41	I	KYSR2 2	87	I	BGB 0	133	O	PRK 4	179	I	TRNS1 0
42	I	KYSR2 3	88	I	BGB 1	134	O	PRK 5	180	I	TRNS1 1
43	I	KYSR2 4	89	I	BGB 2	135	O	PRK 6	181	I	TRNS1 2
44	I	KYSR2 5	90	I	BGB 3	136	O	PRK 7	182	I	TRNS1 3
45	I	KYSR2 6	91	I	BGB 4	137	O	TSTOUT	183	I	TRNS1 4
46	—	GND	92	—	GND	138	—	GND	184	—	GND

INPUT

BGA 0 - BGA 11	: BACKGROUND A VIDEO
BGB 0 - BGB 11	: BACKGROUND B VIDEO
BDR 0 - BDR 11	: WIPE BORDER FILL VIDEO
BLANK	: BLANKING CONTROL
CLK	: CLOCK
HD	: HORIZONTAL SYNC
KF1 0 - KF1 11	: KEY 1 FILL VIDEO
KF2 0 - KF2 11	: KEY 2 FILL VIDEO
KYSR1 0 - KYSR1 12	: KEY 1 SOURCE RANGE
KYSR2 0 - KYSR2 12	: KEY 2 SOURCE RANGE
NAMI	: NAM CONTROL
RST	: RESET
TRNS1 0 - TRNS1 12	: OUTER WIPE TRANSITION RANGE
TRNS2 0 - TRNS2 12	: INNER WIPE TRANSITION RANGE

OUTPUT

BLANKO	: DELAYED BLANKING CONTROL
NAMO	: NAM CONTROL
PGM 0 - PGM 11	: PROGRAM VIDEO
PRK 0 - PRK 11	: PROCESSED KEY
PVW - PVW 11	: PREVIEW VIDEO
TSTOUT	: (FOR TEST)

INPUT/OUTPUT

ADR	: ADDRESS
CKD	: SIF CLOCK
CKX	: SIF REGISTER UPDATE PULSE
$\overline{\text{CS}}$	: SIF CHIP SELECT
DIO	: DATA
SAMP	: OUTPUT CAPTURE TO SERIAL INTERFACE